

# SANYO Semiconductors **DATA SHEET**

# LA9450CL—For Laser Diode Pulse Driver IC

#### Overview

The LA9450CL is a pulse driver IC for laser diode that enables low voltage operation.

#### **Features**

- Two-power voltage design for low power consumption. Two-mode switching function of DC (supplied from V<sub>CC</sub>1: 2.4V) and pulse luminescence (supplied from V<sub>CC</sub>2: 2.8V).
- Low voltage (V<sub>CC</sub>1=2.0V min, V<sub>CC</sub>2=2.6V min) and low current consumption (I<sub>CC</sub>1=500µA) design.
- Low saturation PNP driver is used for DC mode for the low VCEsat.
- Small package ECSP3020-10 (size 3×2mm, pin pitch 0.65mm)

#### **Function**

- · Laser driver
- Two-mode switching functions of DC and pulse luminescence

#### **Specifications**

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		4.5	٧
Allowable power dissipation	Pd max	For every 1°C rise in temperature over 25°C, the power is reduced by a factor of 1.55mW/°C	150	mW
Operating temperature	Topr		-10 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

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# **LA9450CL**

#### Operating Condition at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommend supply voltage	V <sub>CC</sub> 1		2.4	V
	V <sub>CC</sub> 2		2.8	V
Operating supply voltage range	V <sub>CC</sub> 1 opg		2.0 to 3.5	V
	V <sub>CC</sub> 2 opg		2.6 to 3.5	V

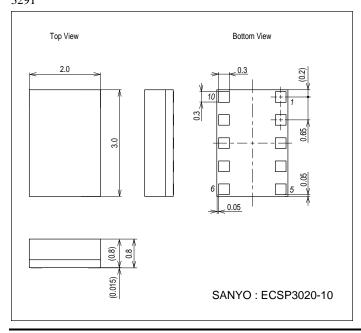
## Electrical Characteristics at $Ta=25^{\circ}C,\ V_{CC}1=2.4V,\ V_{CC}2=2.8V,\ R_{L}=25\Omega$

Parameter	Symbol	Conditions		Ratings			:
Parameter	Symbol			min	typ	max	unit
Supply current 1	I <sub>CC</sub> 11	I <sub>IN</sub> =0μA, Vcont=V <sub>CC</sub> 2	V <sub>CC</sub> 1	300	500	1500	μΑ
DC mode	I <sub>CC</sub> 12	VSW=0V, R <sub>L</sub> =∞	V <sub>CC</sub> <sup>2</sup>		0.1	5	μΑ
Supply current 2	I <sub>CC</sub> 21	I <sub>IN</sub> =0μA, Vcont=V <sub>CC</sub> 2	V <sub>CC</sub> 1	300	500	670	μΑ
Pulse mode	I <sub>CC</sub> 22	VSW=V <sub>CC</sub> 2	V <sub>CC</sub> <sup>2</sup>	70	110	150	μΑ
Supply current 3	I <sub>CC</sub> 31	I <sub>IN</sub> =500μA, Vcont=0V	V <sub>CC</sub> 1	300	530	710	μΑ
Pulse mode	I <sub>CC</sub> 32	VSW=V <sub>CC</sub> 2	V <sub>CC</sub> 2	68	80	93	mA
Output current	IOUT	I <sub>IN</sub> =500μA, Vcont=0V		65	75	85	mA
Current gain	Igain	I <sub>IN</sub> =500μA, Vcont=0V		130	150	170	
Maximum output current Pulse	I <sub>OUT</sub> maxP	I <sub>IN</sub> =1200μA, Vcont=0V, R <sub>L</sub> =10Ω		140	165	210	mA
Maximum output current DC	I <sub>OUT</sub> maxD	I <sub>IN</sub> =1200μA, Vcont=0V, R <sub>L</sub> =10Ω		150	175	210	mA
Maximum output voltage Pulse	V <sub>OUT</sub> maxP	I <sub>IN</sub> =1000μA, Vcont=0V, VSW=V <sub>CC</sub> 2		2.4	2.58		V
Maximum output voltage DC	V <sub>OUT</sub> maxD	I <sub>IN</sub> =1000μA, Vcont=0V, VSW=0V		2.15	2.24		V
Cont high level	Vcont H			V <sub>CC</sub> 2/2		V <sub>CC</sub> 2+0.2	V
Cont low level	Vcont L			-0.2		0.4	V
SW High level	VSW H			V <sub>CC</sub> 1-0.7		V <sub>CC</sub> 2+0.2	V
SW Low level	VSW L			-0.2		0.15	V
I <sub>IN</sub> Input resistance	R <sub>IN</sub>			270	330	390	Ω
* Rising edge time	tr	R <sub>L</sub> =10Ω, I <sub>OUT</sub> peak=40mA, 10→90%			2.9	4.1	ns
* Falling edge time	tf	R <sub>L</sub> =10Ω, I <sub>OUT</sub> peak=40mA, 90→10%			6.1	8.6	ns
*Cont falling edge delay time	Tondelay	I <sub>OUT</sub> peak=55mA, cont 50%→Output 50%			6.8	8.9	ns
*Cont falling edge delay time	Tofdelay	IOUT peak=55mA, cont 50%-	→Output 50%		10.8	14.1	ns

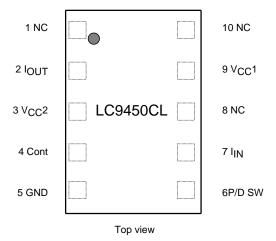
<sup>\*</sup> Design target value and no measurement is performed.

## **Package Dimensions**

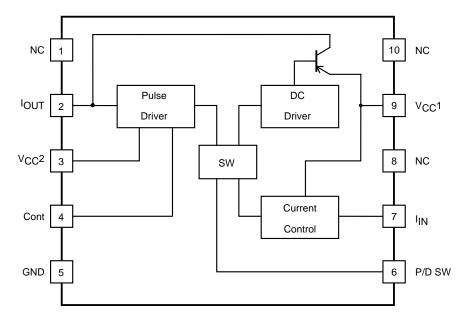
unit : mm (typ) 3291



# **Pin Assignment**



# **Block Diagram**

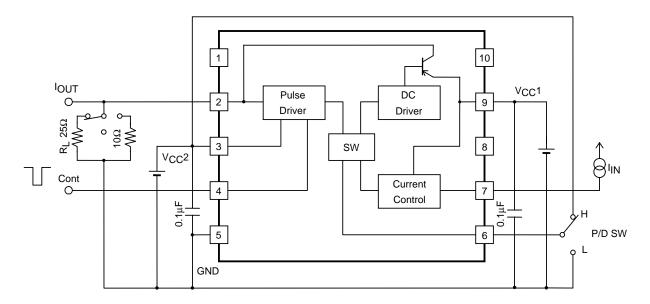


# **LA9450CL**

#### **Pin Functions**

Pin No.	Pin Name	Pin Description	Equivalent Circuit
1	NC	NC NC	
2	lout	This is a LD driver output terminal.	VCC <sup>2</sup> VCC <sup>1</sup>
3	V <sub>CC</sub> <sup>2</sup>	This is a supply terminal for a pulse driver output. In DC luminescence mode, voltage which is bigger than V <sub>CC</sub> 1, and flowing are available.	
4	Cont	"Low" at pulse driver, and lout output is ON.	Vcc²  W  M
5	GND		
6	P/D SW	This is a switching terminal for DC/Pulse. (Low: DC, High: Pulse)	Vcc <sup>2</sup> W
7	lin	This is a controlled current input terminal. (Input resistance $330\Omega$ )	V <sub>CC</sub> 1
			# S00 \$ m
8	NC	NC NC	
8 9	NC VCC1	NC  This is a power supply terminal of a controlled circuit and driver output at DC luminescence. This can be connected to V <sub>CC</sub> 2 to use as a common power supply.	

#### **Test Circuit**



Power supplies of IOUT drive current - Pulse mode:  $V_{CC}2$  DC mode:  $V_{CC}1$ 

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