



GaAs SPDT Switch DC - 2.5 GHz

SW-239 V6

Features

Very Low Power Consumption: 100 μW

• Low Insertion Loss: 0.5 dB

High Isolation: 25 dB up to 2 GHz

Very High Intercept Point: 45 dBm IP3

· Nanosecond Switching Speed

• Temperature Range: -40°C to +85°C

• Low Cost SOIC-8 Plastic Package

• Tape and Reel Packaging Available

Description

M/A-COM's SW-239 is a GaAs MMIC SPDT switch in a low cost SOIC-8 lead surface mount plastic package. The SW-239 is ideally suited for use where low power consumption is required.

Typical applications include transmit/receive switching, switch matrices and switched filter banks in systems such as radio and cellular equipment, PCM, GPS, fiber optic modules, and other battery powered radio equipment.

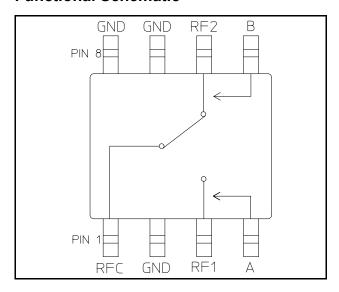
The SW-239 is fabricated using a monolithic GaAs MMIC using a mature 1 micron process. The process features full chip passivation for increased performance and reliability.

Ordering Information

Part Number	Package		
SW-239	Bulk Packaging		
SW-239TR	1000 piece reel		

Note: Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

Pin No.	Function	Pin No.	Function
1	RF Common	5	Control B
2	Ground	6	RF Port 2
3	RF Port 1	7	Ground
4	Control A	8	Ground

Absolute Maximum Ratings ^{1,2}

Parameter	Absolute Maximum		
Input Power 0.05 GHz 0.5-2.0 GHz	+27 dBm +34 dBm		
Control Voltage	-8.5 V ≤ V _C ≤ + 5 V		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

- 1. Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

Visit www.macom.com for additional data sheets and product information.

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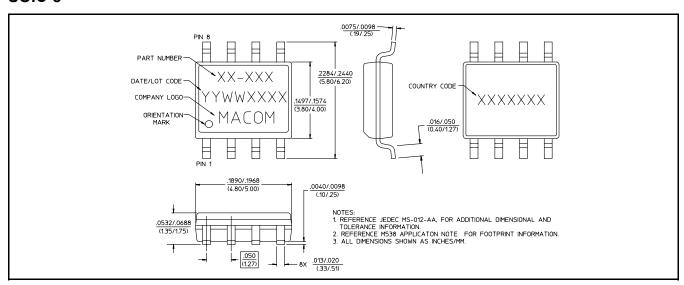
SW-239 V6

Electrical Specifications: $T_A = 25^{\circ}C$, $V_C = 0 \text{ V} / -5 \text{ V}$, $Z_0 = 50 \text{ Ohms}^3$

Parameter	Test Conditions	Units	Min	Тур	Max
Insertion Loss	DC - 0.1 GHz DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz	DC - 0.5 GHz		0.4 0.4 0.5 0.6	 0.8
Isolation	DC - 0.1 GHz DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz	DC - 0.5 GHz		56 43 33 24	_ _ _
VSWR	DC - 2.0 GHz	Ratio	_	1.2:1	_
Trise, Tfall	10% to 90% RF, 90% to 10% RF	nS	_	2	_
Ton, Toff	50% Control to 90% RF, 50% Control to 10% RF	nS	_	4	_
Transients	In-Band	mV	_	15	_
1 dB Compression Point	Input Power, 0.05 GHz Input Power, 0.5 - 2.0 GHz	dBm dBm	_	21 27	_
2nd Order Intercept	Measured Relative to Input Power (for two-tone input power up to +6 dBm) 0.05 GHz 0.5 - 2.0 GHz	dBm dBm	_	55 68	_
3rd Order Intercept	Measured Relative to Input Power (for two-tone input power up to +6 dBm) 0.05 GHz 0.5 - 2.0 GHz	dBm dBm	=	40 45	_
Control Current	V _C = 5 V	μA	_	20	25

^{3.} For positive voltage control, external DC blocking capacitors are required on all RF ports.

SOIC-8



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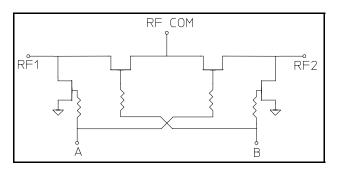




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SW-239

Electrical Schematic



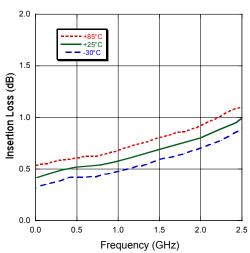
Truth Table ⁴

Control Inputs		Condition of Switch RF Common to Each RF Port		
Α	В	RF1	RF2	
1	0	On	Off	
0	1	Off	On	

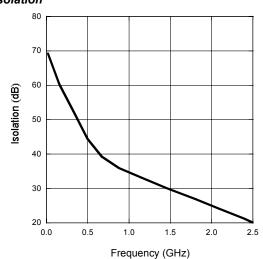
4. 0 = 0 V to -0.2 V, 1 = -5 V to -8 V

Typical Performance Curves

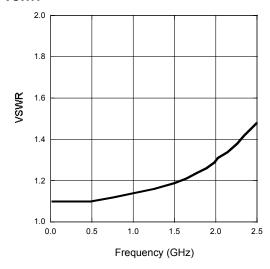
Insertion Loss



Isolation



VSWR



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