

## Crystal Clock Oscillator



## TCO-7116X1V

SMO-N-K VCXO

**Features**

- CMOS logic output
- Ceramic package
- Space saving
- Enable / Disable feature
- Voltage controlled oscillator

**Specifications**

Parameter		TCO-7116X1V	Conditions
Frequency	fo	1.5 to 55 MHz	(*1)
Frequency Stability	Δf/fo	±50 ppm max.	(*2)
Pullability		±100 ppm min.	at Vcont=0.0 to +3.3V Ref=+1.65V
Control Voltage Range	Vcont	+1.65 V ±1.65 V	DC, Lead #1
Operating Temperature	Topr	0°C to +70°C	
Supply Voltage	Vcc	+3.3 V ±5 %	DC, Lead #6
Supply Current	Icc	25 mA max.	Vcc=+3.46V
Input Voltage	V <sub>IH</sub> V <sub>IL</sub>	V <sub>IH</sub> =70% Vcc min. / V <sub>IL</sub> =30% Vcc max.	#2:V <sub>IH</sub> or OPEN ... Enable #2:V <sub>IL</sub> or GND ... Disable
Output Voltage	V <sub>OH</sub> V <sub>OL</sub>	V <sub>OH</sub> =90% Vcc min. / V <sub>OL</sub> =10% Vcc max.	I <sub>OH</sub> =-0.8mA, I <sub>OL</sub> =+3.2mA, Lead #4
Symmetry	SYM	40 to 60 %	CMOS logic output at 50 % Vcc
Rise/Fall time	tr/tf	10 ns max.	CMOS logic output at 20 to 80% Vcc
Load Capacitance	CL	15 pF max.	CMOS logic output
Start-up time	tst	5 ms max.	(*3)

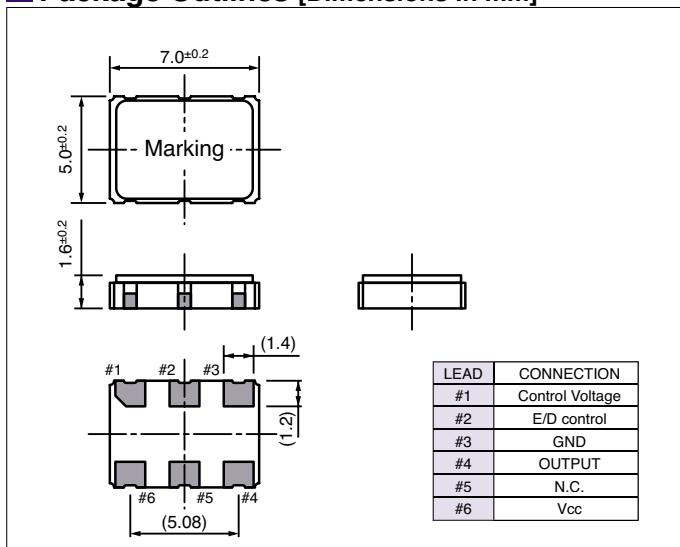
\*1 Please contact us for standard frequency.

\*2 Inclusive of calibration tolerance at +25°C, operating temperature, operating voltage range.

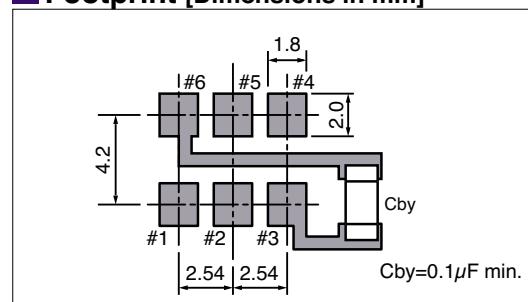
\*3 Rise time (0 to +3.0V) of Vcc &gt;150μs

**Absolute Maximum Ratings**

Parameter	Symbol	Rating
Supply voltage	V <sub>cc</sub>	-0.5 to +6.0 V
Input voltage	V <sub>IN</sub>	-0.5 to V <sub>cc</sub> +0.5 V
Output voltage	V <sub>O</sub>	-0.5 to V <sub>cc</sub> +0.5 V
Output current	I <sub>O</sub>	±10 mA
Storage temperature	T <sub>stg</sub>	-55 to +125°C
Soldering condition	T <sub>sol</sub> T	+260°C / 20sec or +230°C / 180sec

**Package Outlines [Dimensions in mm]****Test Circuit**

See Test Circuit page TEST-8

**Footprint [Dimensions in mm]**

(2003. 3.)