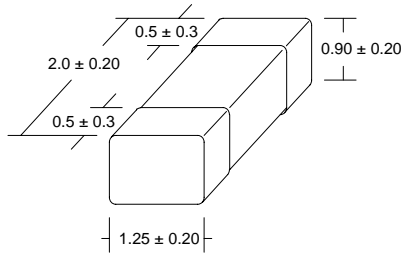


Surface Mount Multilayer Chip Inductor (LSML-201209 Series)

Dimensions



(Not to scale)

Dimensions in mm

Part Numbering Guide

LSML - 201209 - 47N M - T

Dimensions
(Length, Width, Height)

Inductance Code

Packaging Style

B=Bulk
T=Tape & Reel
(4000 pcs per reel)

Tolerance

K=10%, M=20%

Features

Inductance Range	0.047 μ H to 2.2 μ H
Tolerance	10%, 20%
Operating Temperature	-25°C to +85°C

Electrical Specifications

Inductance Code	Inductance (μ H)	Available Tolerance	Q Min	LQ Test Freq (MHz)	SRF Min (MHz)	DCR Max (Ohms)	IDC Max (mA)
47N	0.047	M	30	50	320	0.20	300
68N	0.068	M	30	50	280	0.20	300
82N	0.082	M	30	50	255	0.20	300
R10	0.10	K, M	50	25	235	0.30	250
R12	0.12	K, M	50	25	220	0.30	250
R15	0.15	K, M	50	25	200	0.40	250
R18	0.18	K, M	50	25	185	0.40	250
R22	0.22	K, M	50	25	170	0.50	250
R27	0.27	K, M	50	25	150	0.50	250
R33	0.33	K, M	50	25	145	0.55	250
R39	0.39	K, M	50	25	135	0.65	200
R47	0.47	K, M	50	25	125	0.65	200
R56	0.56	K, M	50	25	115	0.75	150
R68	0.68	K, M	50	25	105	0.80	150
R82	0.82	K, M	50	25	100	1.00	150
1R0	1.0	K, M	40	10	75	0.40	50
1R2	1.2	K, M	40	10	65	0.50	50
1R5	1.5	K, M	40	10	60	0.50	50
1R8	1.8	K, M	40	10	55	0.60	50
2R2	2.2	K, M	40	10	50	0.65	30

Specifications subject to change without notice. Rev. 10/00

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