EPCOS

SAW Components

Data Sheet B4067

Data Sheet

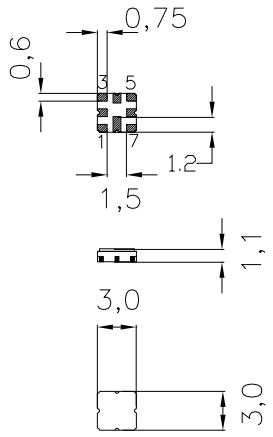
SMD ceramic package **QCC8D**

Features

- Low loss IF filter for HiperLAN
- Balanced to balanced operation
- Package for **S**urface **M**ounted **T**echnology **(SMT)**

Terminals

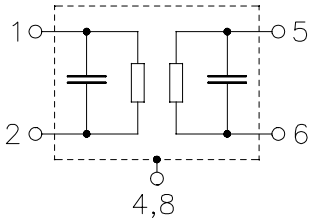
- Ni, gold-plated



Dimensions in mm, approx. weight 0,037 g

Pin configuration

1	Input
2	Input
5	Output
6	Output
3, 7	To be grounded
4, 8	Case - ground



Type	Ordering code	Marking and Package according to	Packing according to
B4067	B39811-B4067-U810	C61157-A7-A72	F61074-V8101-Z000

Electrostatic **S**ensitive **D**evice (**ESD**)

Maximum ratings

Operable temperature range	T	– 40/+ 85	˚C	
Storage temperature range	T_{stg}	– 40/+ 85	˚C	
DC voltage	V_{DC}	0	V	
Source power	P_s	0	dBm	source impedance 200 Ω

Data Sheet

Characteristics

Operating temperature range: T_A = 0 ... +70 °C
Terminating source impedance: Z_S = 200 Ω
Terminating load impedance: Z_L = 200 Ω

		min.	typ.	max.	
Nominal frequency	f_N	—	810,0	—	MHz
Minimum insertion attenuation	α_{min}	—	1,7	4,0	dB
Amplitude ripple in passband (p-p)	$\Delta\alpha$				
$f_N \pm 8{,}0$ MHz		—	0,6	1,0	dB
$f_N \pm 8{,}5$ MHz		—	0,7	1,2	dB
Group delay ripple (p-p)	$\Delta\tau$				
$f_N \pm 8{,}5$ MHz		—	25	75	ns
Relative attenuation (relative to α_{min})	α_{rel}				
$f_N - 20{,}0$ MHz		15,5	36	—	dB
$f_N + 20{,}0$ MHz		15,5	24	—	dB
$f_N - 40{,}0$ MHz		23	54	—	dB
$f_N + 40{,}0$ MHz		23	48	—	dB
$f_N - 500$ MHz ... $f_N - 50{,}0$ MHz		45	54	—	dB
$f_N + 50{,}0$ MHz ... $f_N + 500$ MHz		45	58	—	dB
Reflected wave signal suppression					
450 ns after main pulse		46,0	48,0	—	dB

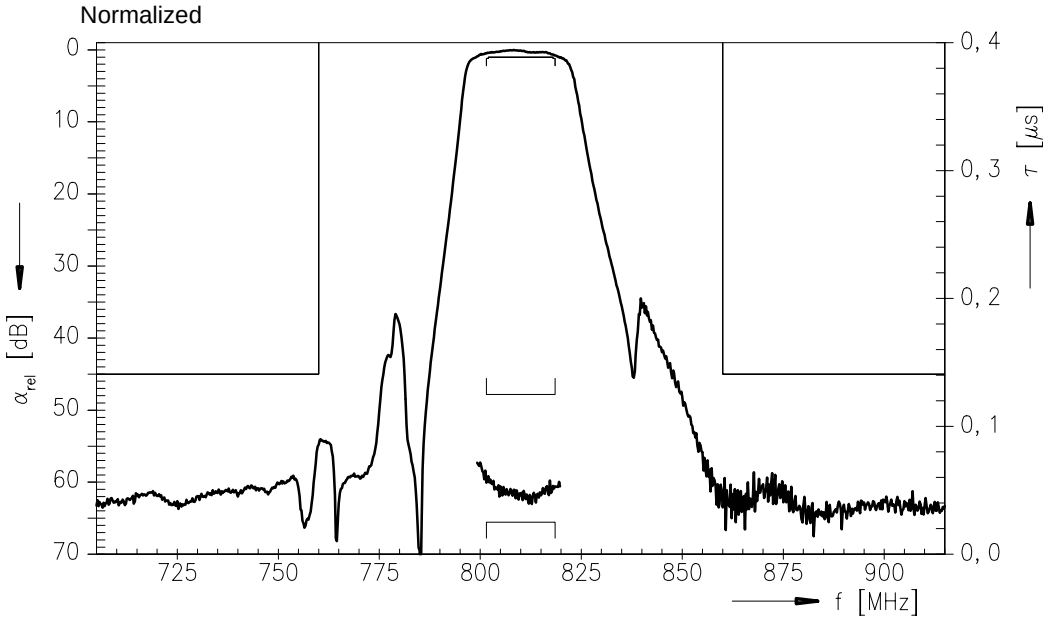
Characteristics (2 filters cascaded)

Operating temperature range: T_A = 0 ... +70 °C
Terminating source impedance: Z_S = 200 Ω
Terminating load impedance: Z_L = 200 Ω

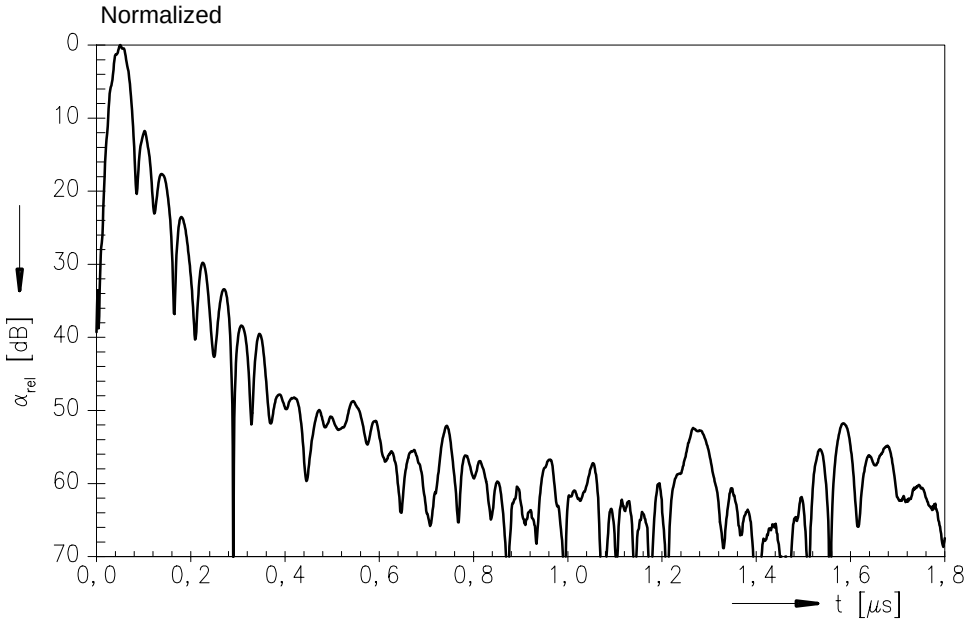
		min.	typ.	max.	
Nominal frequency	f_N	—	810,0	—	MHz
Minimum insertion attenuation	α_{min}	—	3,4	8,0	dB
Amplitude ripple in passband (p-p)	$\Delta\alpha$				
$f_N \pm 8{,}0$ MHz		—	1,2	2,0	dB
$f_N \pm 8{,}5$ MHz		—	1,8	2,4	dB
Group delay ripple (p-p)	$\Delta\tau$				
$f_N \pm 8{,}5$ MHz		—	50	150	ns
Relative attenuation (relative to α_{min})	α_{rel}				
$f_N - 20{,}0$ MHz		31	60	—	dB
$f_N + 20{,}0$ MHz		31	48	—	dB
$f_N - 40{,}0$ MHz		46	108 *)	—	dB
$f_N + 40{,}0$ MHz		46	96 *)	—	dB
$f_N - 500$ MHz ... $f_N - 50{,}0$ MHz		90	108 *)	—	dB
$f_N + 50{,}0$ MHz ... $f_N + 500$ MHz		90	116 *)	—	dB
Reflected wave signal suppression					
900 ns after main pulse		46,0	48,0	—	dB

*) value depends on pcb layout

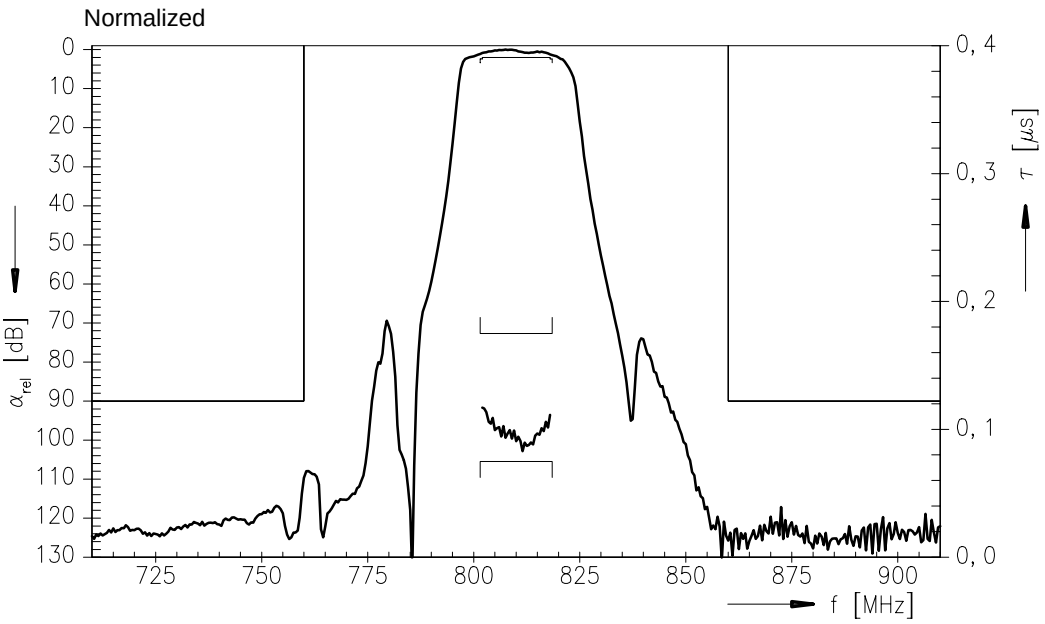
Transfer function (single filter)



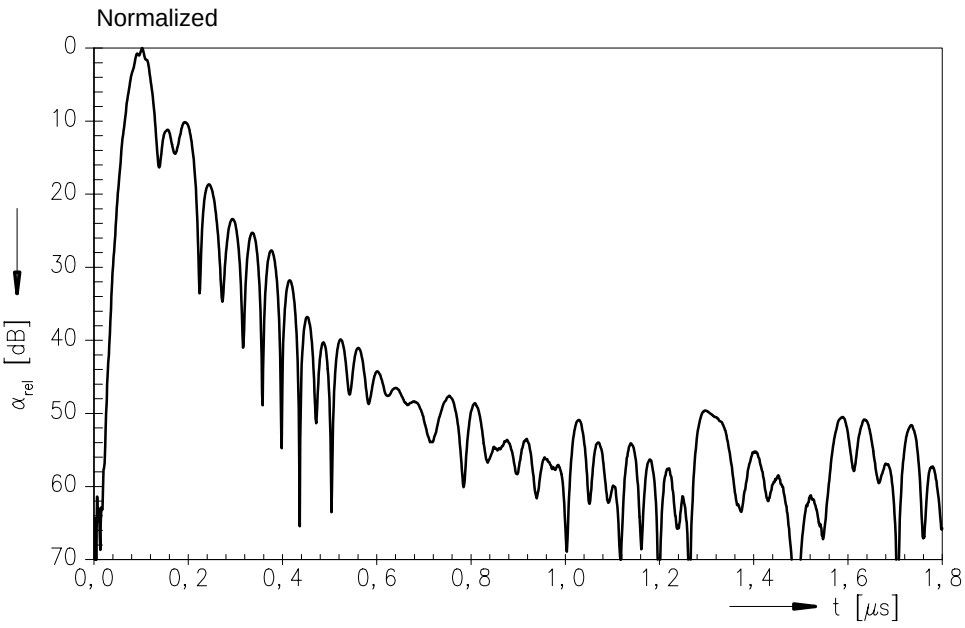
Impulse response (single filter)



Transfer function (2 cascaded filters)



Impulse response (2 cascaded filters)



Published by EPCOS AG
Surface Acoustic Wave Components Division, SAW MC WT
P.O. Box 80 17 09, D-81617 München

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