

SAW Components

Data Sheet B3855





SAW Components B3855
Low Loss Filter 169,00 MHz

Data Sheet

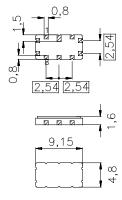
Ceramic package QCC10B

Features

- IF filter for WCDMA
- Low insertion loss
- Ceramic SMD package

Terminals

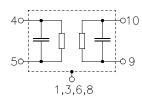
Gold plated



Dimensions in mm, appr. weight 0,23 g

Pin configuration

9, 10	Balanced Input
4, 5	Balanced Output
1, 3, 6, 8	Case ground
2, 7	To be grounded



Туре	Ordering code	Marking and Package according to	Packing according to
B3855	B39171-B3855-Z710	C61157-A7-A49	F61074-V8172-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T_{A}	-40 / +85	°C
Storage temperature range	$T_{\rm stq}$	-40 / +85	°C
DC voltage	$V_{\rm DC}$	0	V
Source power	P_{s}	10	dBm



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Characteristics

Operating temperature:

 $T_{\rm A} = -40 \dots +85 \,^{\circ}{\rm C}$ $Z_{\rm S} = 200 \,\Omega$ and matching network $Z_{\rm L} = 200 \,\Omega$ and matching network Terminating source impedance: Terminating load impedance:

150 kHz Group delay aperture:

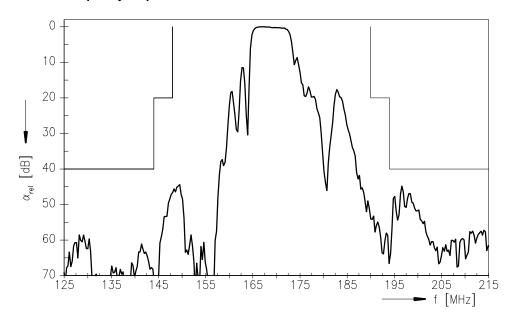
		min.	typ.	max.	
Nominal frequency	f_{N}	_	169,00	_	MHz
Minimum insertion attenuation		1,5	2,0	3,5	dB
(including matching network)					
Passband width					
$\alpha_{rel} \leq$	1 dB B_{1dB}	_	7,5	_	MHz
Amplitude ripple (p-p)	$\Delta \alpha$				
$f_{N} \pm 2$,0 MHz	_	0,2	0,5	dB
Group delay ripple (p-p)	Δau				
$f_{N} \pm 2$,0 MHz	_	40	80	ns
Absolute group delay	τ				
mean value within $f_{ m N}\pm 2$,0 MHz		127	130	137	ns
Relative attenuation (relative to α_{min})) α_{rel}				
10 MHz 144 MHz		40	50	_	dB
144 MHz 148 MHz		20	40	_	dB
190 MHz 194 MHz		20	50	_	dB
194 MHz 2,0 GHz		40	45	_	dB
2,0 GHz 2,5 GHz		35	40	_	dB
VSWR		_	2,0:1	2,5:1	
Impedance at f _N (without matching)					
Input: $Z_{IN} = R_{IN} \parallel C_{IN}$		_	690 1,3	_	Ω pF
Output: Z _{OUT} = R _{OUT} C _{OUT}	г	_	580 1,1	_	Ω pF
Temperature coefficient of frequence	cy TC _f	_	- 70	_	ppm/K



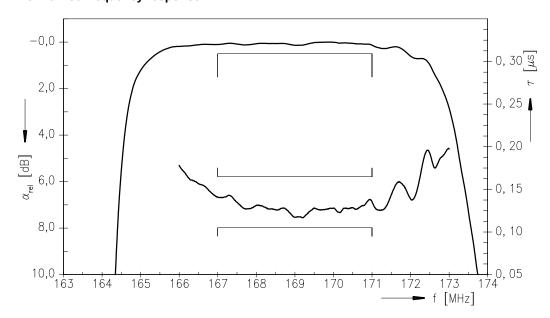
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Normalized frequency response



Normalized frequency response



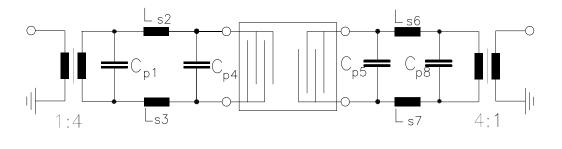


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Matching network

(Element values depend upon PCB layout)



C _{P1} =3,9 pF	C _{P5} =1,0 pF
L _{S2} =150 nH	L _{S6} =150 nH
L _{S3} =150 nH	L _{S7} =180 nH
C _{P4} =1,5 pF	C _{P8} =3,3 pF

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