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MS2210

RF AND MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

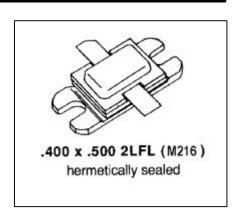
Features

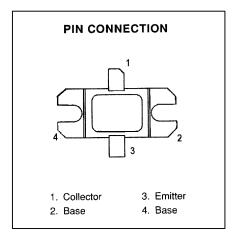
- 255 MHz BANDWIDTH
- GOLD METALLIZATION
- EMITTER SITE BALLASTED
- P_{OUT} = 300W MINIMUM
- $G_P = 7.0 \text{ dB}$
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- 15:1 VSWR CAPABILITY



The MS2210 avionics power transistor is a broadband, high peak pulse power device specifically designed for avionics applications requiring broad bandwidth with moderate duty cycle and pulse width constraints such as ground/ship DME/TACAN.

The MS2210 is also designed for specialized applications where reduced power is provided under pulse formats utilizing short pulse widths and high burst or overall duty cycles. This device is capable of withstanding 15:1 VSWR mismatch load conditions at any phase angle under full rated conditions.





ABSOLUTE MAXIMUM RATINGS ($T_{CASE} = 25^{\circ}C$)

Symbol	Parameter	Value	Unit
P _{DISS}	Power Dissipation*	940	W
I C	Device Current*	24	Α
V _{cc}	Collector-Supply Voltage*	50	
T _J	Junction Temperature (RF Pulsed Operation)	+200	°C
T _{STG}	Storage Temperature	- 65 to + 200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	0.16	°C/W			

^{*}Applies only to rated RF amplifier operation



MS2210

ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

Symbol	Test Conditions		Value	Unit		
Syllibol	rest conditions		Min.	Тур.	Max.	Offic
BV _{CBO}	I _C = 50 mA	I _E = 0 mA	65			V
BV _{EBO}	I _E = 15 mA	$I_C = 0 \text{ mA}$	3.0			V
BV _{CER}	I _C = 50 mA	$R_{BE} = 10 \Omega$	65			V
I _{CES}	V _{CE} = 50 V				30	mA
h _{FE}	V _{CE} = 5 V	I _C = 5A	10			

DYNAMIC

Symbol	Test Conditions		Value		
Symbol			Тур.	Max.	Unit
Pout	$f = 960 - 1215 \text{ MHz}$ $P_{IN} = 60 \text{ W}$ $V_{CC} = 50 \text{ V}$	300	330		W
ης	$f = 960 - 1215 \text{ MHz}$ $P_{IN} = 60 \text{ W}$ $V_{CC} = 50 \text{ V}$	38	45		%
G _P	f = 960 - 1215 MHz P _{IN} = 60 W V _{CC} = 50 V	7.0	7.4		Db

Note: Pulse Format: 10 μ S

Duty Cycle: 10%

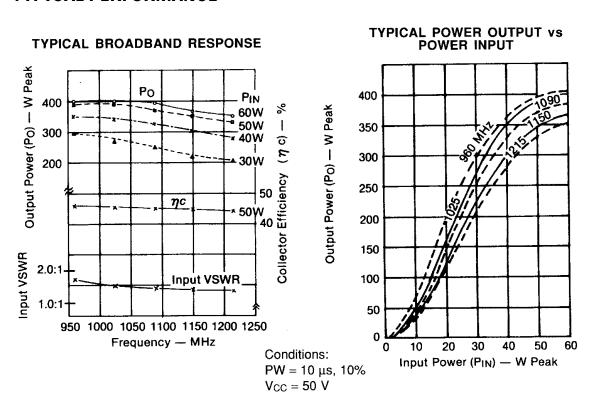
IMPEDANCE DATA

FREQ	$Z_IN(\Omega)$	$Z_{CL}(\Omega)$
960 MHz	2.0 + j3.6	1.7 - j2.2
1090 MHz	3.5 + j1.7	2.0 - j1.7
1215 MHz	1.6 + j0.5	1.8 - j2.0

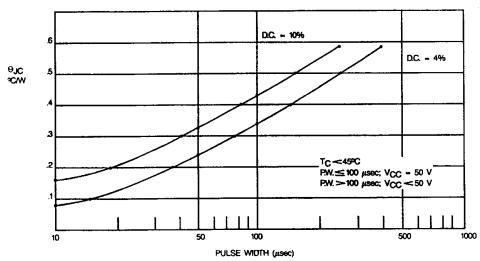




TYPICAL PERFORMANCE



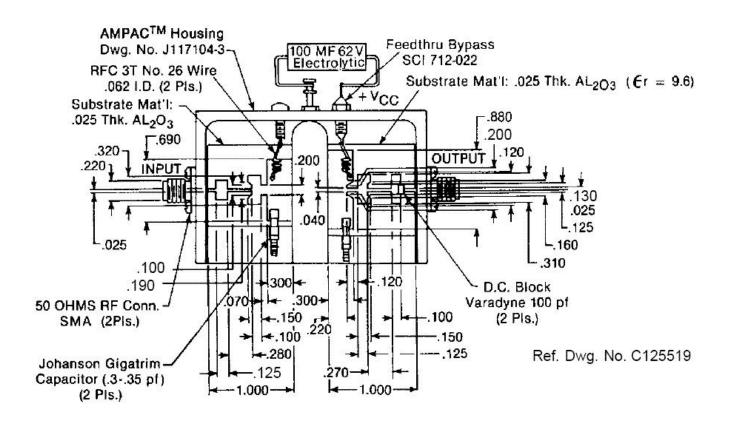
MAXIMUM THERMAL RESISTANCE vs PULSE WIDTH & DUTY CYCLE







TEST CIRCUIT

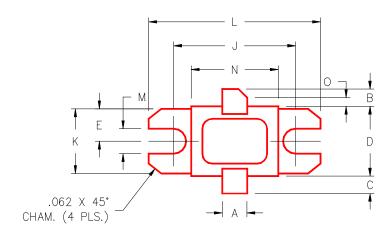


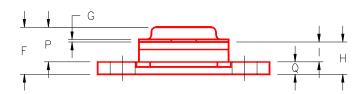




PACKAGE MECHANICAL DATA

PACKAGE STYLE M216





	MINIMUM	MAXIMUM		MINIMUM	MAXIMUM
	INCHES/MM	INCHES/MM		INCHES/MM	INCHES/MM
Α	.140/3,56		J	.700/	17,78
В	.110/2,80		K	.386/9,80	
С	.110/	[′] 2,80	L	.900/22.86	
D	.395/10,03	.407/10,34	М	.120/3,05	
Е	.193/	/4,90	N	.500/12,70	
F		.230/5,84	0	.050/1,27	
G	.003/0,08	.006/0,15	Р		.170/4,32
Н	.118/3,00	.131/3,33	Q	.062/1,58	
	.063,	/1,60			