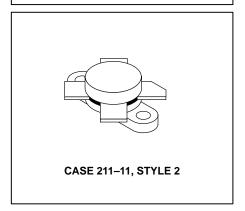
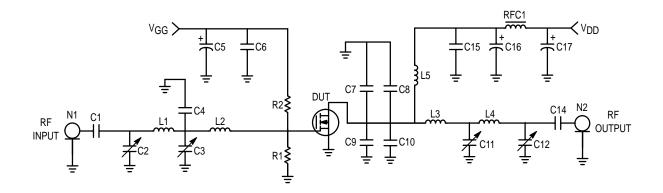
## The RF MOSFET Line RF Power Field-Effect Transistor N-Channel Enhancement-Mode

## MRF255 PHOTOMASTER





C1 — 470 pF, Chip Capacitor
C2, C3, C11, C12 — 20–200 pF, Trimmer, ARCO #464
C4 — 100 pF, Chip Capacitor
C5, C17 — 100  $\mu$ F, 15 V, Electrolytic
C6 — 0.001  $\mu$ F, Disc Ceramic
C7, C8, C9, C10 — 330 pF, Chip Capacitor
C14 — 1200 pF, ATC Chip Capacitor

C7, C8, C9, C10 — 330 pF, Chip Capacit C14 — 1200 pF, ATC Chip Capacitor C15 — 910 pF, 500 V, Dipped Mica C16 — 47 μF, 16 V, Electrolytic L1 — 8 Turns, #20 AWG, 0.126" ID

L2 - 5 Turns, #18 AWG, 0.142" ID

L3 — 3 Turns, #20 AWG, 0.102" ID

L4 — 7 Turns, #24 AWG, 0.070" ID

L5 — 6.5 Turns, #18 AWG, 0.230" ID, 0.5" Long

N1, N2 — Type N Flange Mount

RFC1 — Ferroxcube VK-200-19/4B

 $R1 - 39 k\Omega$ , 1/4 W Carbon

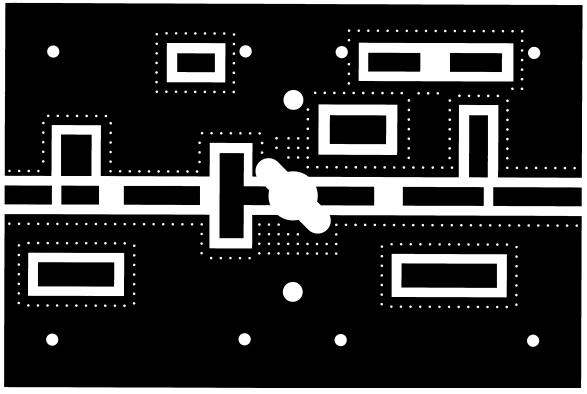
R2 — 150  $\Omega$ , 1/4 W Carbon

Board - G-10 .060"

Figure 1. 54 MHz Linear RF Test Circuit Electrical Schematic

**Handling and Packaging** — MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.





(SCALE: 1:1)

Figure 2. Photomaster for 54 MHz Narrowband Test Fixture

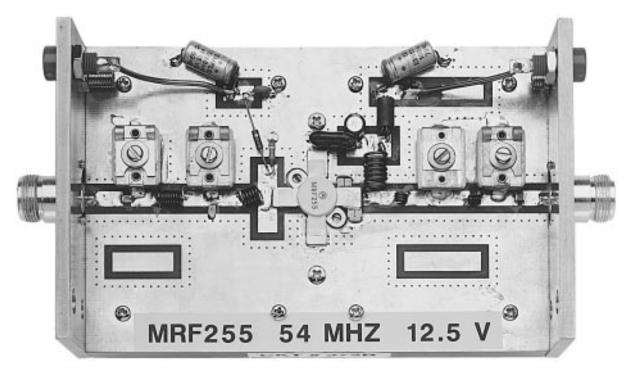
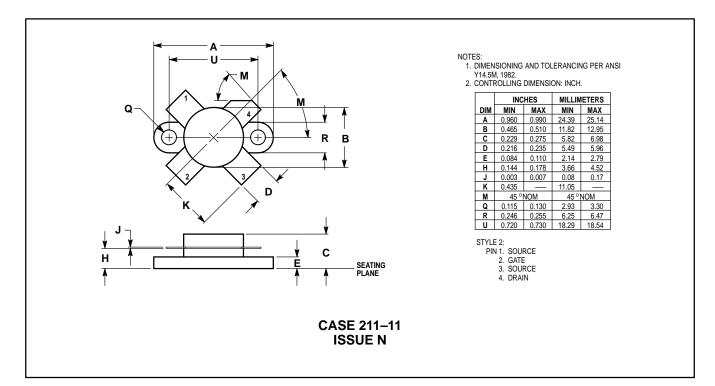


Figure 3. Test Fixture Photograph — MRF255

## **PACKAGE DIMENSIONS**



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