# HMIC $^{\text {™ }}$ Silicon PIN Diode Switches 

## Features

- Broad Bandwidth
- Specified from 50 MHz to 20 GHz
- Usable from 50 MHz to 26.5 GHz
- Lower Insertion Loss and Higher Isolation than Comparable pHempt Designs
- Rugged Fully Monolithic, Glass Encapsulated Chip with Polymer Protection Coating
- Up to +30dBm C.W. Power Handling @ +25 ${ }^{\circ} \mathrm{C}$


## Description

The MA4SW110, MA4SW210 and MA4SW310 are broadband monolithic switches using series and shunt connected silicon PIN diodes. They are designed for use as moderate signal, high performance switches in applications up to 26.5 GHz . They provide performance levels superior to those realized by hybrid MIC designs incorporating beam lead and PIN chip diodes that require chip and wire assembly.

These switches are fabricated using M/A-COM's patented HMIC ${ }^{\text {TM }}$ (Heterolithic Microwave Integrated Circuit) process, US Patent $5,268,310$. This process allows the incorporation of silicon pedestals that form series and shunt diodes or vias by imbedding them in low loss, low dispersion glass. By using small spacing between elements, this combination of silicon and glass gives HMIC devices low loss and high isolation performance through low millimeter frequencies.

Large bond pads facilitate the use of low inductance ribbon leads, while gold backside metallization allows for manual or automatic chip bonding via 80/20, AuSn solder or conductive Ag epoxy.
Absolute Maximum Ratings $@ \mathbf{T}_{\mathbf{A}}+\mathbf{+ 2 5} \mathbf{}{ }^{\circ} \mathbf{C}$

| Parameter | Absolute Maximum |
| :--- | :---: |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Junction Temperature | $+175^{\circ} \mathrm{C}$ |
| Applied Reverse Voltage | -50 V |
| RF C.W. Incident Power | +30 dBm C.W. |
| Bias Current $+25^{\circ} \mathrm{C}$ | $\pm 20 \mathrm{~mA}$ |

Max. operating conditions for a combination of RF power, D.C. bias and temperature:
+30 dBm CW @ 15mA (per diode) @+85ㅇ

MA4SW110


MA4SW210


MA4SW310


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HMIC $^{\text {TM }}$ Silicon PIN Diode Switches
RoHS Compliant
MA4SW110 (SPST)
Electrical Specifications @ $\mathrm{T}_{\mathrm{A}}=\mathbf{+ 2 5 ^ { \circ }} \mathrm{C}, \mathbf{2 0 m A}$

| Parameter | Frequency | Minimum | Nominal | Maximum | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 6 GHz | - | 0.4 | 0.7 | dB |
| Insertion Loss | 13 GHz | - | 0.5 | 0.9 | dB |
|  | 20 GHz | - | 0.7 | 1.2 | dB |
|  | 6 GHz | 46 | 55 | - | dB |
| Isolation | 13 GHz | 39 | 47 | - | dB |
|  | 20 GHz | 34 | 42 | - | dB |
|  | 6 GHz | 22 | 31 | - | dB |
| Input Return Loss | 13 GHz | 15 | 33 | - | dB |
| Switching Speed ${ }^{1}$ | 20 GHz | 14 | 27 | - | dB |
| Voltage Rating ${ }^{2}$ | - | - | 20 | - | ns |
| Signal Compression $(500 \mathrm{~mW})$ | 1 GHz | - | - | 50 | V |

1.) Typical Switching Speed measured from $10 \%$ to $90 \%$ of detected RF signal driven by TTL compatible drivers.

MA4SW210 (SPDT)
Electrical Specifications @ $\mathrm{T}_{\mathrm{A}}=+\mathbf{2 5}{ }^{\circ} \mathrm{C}, 20 \mathrm{~mA}$
2.) Maximum reverse leakage current in either the shunt or series PIN diodes shall be $10 \mu \mathrm{~A}$ maximum at -50 volts.

| Parameter | Frequency | Minimum | Nominal | Maximum | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 6 GHz | - | 0.4 | 0.7 | dB |
| Insertion Loss | 13 GHz | - | 0.5 | 1.0 | dB |
|  | 20 GHz | - | 0.7 | 1.2 | dB |
|  | 6 GHz | 48 | 63 | - | dB |
| Isolation | 13 GHz | 40 | 50 | - | dB |
|  | 20 GHz | 34 | 42 | - | dB |
|  | 6 GHz | 20 | 27 | - | dB |
| Input Return Loss | 13 GHz | 18 | 25 | - | dB |
| Switching Speed ${ }^{1}$ | 20 GHz | 15 | 25 | - | dB |
| Voltage Rating ${ }^{2}$ | - | - | 20 | - | ns |
| Signal Compression $(500 \mathrm{~mW})$ | 1 GHz | - | - | 50 | V |

1.) Typical Switching Speed measured from $10 \%$ to $90 \%$ of detected RF signal driven by TTL compatible drivers.
MA4SW310 (SP3T)
Electrical Specifications @ $\mathrm{T}_{\mathrm{A}}=+\mathbf{2 5}{ }^{\circ} \mathrm{C}, 20 \mathrm{~mA}$
2.) Maximum reverse leakage current in either the shunt or series PIN diodes shall be $10 \mu \mathrm{~A}$ maximum at -50 volts.

| Parameter | Frequency | Minimum | Nominal | Maximum | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 6 GHz | - | 0.5 | 0.8 | dB |
| Insertion Loss | 13 GHz | - | 0.7 | 1.1 | dB |
|  | 20 GHz | - | 0.9 | 1.5 | dB |
|  | 6 GHz | 49 | 57 | - | dB |
| Isolation | 13 GHz | 42 | 48 | - | dB |
|  | 20 GHz | 33 | 42 | - | dB |
|  | 6 GHz | 20 | 24 | - | dB |
| Input Return Loss | 13 GHz | 14 | 22 | - | dB |
|  | 20 GHz | 11 | 21 | - | dB |
| Switching Speed ${ }^{1}$ | - | - | 20 | - | ns |
| Voltage Rating ${ }^{2}$ | - | - | - | 50 | V |
| Signal Compression $(500 \mathrm{~mW})$ | 1 GHz | - | 0.2 | - | dB |

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HMIC $^{\text {M }}$ Silicon PIN Diode Switches

Typical Performance Curves at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, 20 \mathrm{~mA}$ Bias Current


MASW110 INSERTION LOSS vs. FREQUENCY


MA4SW210 INSERTION LOSS vs. FREQUENCY


MA4SW310 INSERTION LOSS vs. FREQUENCY


S-Parameters: S-Parameter data is available upon request.

ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed
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Solutions has under development. Performance is based on engineering tests. Specifications are
typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

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HMIC $^{\text {M }}$ Silicon PIN Diode Switches

## Typical Performance Curves @ TA = +25 ${ }^{\circ} \mathrm{C}, 20 \mathrm{~mA}$ Bias Current



> ISOLATION vs. FREQUENCY

ISOLATION vs. FREQUENCY

MA4SW310
ISOLATION vs. FREQUENCY


INPUT RETURN LOSS vs. BIAS CURRENT @ 10 GHz


OUTPUT RETURN LOSS vs. BIAS CURRENT@ 10 GHz


INSERTION LOSS vs. BIAS CURRENT @ 10 GHz


ISOLATION vs. BIAS CURRENT @ 10 GHz


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## HMIC ${ }^{\text {M }}$ Silicon PIN Diode Switches

RoHS Compliant

## Operation of the MA4SW Series Switches

Operation of the MA4SW series of PIN switches is achieved by simultaneous application of a negative DC current to the low loss switching arm J1, J2, or J3, and a positive DC current to the remaining switching arms as shown in the bias connection circuits. DC return is achieved via J1. The control currents should be supplied by constant current sources. The voltages at these points will not exceed $\pm 1.5$ volts ( 1.2 volts typical) at currents up to $\pm 20 \mathrm{~mA}$. In the low loss state, the series diode must be forward biased and the shunt diode reverse biased. In the isolated arm, the shunt diode is forward biased and the series diode is reverse biased.

## Driver Connections <br> MA4SW110

| Control Level <br> DC Current @ | Condition of <br> RF Output |
| :---: | :---: |
| $\mathbf{J 2}$ | J1-J2 |
| -20 mA | Low Loss |
| +20 mA | Isolation |

## MA4SW210

| Control Level <br> DC Current @ |  | Condition of <br> RF Output | Condition of <br> RF Output |
| :---: | :---: | :---: | :---: |
| J2 | J3 | J1-J2 | J1-J3 |
| -20 mA | +20 mA | Low Loss | Isolation |
| +20 mA | -20 mA | Isolation | Low Loss |

MA4SW310

| Control Level <br> DC Current @ |  | Cond. of <br> RF Output | Cond. of <br> RF Output | Cond. of <br> RF Output |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| J2 | J3 | J4 | J1-J2 | J1-J3 | J1-J4 |
| -20 mA | +20 mA | +20 mA | Low Loss | Isolation | Isolation |
| +20 mA | -20 mA | +20 mA | Isolation | Low Loss | Isolation |
| +20 mA | +20 mA | -20 mA | Isolation | Isolation | Low Loss |

## Handling Considerations

Cleanliness: These chips should be handled in a clean environment.
Electro-Static Sensitivity: The MA4SW Series PIN diode switches are ESD, Class 1A sensitive (HBM). Proper ESD precautions should be taken.


Notes:

1. RLC values are for an operation frequency of $2-18 \mathrm{GHz}$ and bias current of $\pm 20 \mathrm{~mA}$ per diode.

## MA4SW110 and Bias Connections ${ }^{1}$



MA4SW210 and Bias Connections ${ }^{1}$


HMIC $^{\text {TM }}$ Silicon PIN Diode Switches
RoHS Compliant

## Wire Bonding

Thermosonic wedge bonding using $0.003^{\prime \prime} \times 0.00025$ " ribbon or 0.001 " diameter gold wire is recommended. A heat stage temperature of $150^{\circ} \mathrm{C}$ and a force of 18 to 22 grams should be used. If ultrasonic energy is necessary, it should be adjusted to the minimum level required to achieve a good bond. RF bond wires should be kept as short as possible.

## Chip Mounting

The HMIC switches have Ti-Pt-Au back metal. They can be die mounted with a gold-tin eutectic solder preform or conductive epoxy. Mounting surface must be clean and flat.

Eutectic Die Attachment: An 80/20, gold-tin, eutectic solder preform is recommended with a work surface temperature of $255^{\circ} \mathrm{C}$ and a tool tip temperature of $265^{\circ} \mathrm{C}$. When hot gas is applied, the temperature at the chip should be $290^{\circ} \mathrm{C}$. The chip should not be exposed to temperatures greater than $320^{\circ} \mathrm{C}$ for more than 20 seconds. No more than three seconds should be required for attachment. Solders rich in tin should not be used.

Epoxy Die Attachment: A minimum amount of epoxy, 1-2 mils thick, should be used to attach chip. A thin epoxy fillet should be visible around the outer perimeter of the chip after placement. Cure epoxy per product instructions. Typically $150^{\circ} \mathrm{C}$ for 1 hour.

## MA4SW110 Chip Outline Drawing ${ }^{1,2}$



| DIM | INCHES |  | MM |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | MAX. | MIN. | MAX. |  |  |
| A | 0.014 | 0.018 | 0.35 | 0.45 |  |  |
| B | 0.025 | 0.029 | 0.64 | 0.74 |  |  |
| C | 0.008 REF |  | 0.20 REF |  |  |  |
| D | 0.004 |  | 0.006 | 0.10 |  | 0.15 |
| E | 0.004 REF |  | 0.10 REF |  |  |  |
| F | 0.003 REF |  | 0.08 REF |  |  |  |
| G | 0.003 REF |  | 0.08 REF |  |  |  |
| H | 0.020 REF |  | 0.52 REF |  |  |  |

Notes:

1. Topside and backside metallization is gold , $2.5 \mu \mathrm{~m}$ thick typical.
2. Yellow areas indicate wire bonding pads

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## MA4SW210 Chip Outline Drawing ${ }^{1,2}$



| DIM | INCHES |  | MM |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN. | MAX. | MIN. | MAX. |
| A | 0.029 | 0.033 | 0.73 | 0.83 |
| B | 0.004 | 0.006 | 0.10 | 0.15 |
| C | 0.004 REF |  | 0.10 REF |  |
| D | 0.005 REF |  | 0.13 REF |  |
| E | 0.009 REF |  | 0.23 REF |  |
| F | 0.023 REF |  | 0.17 REF |  |
| G | 0.007 REF |  | 0.10 REF |  |
| H |  |  |  |  |

## Notes:

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## MA4SW310 Chip Outline Drawing ${ }^{1,2}$



| DIM | INCHES |  | MM |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN. | MAX. | MIN. | MAX. |
| A | 0.046 | 0.050 | 1.16 | 1.26 |
| B | 0.036 | 0.040 | 0.92 | 1.02 |
| C | 0.019 REF |  | 0.48 REF |  |
| D | 0.014 REF |  | 0.36 REF |  |
| E | 0.004 REF |  | 0.10 REF |  |
| F | 0.005 REF |  | 0.13 REF |  |
| G | 0.004 |  | 0.006 | 0.15 |
| H | 0.005 REF |  | 0.12 REF |  |
| J | 0.10 REF |  |  |  |

Notes:

1. Topside and backside metallization is gold , $2.5 \mu \mathrm{~m}$ thick typical.
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