CFS33xx Model 9x14 mm SMD, 3.3V, CMOS



Frequency Range: Frequency Stability: Temperature Range:

(Option M)

(Option E)

1.544~34.00MHz 35.00~50.00MHz 51.00~69.00MHz 70.00~156.25 MHz

Standby Current Output:

Storage:

Input Voltage: Input Current:

> Symmetry: Rise/Fall Time: 1.54~10.00 MHz 10.10~30.00 MHz 30.10~50.00 MHz 50.10~80.00 MHz 80.10~156.25 MHz Logic:

Start-up Time: Load:

Jitter RMS: 12KHz~80MHz

Aging:

1.544MHz to 156.250MHz

±20ppm to ±100ppm 0°C to 70°C -20°C to 70°C -40°C to 85°C

-55°C to 120°C $3.3V \pm 0.3V$

18mA Max 25mA Max 30mA Max 45mA Max

3uA Typ., 10uA Max CMOS

45/55% Max @ 50% Vdd

5ns Max @ 20/% to 80% 4ns Max @ 20/% to 80% 3ns Max @ 20/% to 80% 2.5ns Max @ 20/% to 80% 2ns Max @ 20/% to 80% "0" = 10% Vdd Max "1" = 90% Vdd Min 5ms Max

30pF Max, >80MHz 15pF Max

0.5ps Typ, 1ps Max

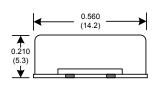
<3ppm 1st/yr, <1ppm every year thereafter

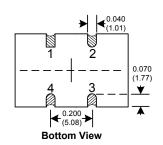
Clock Oscillator With Standby Mode

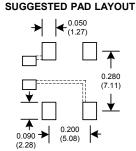


The CFS33xx Series utilizes fundamental and 3rd overtone crystal technology to provide a low jitter output frequency. The oscillator is equipped with power saving standby feature for battery and other low drain applications. Available on tape and reel in quantities of 500ea.

0.560 (14.2)C P/N DC 0.360 (9.14) Frequency \forall

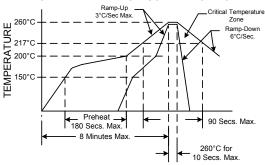


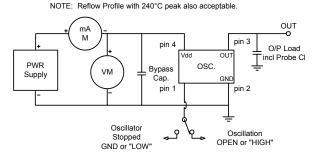




0.01uF Bypass Capacitor Recommended

RECOMMENDED REFLOW SOLDERING PROFILE





Crystek Part Number Guide Example: CFS3392-44.736MHZ

Example: CFSM3392-44.736MHZ Example: CFSE3392-44.736MHZ

| Temperature | | | Frequency Stability |
|--|---|---|---|
| 0/ 70°C | -20/ 70°C | -40/ 85°C | |
| CFS3390 CFS3392 CFS3391 CFS3398 | CFSM3390 CFSM3392 CFSM3391 N/A | CFSE3390 CFSE3392 CFSE3391 N/A | +/- 100ppm +/- 50ppm +/- 25ppm +/- 20ppm |

| Standby Fu | Standby Function | | |
|---|--|--|--|
| Function pin 1 | Oscillator State | | |
| Open "1" level .7Vdd Min "0" level 0.3Vdd Max | Oscillator Active Oscillator Active Oscillator Stopped | | |

Standby Feature: When pin 1 is disabled by applying a logic "0", the output buffer of the oscillator goes into tri-state (high impedance). In addition, the internal oscillator is stopped, hence the entire oscillator consumes less than 10uA from

Specifications subject to change without notice.

TD-102703 Rev.C



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