

### **OVERVIEW**

The SM8143A is a transformer-less electroluminescent (EL) driver IC, capable of driving displays up to  $80 \text{cm}^2$  (SM8143AV) or  $70 \text{cm}^2$  (SM8143AD) in size. It is a high-efficiency driver that features revised coil switching transistor ON resistance and output circuit configuration to reduce loss. The EL drive frequency and coil drive frequency can be controlled independently, making the driver circuit optimizable to match application requirements. A microcontroller interface pin (ENA) is provided, which can be used to control the EL driver ON/OFF function.

### **FEATURES**

- Dedicated EL driver
- 1.6 to 5.5V supply voltage
- Maximum operating current
  - SM8143AV: 150mA max. (V<sub>DD</sub> = 3.0V, Ta ≤ 70°C)
  - SM8143AD: 100mA max.  $(V_{DD} = 3.0V, Ta \le 70^{\circ}C)$
- $3.5\Omega$  typical output resistance
- 200Vp-p maximum EL driver voltage\*
- 31 to 1500Hz EL drive frequency range\*
- High voltage CMOS Process
- Package: VSOP-16, SON-10
- \*: Adjustable with external resistance.

### **APPLICATIONS**

- Cellular phone
- Mobile equipment
- PDA

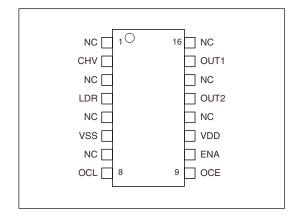
### **ORDERING INFORMATION**

Device	Package
SM8143AV	VSOP-16
SM8143AD	SON-10

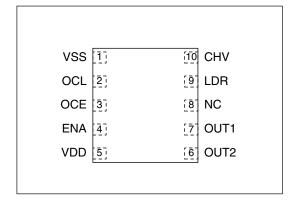
#### **PINOUT**

(Top view)

■ VSOP-16



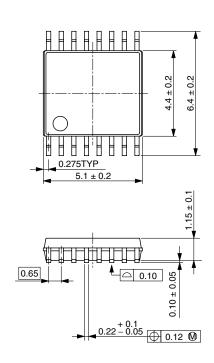
■ SON-10

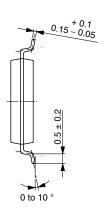


### **PACKAGE DIMENSIONS**

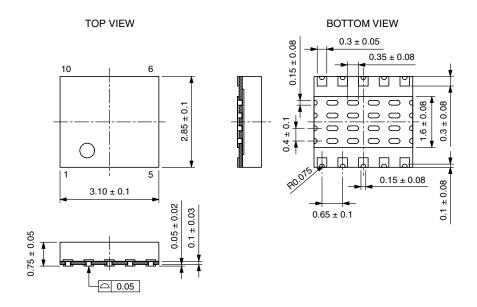
(Unit: mm)

■ VSOP-16

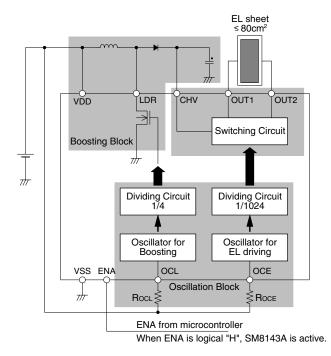




### ■ SON-10



## **BLOCK DIAGRAM**



Note: Brightness and operating current are adjusted with  $\rm R_{\rm OCL}$  . EL driving frequency is adjusted with  $\rm R_{\rm OCE}$  .

## **PIN DESCRIPTION**

Pin nı	umber	Name	1/0	Function	
VSOP-16	SON-10	Name	1/0		
1	8	NC	-	No connection (must be open)	
2	10	CHV	I	High-voltage DC input	
3	-	NC	-	No connection (must be open)	
4	9	LDR	0	Booster coil driver output	
5	-	NC	-	No connection (must be open)	
6	1	VSS	-	Ground	
7	-	NC	-	No connection (must be open)	
8	2	OCL	1	Coil driver oscillator (oscillator frequency determined by external resistor)	
9	3	OCE	I	EL driver oscillator (oscillator frequency determined by external resistor)	
10	4	ENA	I	Enable input (built-in pull-down resistor)	
11	5	VDD	-	Supply	
12	-	NC	-	No connection (must be open)	
13	6	OUT2	0	Output 2	
14	-	NC	-	No connection (must be open)	
15	7	OUT1	0	Output 1	
16	-	NC	_	No connection (must be open)	

# **SPECIFICATIONS**

# **Absolute Maximum Ratings**

Parameter	Symbol	Condition		Rating	Unit		
Supply voltage range	V <sub>DD</sub>			- 0.3 to 7.0	V		
Input voltage range	V <sub>IN</sub>	All input pins		All input pins V <sub>SS</sub> - 0.3 to		$V_{SS} - 0.3 \text{ to } V_{DD} + 0.3$	V
	V <sub>CHV</sub>	CHV pin		0.5 to 120	V		
Output voltage	V <sub>LDR</sub>	LDR pin		LDR pin		0.5 to 120	V
	V <sub>OUT1/2</sub>	OUT1, OUT2 pin		0.5 to 120			
	P <sub>D</sub>	SM8143AV (VSOP-16)	Ta ≤ 70°C	200	mW		
Power dissipation			Ta ≤ 85°C	140	mW		
		01101 101 D (001 10)	Ta ≤ 70°C	140	mW		
		SM8143AD (SON-10) Ta ≤ 85°C		100	mW		
Storage temperature range	T <sub>STG</sub>		-	– 55 to 125	°C		

# **Recommended Operating Conditions**

Parameter	Cumbal	Symbol Condition		Rating			Unit	
Parameter	Syllibol			Condition		typ	max	Unit
Supply voltage range	V <sub>DD2</sub>				1.6	3.0	5.5	V
Operating temperature	T <sub>OPR</sub>				<b>- 40</b>	-	85	°C
Operating current <sup>1</sup>	I <sub>DD2</sub>	SM8143AV (VSOP-16) Including coil current	V <sub>DD</sub> = 3V	Ta ≤ 70°C	-	_	150	mA
				Ta ≤ 85°C	-	-	105	mA
			V <sub>DD</sub> = 5V	Ta ≤ 70°C	-	-	100	mA
				Ta ≤ 85°C	-	-	70	mA
		(SON-10) Including coil	$V_{DD} = 3V$	Ta ≤ 70°C	-	-	100	mA
				Ta ≤ 85°C	-	_	70	mA
			\/ 5\/	Ta ≤ 70°C	-	-	60	mA
			$V_{DD} = 5V$	Ta ≤ 85°C	-	_	42	mA
Coil inductance	L <sub>LDR</sub>	f <sub>LDR</sub> = 64kHz		-	0.47	_	mH	

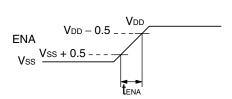
<sup>1.</sup> Max value is as same as Absolute Maximum Ratings.

## **DC Characteristics**

 $V_{DD}$  = 3.0V, Ta = 25°C unless otherwise noted.

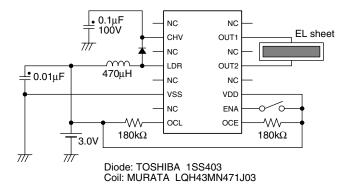
Parameter	Symbol	Condition		Unit		
Parameter	Symbol	Condition	min		max	Onit
Supply voltage	V <sub>DD</sub>		1.6	3.0	5.5	V
CHV output voltage	V <sub>CHV</sub>		0.5	_	100	V
OUT1, OUT2 HIGH-level output voltage	V <sub>OUTH</sub>		-	-	100	V
OUT1, OUT2 LOW-level output voltage	V <sub>OUTL</sub>		-	-	0.5	V
LDR output resistance	R <sub>LDR</sub>	I <sub>LDR</sub> = 50mA	-	3.5	5.25	Ω
OCE oscillator frequency	f <sub>OCE1</sub>	R <sub>OCE</sub> = 180kΩ	205	256	307	kHz
OCE oscillator frequency range	f <sub>OCE2</sub>		32	_	1536	KHZ
OCL oscillator frequency	f <sub>OCL1</sub>	$R_{OCL} = 180k\Omega$	205	256	307	kU-
OCL oscillator frequency range	f <sub>OCL2</sub>		32	-	1536	kHz
OUT1, OUT2 output frequency	f <sub>OUT1</sub>	R <sub>OCE</sub> = 180kΩ	200	250	300	Hz
OUT1, OUT2 output frequency range	f <sub>OUT2</sub>		31	-	1500	HZ
LDR inductance driver frequency	f <sub>LDR1</sub>	$R_{OCL} = 180k\Omega$	51	64	77	kHz
LDR inductance driver frequency range	f <sub>LDR2</sub>		8	-	384	KHZ
ENA HIGH-level input voltage	V <sub>ENAH</sub>	ENA = HIGH, V <sub>DD</sub> = 1.6 to 5.5V	V <sub>DD</sub> – 0.5	-	V <sub>DD</sub> + 0.3	V
ENA LOW-level input voltage	V <sub>ENAL</sub>	ENA = LOW, V <sub>DD</sub> = 1.6 to 5.5V	V <sub>SS</sub> - 0.3	_	V <sub>SS</sub> + 0.5	V
ENA input current	I <sub>ENAH</sub>	$V_{ENAH} = V_{DD} = 3.0V$	2.0	4.0	6.0	μΑ
ENA rise time <sup>1</sup>	t <sub>ENA</sub>		-	-	100	μs
Operating current	I <sub>DD1</sub>	Excluding coil current	-	_	1.0	mA
Stand-by current	I <sub>STB</sub>	ENA = LOW	-	-	1.0	μΑ

1.

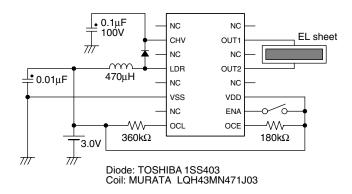


### **TYPICAL APPLICATIONS**

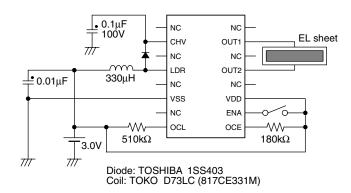
■ EL sheet size: 20 to 30cm², Current consumption: 20mA



■ EL sheet size: 30 to 50cm<sup>2</sup>, Current consumption: 40mA



■ EL sheet size: 50 to 100cm<sup>2</sup>, Current consumption: 80mA



Note: Do not operate the SM8143A with the EL sheet NOT connected (no load to OUT1/OUT2) since the IC will be damaged.

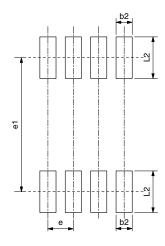
# **FOOTPRINT**

The optimum footprint varies depending on the board material, soldering paste, soldering method, and equipment accuracy, all of which need to be considered to meet design specifications.

(Unit: mm)

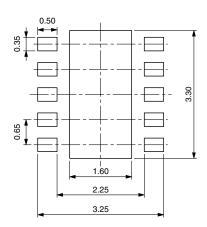
### ■ VSOP-16

Package	b2	L2	е	el	
VSOP-16	0.55	0.95	0.65	5.90	

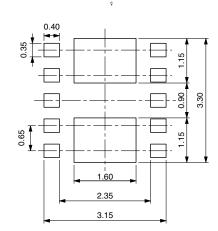


### ■ SON-10

• Footprint pattern



· Metalmask pattern



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