TOSHIBA Field Effect Transistor Silicon N Channel Dual Gate MOS Type

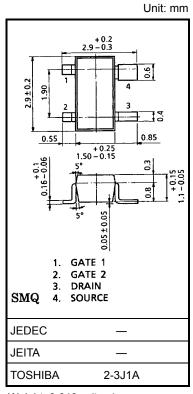
3SK225

TV Tuner, VHF RF Amplifier Applications FM Tuner Applications
TV Tuner, UHF RF Amplifier Applications

- · Superior cross modulation performance.
- Low noise figure: NF = 2.0dB (typ.)

Maximum Ratings (Ta = 25°C)

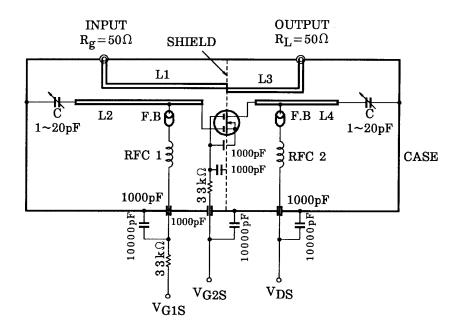
Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	13.5	V
Gate 1-source voltage	V _{G1S}	±8	V
Gate 2-source voltage	V _{G2S}	±8	V
Drain current	I _D	30	mA
Drain power dissipation	P _D	150	mW
Channel temperature	T _{ch}	125	°C
Storage temperature range	T _{stg}	-55~125	°C



Weight: 0.013 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate 1 leakage current	I _{G1SS}	$V_{DS} = 0$, $V_{G1S} = \pm 6$ V, $V_{G2S} = 0$	_	_	±50	nA
Gate 2 leakage current	I _{G2SS}	$V_{DS} = 0$, $V_{G1S} = 0$, $V_{G2S} = \pm 6 \text{ V}$	_	_	±50	nA
Drain-source voltage	V (BR) DSX	$V_{G1S} = -4~V,~V_{G2S} = -4~V,~I_D = 100~\mu\text{A}$	13.5	_	_	V
Drain current	I _{DSS}	$V_{DS} = 6 \text{ V}, V_{G1S} = 0, V_{G2S} = 4.5 \text{ V}$	0	_	0.1	mA
Gate 1-source cut-off voltage	V _{G1S} (OFF)	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 100 \mu A$	0	_	1.0	V
Gate 2-source cut-off voltage	V _{G2S (OFF)}	$V_{DS} = 6 \; V, \; V_{G1S} = 4 \; V, \; I_D = 100 \; \mu A$	0.5	1.0	1.5	V
Forward transfer admittance	Y _{fs}	$\begin{split} V_{DS} = 6 \text{ V}, & V_{G2S} = 4.5 \text{ V}, I_D = 10 \text{ mA}, \\ f = 1 kHz \end{split}$	_	21	_	mS
Input capacitance	C _{iss}	V _{DS} = 6 V, V _{G2S} = 4.5 V, I _D = 10 mA,	_	3.4	4.4	pF
Reverse transfer capacitance	C _{rss}	f = 1 MHz	_	0.020	0.05	pF
Power gain	G _{ps}	V _{DS} = 6 V, V _{G2S} = 4.5 V, I _D = 10 mA,	19	22	_	dB
Noise figure	NF	f = 500 MHz (Figure 1)	_	2.0	3.5	dB



L1 L4: \(\phi 0.8 \) mm silver plated copper wire

C: Air trimmer TTA25A200A (MURATA Manufacturing. Co., Ltd.)

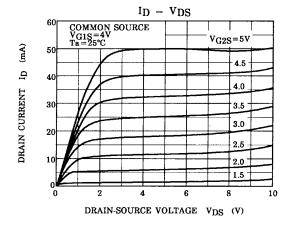
RFC 1: $\phi 0.35$ mm copper wire 3 mm ID, 7 T

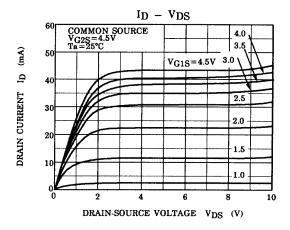
RFC 2: $\phi 0.35$ mm copper wire 3 mm ID, 10 T

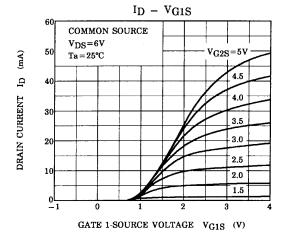
Figure 1 500 MHz, G_{ps}, NF Test Circuit

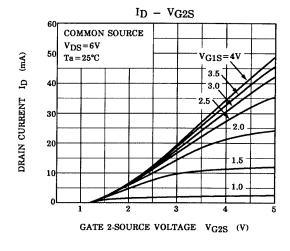
Marking

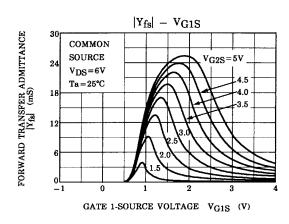


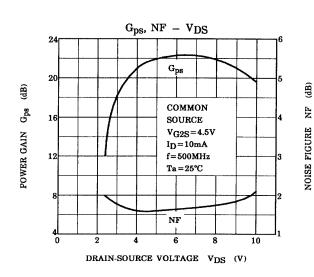


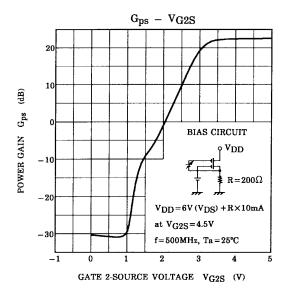


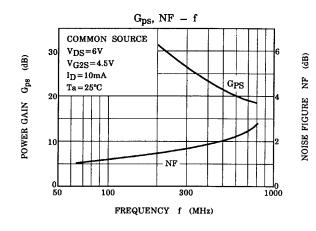


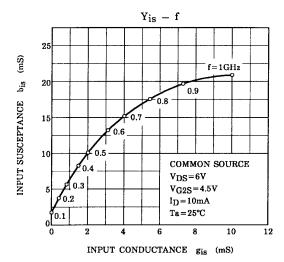


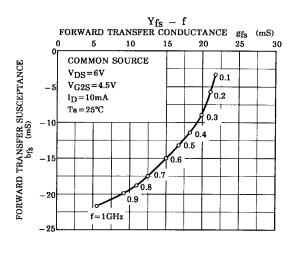


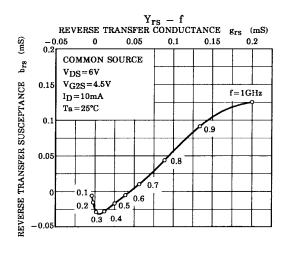


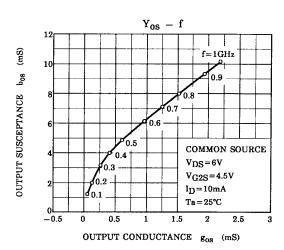


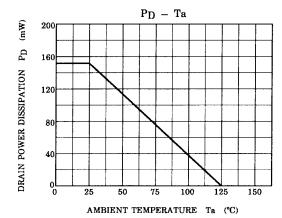












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