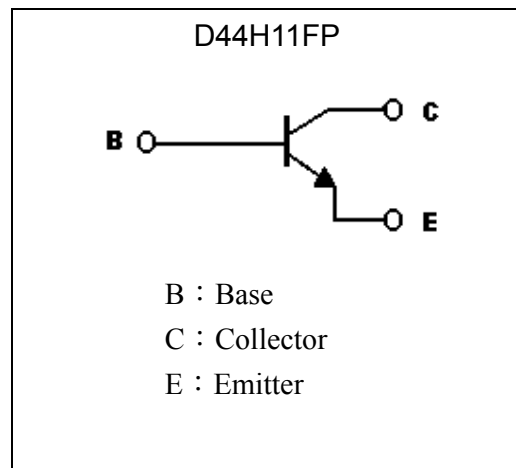
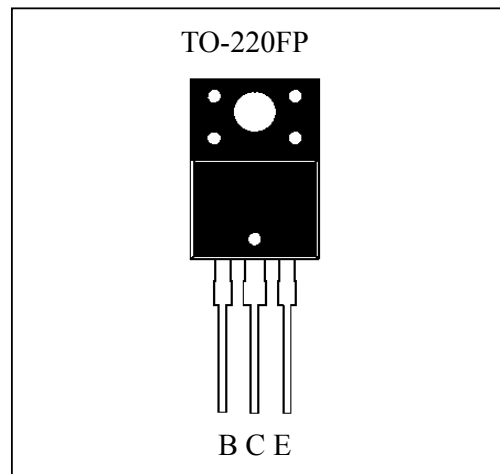


**Low Vcesat NPN Epitaxial Planar Transistor**

# D44H11FP

**Features**

- Low  $V_{CE(sat)}$
- High  $BV_{CEO}$
- Excellent current gain characteristics
- Pb-free package

**Symbol**

**Outline**

**Absolute Maximum Ratings** ( $T_a=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	$V_{CBO}$	80	V
Collector-Emitter Voltage	$V_{CEO}$	80	V
Emitter-Base Voltage	$V_{EBO}$	6	V
Collector Current (DC)	$I_C$	10	A
Collector Current (Pulse)	$I_{CP}$	20 (Note 1)	
Power Dissipation @ $T_A=25^\circ\text{C}$	$P_D$	2	W
Power Dissipation @ $T_C=25^\circ\text{C}$	$P_D$	50	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55~+150	$^\circ\text{C}$

 Note : 1. Single Pulse ,  $P_w \leq 380\mu\text{s}$ ,  $Duty \leq 2\%$ .



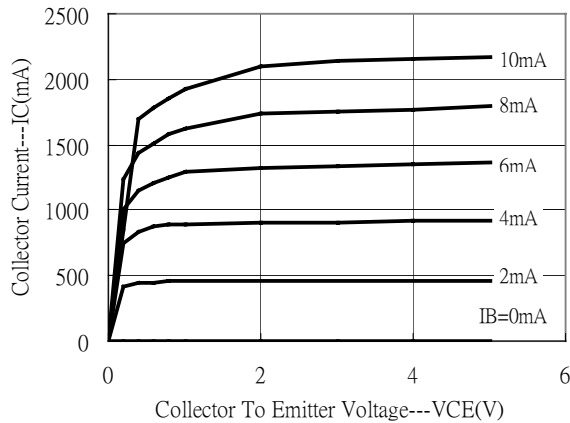
**Characteristics (Ta=25°C)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV <sub>CEO(SUS)</sub>	80	-	-	V	I <sub>C</sub> =30mA, I <sub>B</sub> =0
I <sub>CES</sub>	-	-	10	μA	V <sub>CE</sub> =80V, V <sub>BE</sub> =0
I <sub>EBO</sub>	-	-	50	μA	V <sub>EB</sub> =5V, I <sub>C</sub> =0
*V <sub>CE(sat)</sub>	-	0.3	0.6	V	I <sub>C</sub> =8A, I <sub>B</sub> =0.4A
*V <sub>BE(sat)</sub>	-	1.0	1.5	V	I <sub>C</sub> =8A, I <sub>B</sub> =0.8A
*h <sub>FE</sub>	60	-	-	-	V <sub>CE</sub> =1V, I <sub>C</sub> =2A
*h <sub>FE</sub>	40	-	-	-	V <sub>CE</sub> =1V, I <sub>C</sub> =4A
f <sub>T</sub>	-	50	-	MHz	V <sub>CE</sub> =6V, I <sub>C</sub> =500mA, f=20MHz
Cob	-	130	-	pF	V <sub>CB</sub> =10V, f=1MHz

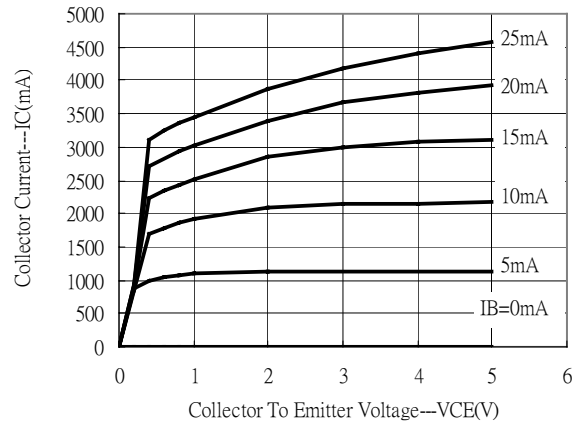
\*Pulse Test : Pulse Width ≤380μs, Duty Cycle≤2%

**Characteristic Curves**

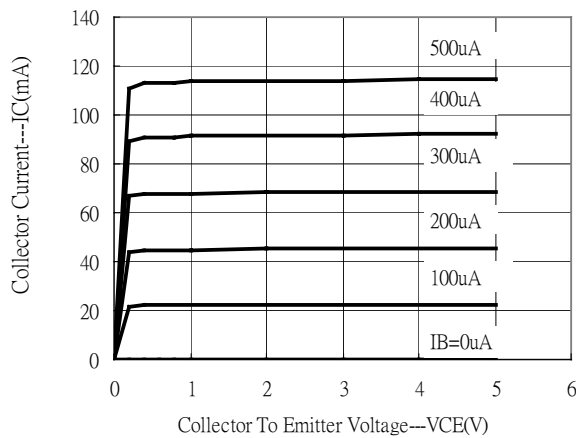
Grounded Emitter Output Characteristics



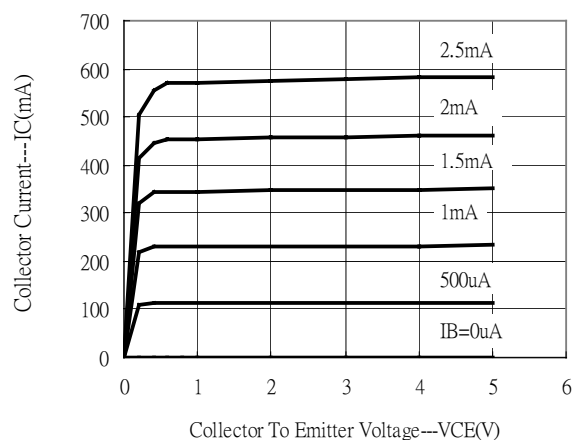
Grounded Emitter Output Characteristics



Grounded Emitter Output Characteristics

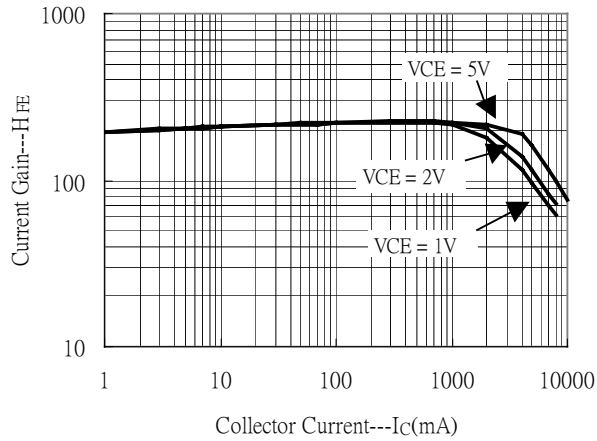


Grounded Emitter Output Characteristics

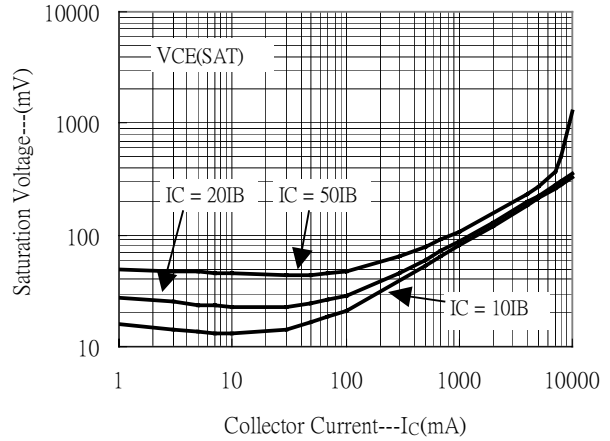




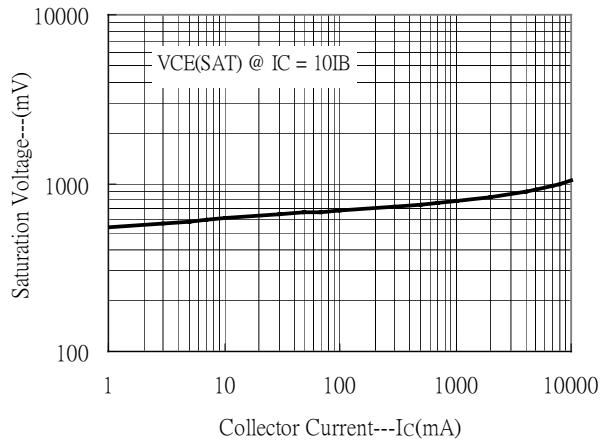
### Current Gain vs Collector Current



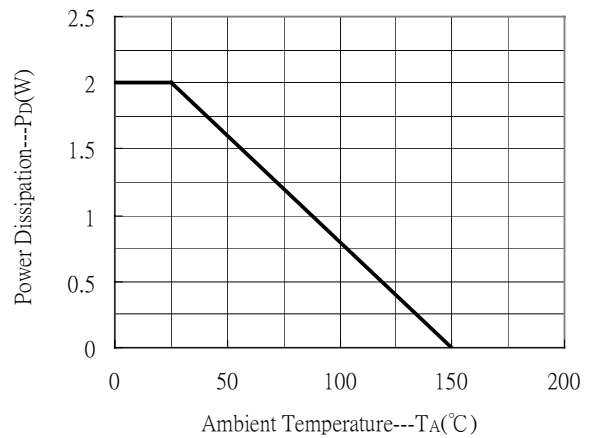
### Saturation Voltage vs Collector Current



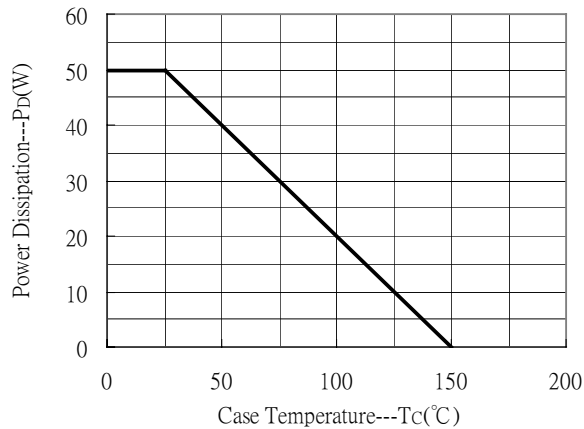
### Saturation Voltage vs Collector Current



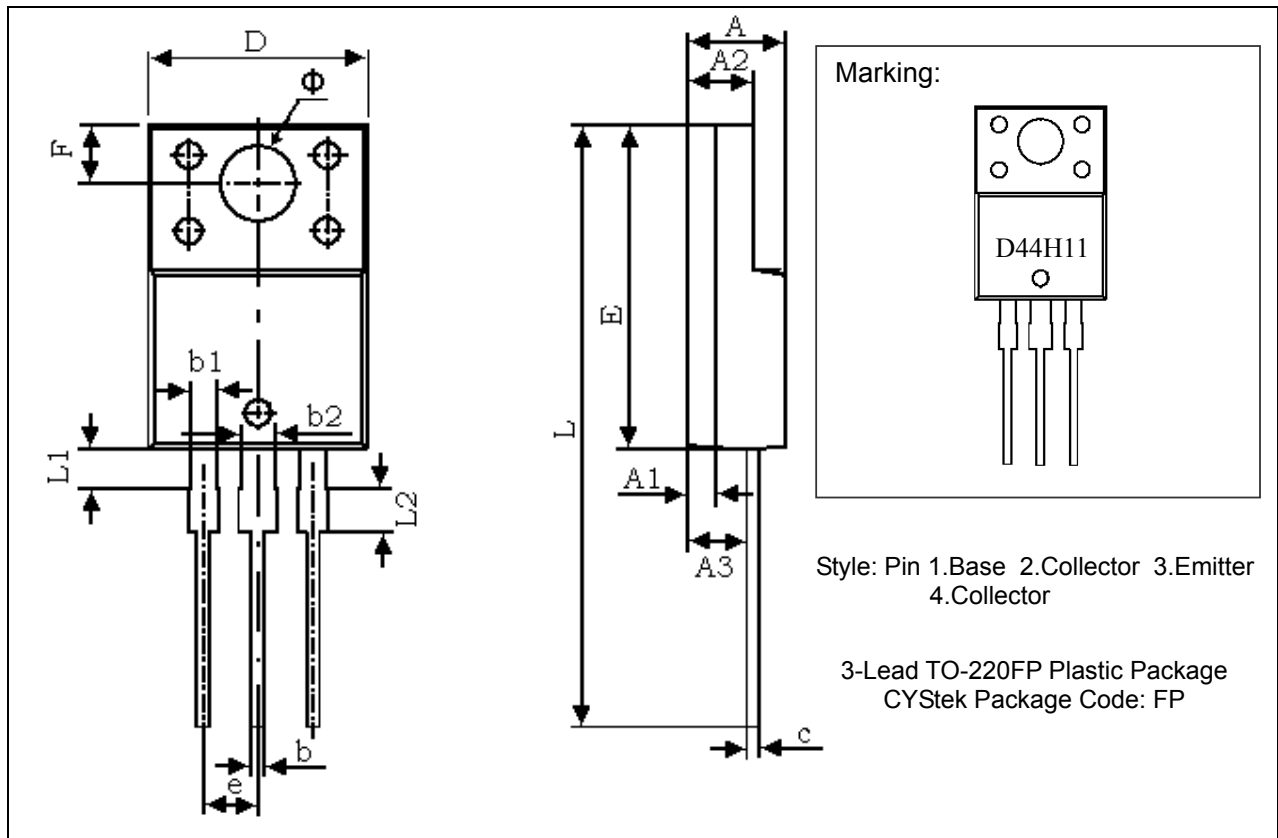
### Power Derating Curve



### Power Derating Curve



**TO-220FP Dimension**



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.169	0.185	4.300	4.700	D	0.392	0.408	9.960	10.360
A1	0.051 REF		1.300 REF		E	0.583	0.598	14.800	15.200
A2	0.110	0.126	2.800	3.200	e	0.100 TYP		2.540 TYP	
A3	0.098	0.114	2.500	2.900	F	0.106 REF		2.700 REF	
b	0.020	0.030	0.500	0.750	Φ	0.138 REF		3.500 REF	
b1	0.043	0.053	1.100	1.350	L	1.102	1.118	28.000	28.400
b2	0.059	0.069	1.500	1.750	L1	0.067	0.075	1.700	1.900
c	0.020	0.030	0.500	0.750	L2	0.075	0.083	1.900	2.100

- Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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