

■ OUTLINE

The R1211X Series are CMOS-based PWM step-up DC/DC converter controllers with low supply current.

Each of the R1211X Series consists of an oscillator, a PWM control circuit, a reference voltage unit, an error amplifier, a reference current unit, a protection circuit, and an under voltage lockout (UVLO) circuit. A low ripple, high efficiency step-up DC/DC converter can be composed of this IC with some external components, or an inductor, a diode, a power MOSFET, divider resistors, and capacitors.

Phase compensation has been made internally in the R1211X002B/D Series, while phase compensation can be made externally as for R1211X002A/C Series. B/D version has stand-by mode. Max duty cycle is internally fixed typically at 90%. Soft start function is built-in, and Soft-starting time is set typically at 9ms(A/B, 700kHz version) or 10.5ms(C/D, 300kHz version). As for the protection circuit, after the soft-starting time, if the maximum duty cycle is continued for a certain period, the R1211X Series latch the external driver with its off state, or Latch-type protection circuit works. The delay time for latch the state can be set with an external capacitor.

To release the protection circuit, restart with power-on (Voltage supplier is equal or less than UVLO detector threshold level), or once after making the circuit be stand-by with chip enable pin and enable the circuit again.

■ FEATURES

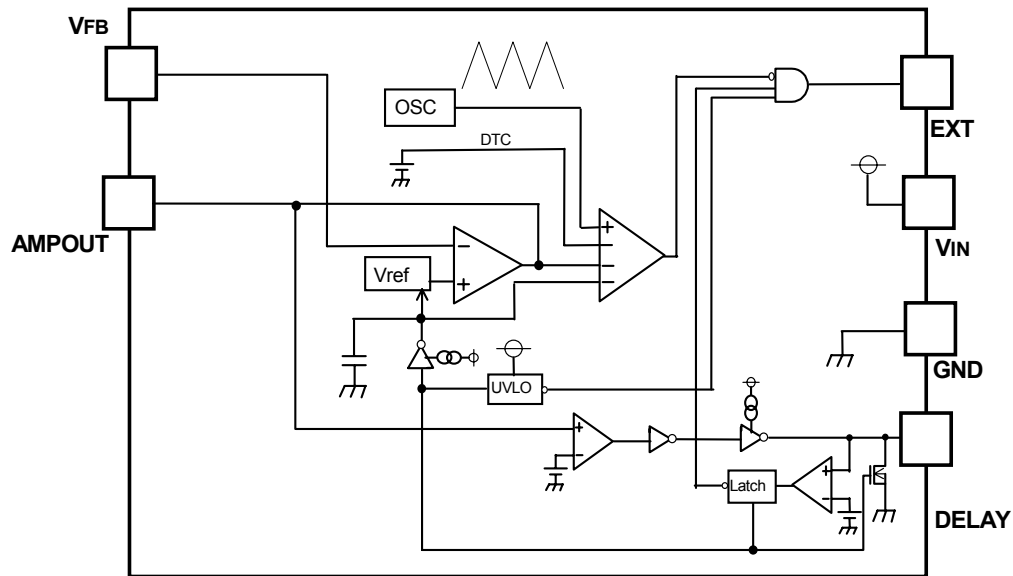
- Standby Current TYP. 0 μ A (for B/D version)
- Input Voltage Range 2.5V to 6.0V
- Built-in Latch-type Protection Function (Output Delay Time can be set with an external capacitor)
- Two Options of Basic Oscillator Frequency •• 300kHz, 700kHz
- Max Duty Cycle Typ. 90%
- High Reference Voltage Accuracy \pm 1.5%
- U.V.L.O. Threshold level Typ. 2.2V (Hysteresis TYP. 0.13V)
- Small Package SOT-23-6W or thin (package height MAX. 0.85mm) SON-6 (Under Development)

■ APPLICATIONS

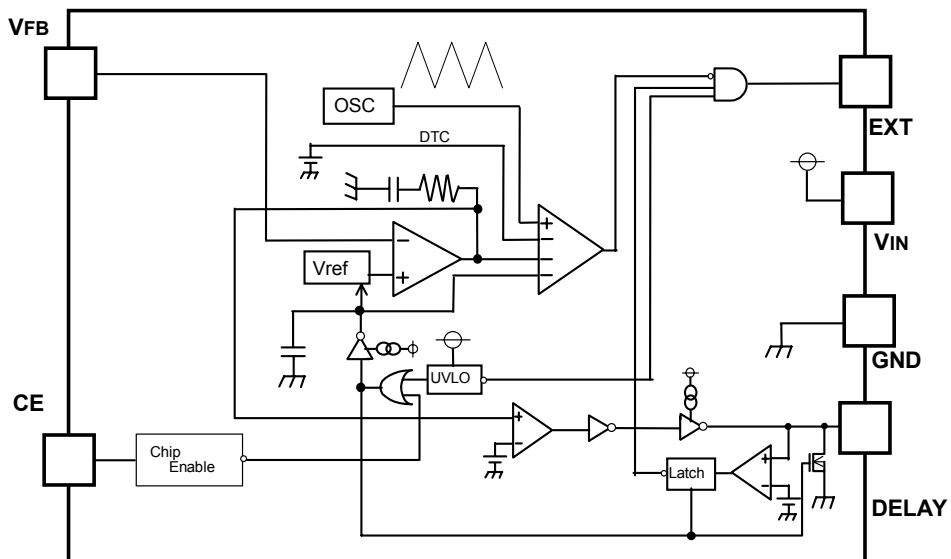
- Constant Voltage Power Source for portable equipment.
- Constant Voltage Power Source for LCD and CCD.

■ BLOCK DIAGRAMS

Version A



Version B



SELECTION GUIDE

In the R1211X Series, the oscillator frequency, the optional function, and the package type for the ICs can be selected at the user's request.

The selection can be made with designating the part number as shown below;

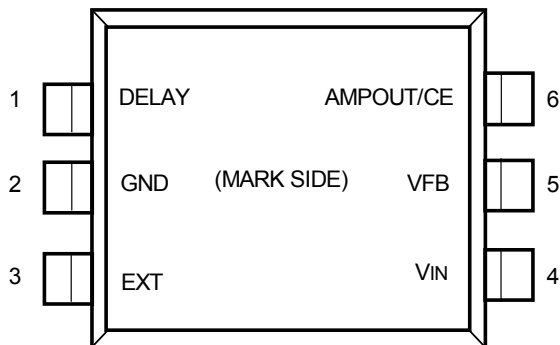
R1211X002X-TR

↑ ↑
 a b

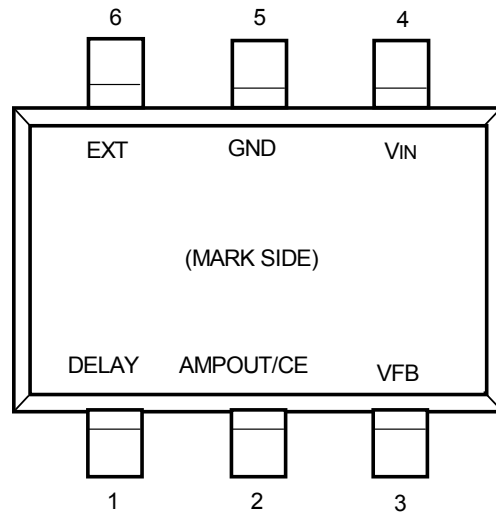
Code	Contents
a	Designation of Package Type: D: SON-6 N: SOT23-6W
b	Designation of Optional Function A : 700kHz, with AMPOUT pin (External Phase Compensation Type) B : 700 kHz, with CE pin (Internal Phase Compensation Type, with Stand-by) C : 300kHz, with AMPOUT pin (External Phase Compensation Type) D : 300kHz, with CE pin (Internal Phase Compensation Type, with Stand-by)

PIN CONFIGURATIONS

SON-6



SOT-23-6W



■ PIN DESCRIPTIONS

Pin No.		Symbol	Description
SON6	SOT23-6W		
1	1	DELAY	Pin for External Capacitor (for Setting Output Delay of Protection)
2	5	GND	Ground Pin
3	6	EXT	External FET Drive Pin (CMOS Output)
4	4	VIN	Power Supply Pin
5	3	VFB	Feedback Pin for monitoring Output Voltage
6	2	AMPOUT or CE	Amplifier Output Pin(A/C Version) or Chip Enable Pin(B/D Version, Active at "H")

■ ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
VIN	VIN Pin Voltage	6.5	V
VEXT	EXT Pin Output Voltage	-0.3~VIN+0.3	V
VDLY	DELAY Pin Voltage	-0.3~VIN+0.3	V
VAMP	AMPOUT Pin Voltage	-0.3~VIN+0.3	V
VCE	CE Pin Input Voltage	-0.3~VIN+0.3	V
VFB	VFB Pin Voltage	-0.3~VIN+0.3	V
IAMP	AMPOUT Pin Current	±10	mA
IEXT	EXT Pin Inductor Drive Output Current	±50	mA
PD	Power Dissipation	250	mW
Topt	Operating Temperature Range	-40~+85	°C
Tstg	Storage Temperature Range	-55~+125	°C

■ ELECTRICAL CHARACTERISTICS

●R1211X002A

(T_{opt}=25°C)

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
V _{IN}	Operating Input Voltage		2.5		6.0	V
V _{F_B}	V _{F_B} Voltage Tolerance	V _{IN} =3.3V	0.985	1.000	1.015	V
ΔV _{F_B} /ΔT	V _{F_B} Voltage Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±150		ppm/°C
I _{F_B}	V _{F_B} Input Current	V _{IN} =6V, V _{F_B} =0V or 6V	-0.1		0.1	μA
f _{OSC}	Oscillator Frequency	V _{IN} =3.3V, V _{DLY} =V _{F_B} =0V	595	700	805	kHz
Δf _{OSC} /ΔT	Oscillator Frequency Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±1.4		kHz/°C
I _{DD1}	Supply Current 1	V _{IN} =6V, V _{DLY} =V _{F_B} =0V, EXT at no load		600	900	μA
maxdty	Maximum Duty Cycle	V _{IN} =3.3V, EXT "H" side	82	90	94	%
R _{EXTH}	EXT "H" ON Resistance	V _{IN} =3.3V, I _{EXT} =-20mA		5	10	Ω
R _{EXTL}	EXT "L" ON Resistance	V _{IN} =3.3V, I _{EXT} =20mA		3	6	Ω
I _{DLY1}	Delay Pin Charge Current	V _{IN} =3.3V, V _{DLY} =V _{F_B} =0V	2.5	5.0	7.5	μA
I _{DLY2}	Delay Pin Discharge Current	V _{IN} =V _{F_B} =2.5V, V _{DLY} =0.1V	2.5	5.5	9.0	mA
V _{DLY}	Delay Pin Detector Threshold	V _{IN} =3.3V, V _{F_B} =0V, V _{DLY} =0V→2V	0.95	1.00	1.05	V
T _{START}	Soft-start Time	V _{IN} =3.3V at 90% of rising edge	4.5	9.0	13.5	ms
V _{UVLO1}	UVLO Detector Threshold	V _{IN} =3.3V→0V, V _{DLY} =V _{F_B} =0V	2.1	2.2	2.3	V
V _{UVLO2}	UVLO Detector Hysteresis	V _{IN} =0V→3.3V, V _{DLY} =V _{F_B} =0V	0.08	0.13	0.18	V
I _{AMP1}	AMP "H" Output Current	V _{IN} =3.3V, V _{AMP} =1V, V _{F_B} =0.9V	0.45	0.90	1.50	mA
I _{AMP2}	AMP "L" Output Current	V _{IN} =3.3V, V _{AMP} =1V, V _{F_B} =1.1V	30	60	90	μA

●R1211X002B

(Topt=25°C)

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
VIN	Operating Input Voltage		2.5		6.0	V
VFB	VFB Voltage Tolerance	VIN=3.3V	0.985	1.000	1.015	V
$\Delta V_{FB}/\Delta T$	VFB Voltage Temperature Coefficient	-40°C ≤ Topt ≤ 85°C		±150		ppm/°C
IFB	VFB Input Current	VIN=6V, VFB=0V or 6V	-0.1		0.1	μA
fOSC	Oscillator Frequency	VIN=3.3V, VDLY=VFB=0V	595	700	805	kHz
$\Delta f_{OSC}/\Delta T$	Oscillator Frequency Temperature Coefficient	-40°C ≤ Topt ≤ 85°C		±1.4		kHz/°C
IDD1	Supply Current 1	VIN=6V, VDLY=VFB=0V, EXT at no load		600	900	μA
maxdty	Maximum Duty Cycle	VIN=3.3V, EXT "H" side	82	90	94	%
REXTH	EXT "H" ON Resistance	VIN=3.3V, IEXT=-20mA		5	10	Ω
REXTL	EXT "L" ON Resistance	VIN=3.3V, IEXT=20mA		3	6	Ω
IDLY1	Delay Pin Charge Current	VIN=3.3V, VDLY=VFB=0V	2.5	5.0	7.5	μA
IDLY2	Delay Pin Discharge Current	VIN=VFB=2.5V, VDLY=0.1V	2.5	5.5	9.0	mA
VDLY	Delay Pin Detector Threshold	VIN=3.3V, VFB=0V, VDLY=0V→2V	0.95	1.00	1.05	V
TSTART	Soft-start Time	VIN=3.3V	4.5	9.0	13.5	ms
VUVLO1	UVLO Detector Threshold	VIN=3.3V→0V, VDLY=VFB=0V	2.1	2.2	2.3	V
VUVLO2	UVLO Detector Hysteresis	VIN=0V→3.3V, VDLY=VFB=0V	0.08	0.13	0.18	V
ISTB	Standby Current	VIN=6V, VCE=0V		0	1	μA
ICEH	CE "H" Input Current	VIN=6V, VCE=6V	-0.5		0.5	μA
ICEL	CE "L" Input Current	VIN=6V, VCE=0V	-0.5		0.5	μA
VCEH	CE "H" Input Voltage	VIN=6V, VCE=0V→6V	1.5			V
VCEL	CE "L" Input Voltage	VIN=2.5V, VCE=2V→0V			0.3	V

●R1211X002C

(Topt=25°C)

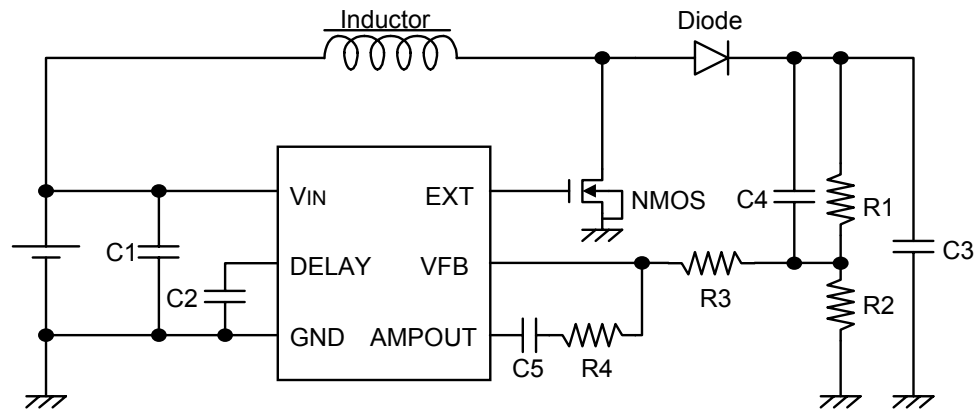
Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
V _{IN}	Operating Input Voltage		2.5		6.0	V
V _{F_B}	V _{F_B} Voltage Tolerance	V _{IN} =3.3V	0.985	1.000	1.015	V
$\frac{\Delta V_{FB}}{\Delta T}$	V _{F_B} Voltage Temperature Coefficient	-40°C ≤ Topt ≤ 85°C		±150		ppm/°C
I _{F_B}	V _{F_B} Input Current	V _{IN} =6V, V _{F_B} =0V or 6V	-0.1		0.1	μA
f _{OSC}	Oscillator Frequency	V _{IN} =3.3V, V _{DLY} =V _{F_B} =0V	240	300	360	kHz
$\frac{\Delta f_{OSC}}{\Delta T}$	Oscillator Frequency Temperature Coefficient	-40°C ≤ Topt ≤ 85°C		±0.6		kHz/°C
I _{DD1}	Supply Current 1	V _{IN} =6V, V _{DLY} =V _{F_B} =0V, EXT at no load		300	500	μA
maxdty	Maximum Duty Cycle	V _{IN} =3.3V, EXT "H" side	82	90	94	%
R _{EXTH}	EXT "H" ON Resistance	V _{IN} =3.3V, I _{EXT} =-20mA		5	10	Ω
R _{EXTL}	EXT "L" ON Resistance	V _{IN} =3.3V, I _{EXT} =20mA		3	6	Ω
I _{DLY1}	Delay Pin Charge Current	V _{IN} =3.3V, V _{DLY} =V _{F_B} =0V	2.0	4.5	7.0	μA
I _{DLY2}	Delay Pin Discharge Current	V _{IN} =V _{F_B} =2.5V, V _{DLY} =0.1V	2.5	5.5	9.0	mA
V _{DLY}	Delay Pin Detector Threshold	V _{IN} =3.3V, V _{F_B} =0V, V _{DLY} =0V→2V	0.95	1.00	1.05	V
T _{START}	Soft-start Time	V _{IN} =3.3V	5.0	10.5	16.0	ms
V _{UVLO1}	UVLO Detector Threshold	V _{IN} =3.3V→0V, V _{DLY} =V _{F_B} =0V	2.1	2.2	2.3	V
V _{UVLO2}	UVLO Detector Hysteresis	V _{IN} =0V→3.3V, V _{DLY} =V _{F_B} =0V	0.08	0.13	0.18	V
I _{AMP1}	AMP "H" Output Current	V _{IN} =3.3V, V _{AMP} =1V, V _{F_B} =0.9V	0.45	0.90	1.50	mA
I _{AMP2}	AMP "L" Output Current	V _{IN} =3.3V, V _{AMP} =1V, V _{F_B} =1.1V	25	50	75	μA

●R1211X002D

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
VIN	Operating Input Voltage		2.5		6.0	V
VFB	VFB Voltage Tolerance	VIN=3.3V	0.985	1.000	1.015	V
$\Delta V_{FB}/\Delta T$	VFB Voltage Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±150		ppm/°C
IFB	VFB Input Current	VIN=6V, VFB=0V or 6V	-0.1		0.1	μA
fOSC	Oscillator Frequency	VIN=3.3V, VDLY=VFB=0V	240	300	360	kHz
$\Delta f_{OSC}/\Delta T$	Oscillator Frequency Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±0.6		kHz/°C
IDD1	Supply Current 1	VIN=6V, VDLY=VFB=0V, EXT at no load		300	500	μA
maxdty	Maximum Duty Cycle	VIN=3.3V, EXT "H" side	82	90	94	%
REXTH	EXT "H" ON Resistance	VIN=3.3V, IEXT=-20mA		5	10	Ω
REXTL	EXT "L" ON Resistance	VIN=3.3V, IEXT=20mA		3	6	Ω
IDLY1	Delay Pin Charge Current	VIN=3.3V, VDLY=VFB=0V	2.0	4.5	7.0	μA
IDLY2	Delay Pin Discharge Current	VIN=VFB=2.5V, VDLY=0.1V	2.5	5.5	9.0	mA
VDLY	Delay Pin Detector Threshold	VIN=3.3V, VFB=0V, VDLY=0V→2V	0.95	1.00	1.05	V
TSTART	Soft-start Time	VIN=3.3V	5.0	10.5	16.0	ms
VUVLO1	UVLO Detector Threshold	VIN=3.3V→0V, VDLY=VFB=0V	2.1	2.2	2.3	V
VUVLO2	UVLO Detector Hysteresis	VIN=0V→3.3V, VDLY=VFB=0V	0.08	0.13	0.18	V
ISTB	Standby Current	VIN=6V, VCE=0V		0	1	μA
ICEH	CE "H" Input Current	VIN=6V, VCE=6V	-0.5		0.5	μA
ICEL	CE "L" Input Current	VIN=6V, VCE=0V	-0.5		0.5	μA
VCEH	CE "H" Input Voltage	VIN=6V, VCE=0V→6V	1.5			V
VCEL	CE "L" Input Voltage	VIN=2.5V, VCE=2V→0V			0.3	V

■ TYPICAL APPLICATIONS AND TECHNICAL NOTES

<R1211X002A/R1211X002C>



NMOS: IRF7601 (International Rectifier)

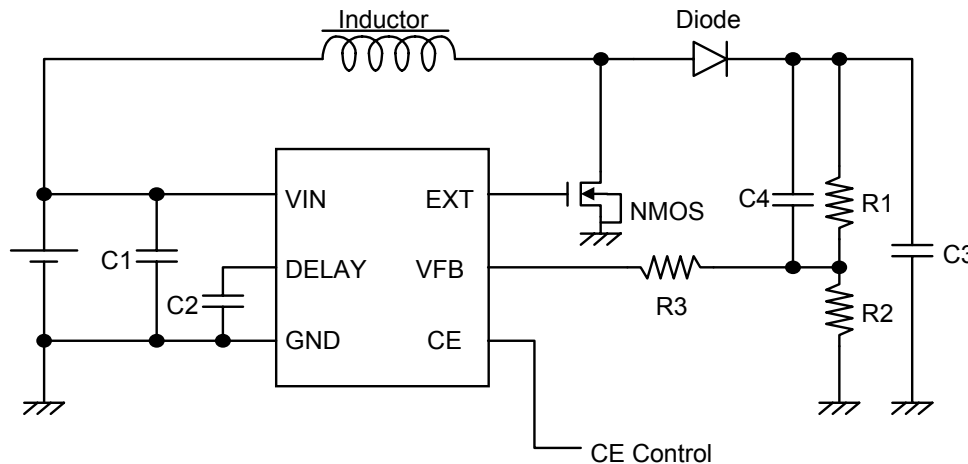
Inductor : LDR655312T-100 10 μ H (TDK) for R1211X002A
: LDR655312T-220 22 μ H (TDK) for R1211X002C

Diode: CRS02 (Toshiba)

C1: 4.7 μ F (Ceramic)
C2: 0.22 μ F (Ceramic)
C3: 10 μ F (Ceramic)
C4: 680pF (Ceramic)
C5: 2200pF (Ceramic)

R1: Output Voltage Setting Resistor 1
R2: Output Voltage Setting Resistor 2
R3: 30k Ω
R4: 30k Ω

<R1211X002B/R1211X002D>



NMOS: IRF7601 (International Rectifier)

Inductor: LDR655312T-100 10 μ H (TDK) for R1211X002B
LDR655312T-220 22 μ H (TDK) for R1211X002D

Diode: CRS02 (Toshiba)

C1: 4.7 μ F (Ceramic)
C2: 0.22 μ F (Ceramic)
C3: 10 μ F (Ceramic)
C4: 680pF (Ceramic)

R1: Setting Output Voltage Resistor1
R2: Setting Output Voltage Resistor2
R3 : 30k Ω

[Note] These example circuits may be applied to the output voltage requirement is 15V or less. If the output voltage requirement is 15V or more, ratings of NMOS and diode as shown above is over the limit, therefore, choose other external components.

● Use a 1μF or more capacitance value of bypass capacitor between VIN pin and GND, C1 as shown in the typical applications above.

● In terms of the capacitor for setting delay time of the latch protection, C2 as shown in typical applications of the previous page, connect between Delay pin and GND pin of the IC with the minimum wiring distance.

● Connect a 1μF or more value of capacitor between VOUT and GND, C3 as shown in typical applications of the previous page. (Recommended value is from 10μF to 22μF.) If the operation of the composed DC/DC converter may be unstable, use a tantalum type capacitor instead of ceramic type.

● Connect a capacitor between VOUT and the dividing point, C4 as shown in typical applications of the previous page. The capacitance value of C4 depends on divider resistors for output voltage setting. Typical value is between 100pF and 1000pF.

● Output Voltage can be set with divider resistors for voltage setting, R1 and R2 as shown in typical applications of the previous page. Refer to the next formula.

$$\text{Output Voltage} = V_{FB} \times (R1+R2)/R2$$

R1+R2=100kΩ is recommended range of resistances.

● The operation of Latch protection circuit is as follows: When the IC detects maximum duty cycle, charge to an external capacitor, C2 of DELAY pin starts. And maximum duty cycle continues and the voltage of DELAY pin reaches delay voltage detector threshold, VDLY, outputs “L” to EXT pin and turns off the external power MOSFET.

To release the latch protection operation, make the IC be standby mode with CE pin and make it active in terms of B/D version. Otherwise, restart with power on.

The delay time of latch protection can be calculated with C2, VDLY, and Delay Pin Charge Current, IDLY1, as in the next formula.

$$t=C2 \times VDLY/IDLY1$$

Once after the maximum duty is detected and released before delay time, charge to the capacitor is halt and delay pin outputs “L”.

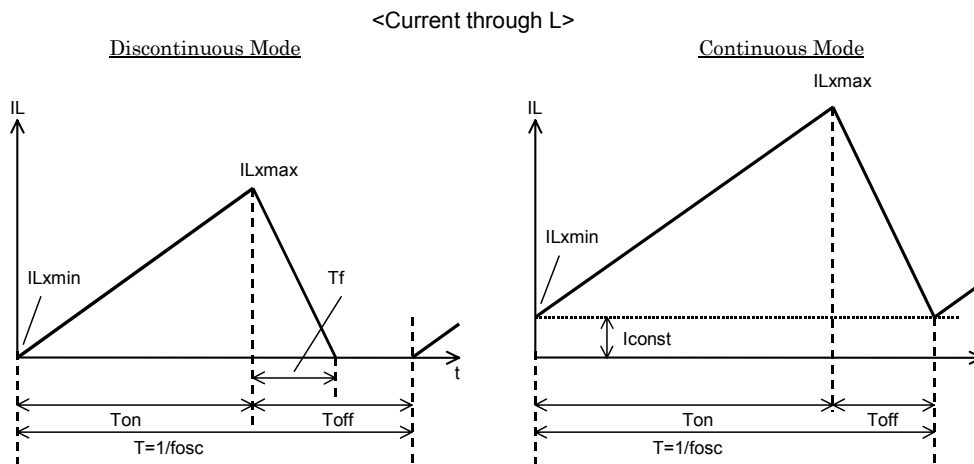
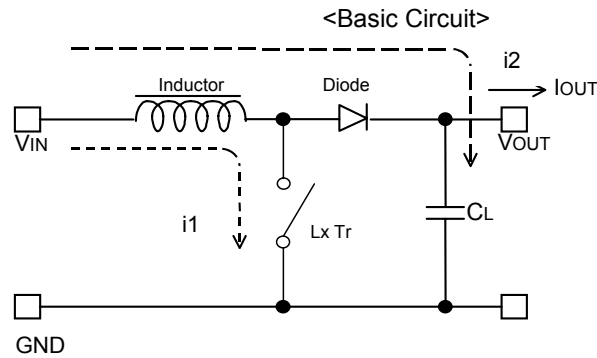
● As for R1211X002A/C version, the values and positioning of C4, C5, R3, and R4 shown in the above diagram are just an example combination. These are for making phase compensation. If the spike noise of VOUT may be large, the spike noise may be picked into VFB pin and make the operation unstable. In this case, a resistor R3, shown in typical applications of the previous page. The recommended resistance value of R3 is in the range from 10kΩ to 50kΩ. Then, noise level will be decreased.

● As for R1211X002B/D version, EXT pin outputs GND level at standby mode.

● Select the Power MOSFET, the diode, and the inductor within ratings (Voltage, Current, Power) of this IC. Choose the power MOSFET with low threshold voltage depending on Input Voltage to be able to turn on the FET completely. Choose the diode with low VF such as Schottky type, and with low reverse current IR, and with fast switching speed. When an external transistor is switching, spike voltage may be generated caused by an inductor, therefore recommended voltage tolerance of capacitor connected to VOUT is three times of setting voltage or more.

☆The performance of power circuit with using this IC depends on external components. Choose the most suitable components for your application.

Output Current and Selection of External Components



There are two modes, or discontinuous mode and continuous mode for the PWM step-up switching regulator depending on the continuous characteristic of inductor current.

During on time of the transistor, when the voltage added on to the inductor is described as V_{IN} , the current is $V_{IN} \times t/L$. Therefore, the electric power, P_{ON} , which is supplied with input side, can be described as in next formula.

$$P_{ON} = \int_0^{T_{ON}} V_{IN}^2 \times t/L \, dt \quad \text{Formula 1}$$

With the step-up circuit, electric power is supplied from power source also during off time. In this case, input current is described as $(V_{OUT} - V_{IN}) \times t/L$, therefore electric power, P_{OFF} is described as in next formula.

$$P_{OFF} = \int_0^{T_f} V_{IN} \times (V_{OUT} - V_{IN}) \times t/L \, dt \quad \text{Formula 2}$$

In this formula, T_f means the time of which the energy saved in the inductance is being emitted. Thus average electric power, P_{AV} is described as in the next formula.

$$P_{AV} = 1/(T_{on} + T_{off}) \times \left\{ \int_0^{T_{ON}} V_{IN}^2 \times t/L \, dt + \int_0^{T_f} V_{IN} \times (V_{OUT} - V_{IN}) \times t/L \, dt \right\} \quad \text{Formula 3}$$

In PWM control, when $T_f = T_{off}$ is true, the inductor current becomes continuous, then the operation of switching regulator becomes continuous mode.

In the continuous mode, the deviation of the current is equal between on time and off time.

$$V_{IN} \times T_{on}/L = (V_{OUT} - V_{IN}) \times T_{off}/L \quad \text{Formula 4}$$

Further, the electric power, P_{AV} is equal to output electric power, $V_{OUT} \times I_{OUT}$, thus,

$$I_{OUT} = f_{OSC} \times V_{IN}^2 \times T_{ON}^2 / \{2 \times L \times (V_{OUT} - V_{IN})\} = V_{IN}^2 \times T_{ON} / (2 \times L \times V_{OUT}) \quad \text{Formula 5}$$

When I_{OUT} becomes more than formula 5, the current flows through the inductor, then the mode becomes

continuous. The continuous current through the inductor is described as I_{const} , then,

$$I_{OUT} = f_{OSC} \times V_{IN}^2 \times t_{ON}^2 / (2 \times L \times (V_{OUT} - V_{IN})) + V_{IN} \times I_{const} / V_{OUT} \quad \text{Formula 6}$$

In this moment, the peak current, I_{Lxmax} flowing through the inductor and the driver Tr. is described as follows:

$$I_{Lxmax} = I_{const} + V_{IN} \times T_{on} / L \quad \text{Formula 7}$$

With the formula 4,6, and I_{Lxmax} is,

$$I_{Lxmax} = V_{OUT} / V_{IN} \times I_{OUT} + V_{IN} \times T_{on} / (2 \times L) \quad \text{Formula 8}$$

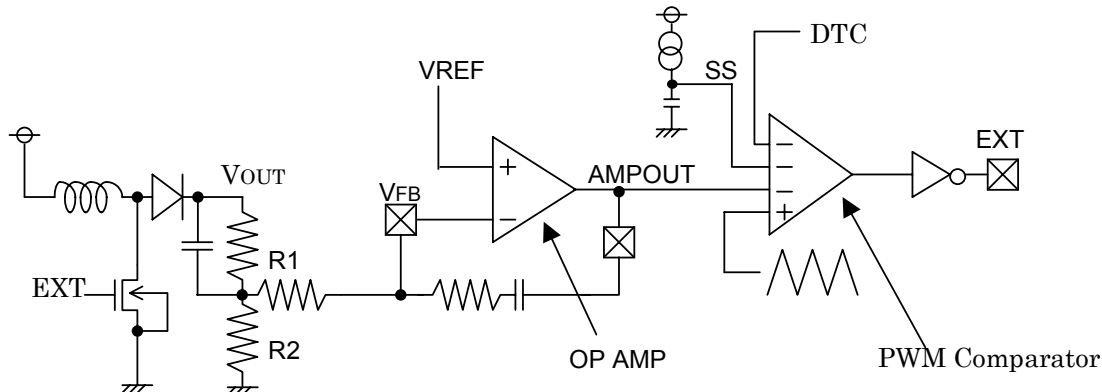
Therefore, peak current is more than I_{OUT} . Considering the value of I_{Lxmax} , the condition of input and output, and external components should be selected.

In the formula 7, peak current I_{Lxmax} at discontinuous mode can be calculated. Put $I_{const}=0$ in the formula.

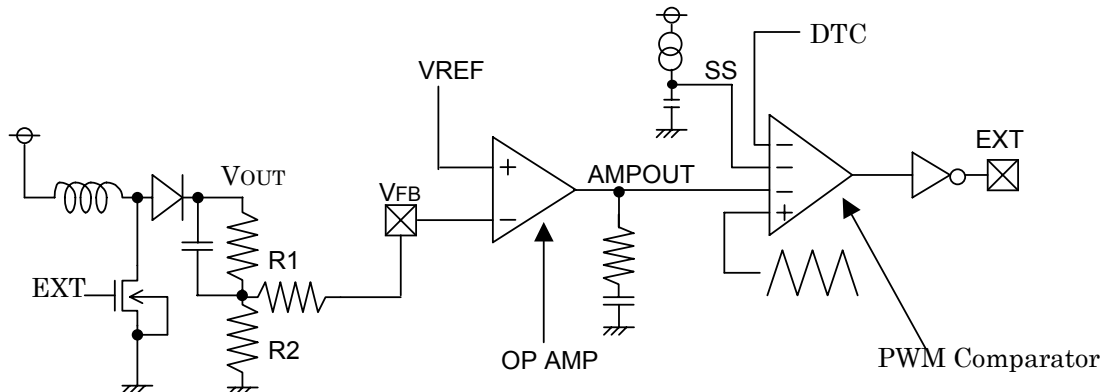
The explanation above is based on the ideal calculation, and the loss caused by L_x switch and external components is not included. The actual maximum output current is between 50% and 80% of the calculation. Especially, when the I_{Lx} is large, or V_{IN} is low, the loss of V_{IN} is generated with the on resistance of the switch. As for V_{OUT} , V_f (as much as 0.3V) of the diode should be considered.

■ TIMING CHART

- R1211X002A/R1211X002C



- R1211X002B/R1211X002D



<Soft-start Operation>

Soft-start operation is starting from power-on as follows:

(Step1)

The voltage level of SS is rising gradually by constant current circuit of the IC and a capacitor. V_{REF} level which is input to OP AMP is also gradually rising. V_{OUT} is rising up to input voltage level just after the power-on, therefore, V_{FB} voltage is rising up to the setting voltage with input voltage and the ration of R1 and R2. $AMPOUT$ is at "L", and switching does not start.

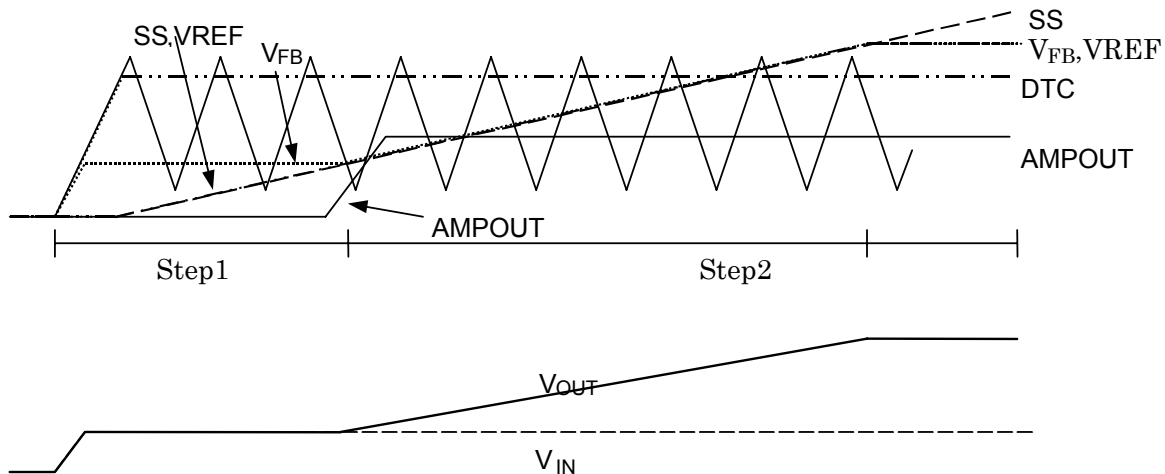
(Step2)

RIGOH

When the voltage level of SS becomes the setting voltage with the ration of R1 and R2 or more, switching operation starts. VREF level gradually increases together with SS level. VOUT is also rising with balancing VREF and VFB. Duty cycle depends on the lowest level among AMPOUT, SS, and DTC of the 4 input terminals in the PWM comparator.

(Step3)

When SS reaches 1V, soft-start operation finishes. VREF becomes constant voltage (=1V). Then the switching operation becomes normal mode.



<Latch Protection Operation>

The operation of Latch protection circuit is as follows: When AMPOUT becomes “H” and the IC detects maximum duty cycle, charge to an external capacitor, C2 of DELAY pin starts. And maximum duty cycle continues and the voltage of DELAY pin reaches delay voltage detector threshold, VDLY, outputs “L” to EXT pin and turns off the external power MOSFET.

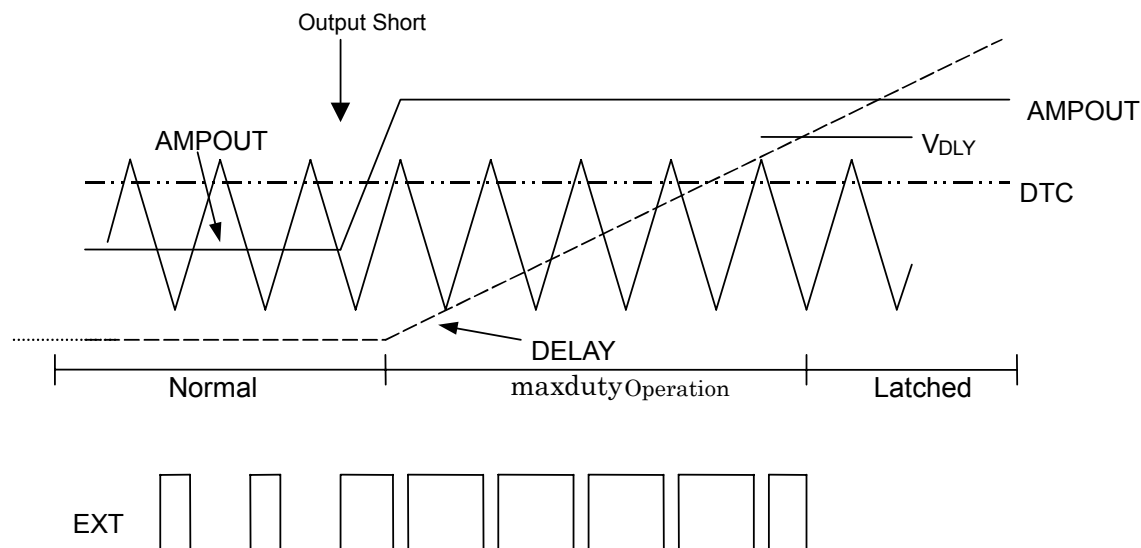
To release the latch protection operation, make the IC be standby mode with CE pin and make it active in terms of R1211X002B/D version. Otherwise, make supply voltage down to UVLO detector threshold or lower, and make it rise up to the normal input voltage.

During the soft-start time, if the duty cycle may be the maximum, protection circuit does not work and DELAY pin is fixed at GND level.

The delay time of latch protection can be calculated with C2, VDLY, and Delay Pin Charge Current, IDLY1, as in the next formula.

$$t = C2 \times VDLY / IDLY1$$

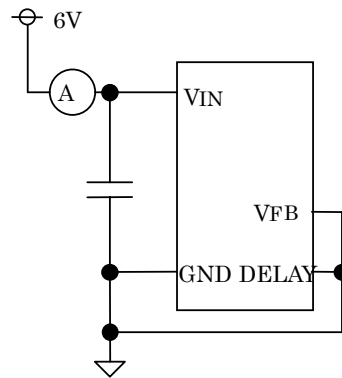
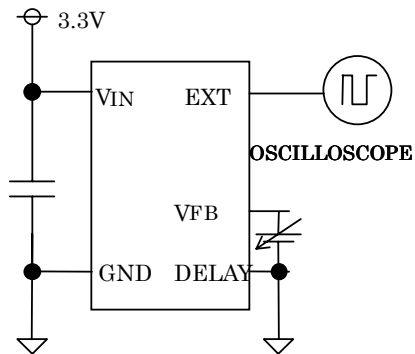
Once after the maximum duty is detected and released before delay time, charge to the capacitor is halt and delay pin outputs “L”.



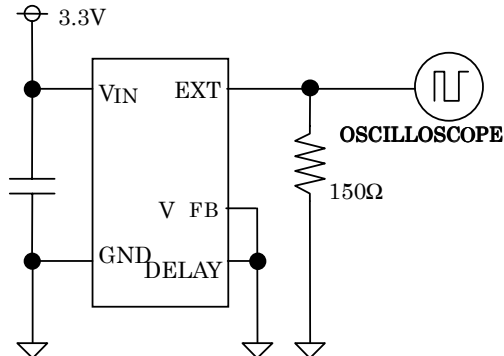
■ TEST CIRCUITS

- R1211X002A/R1211X002C

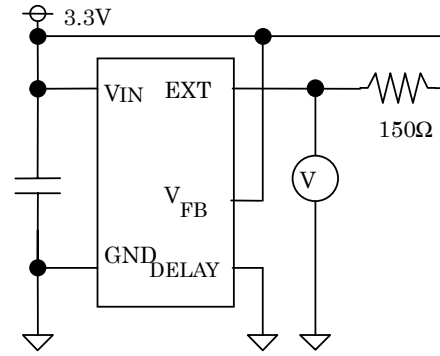
*Oscillator Frequency, Maximum Duty Cycle, VFB Voltage Test *Consumption Current Test



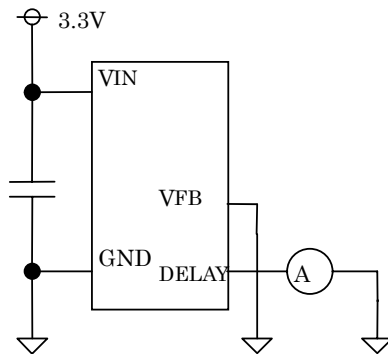
*EXT "H" ON Resistance



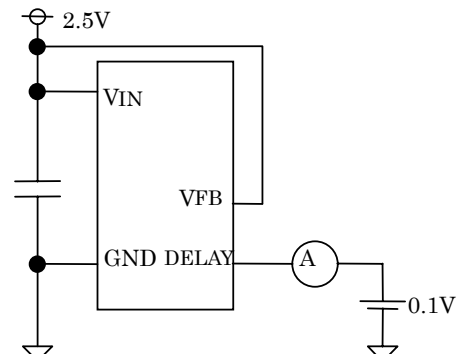
*EXT "L" ON Resistance



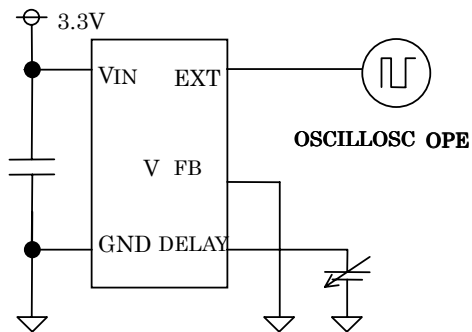
*DELAY Pin Charge Current



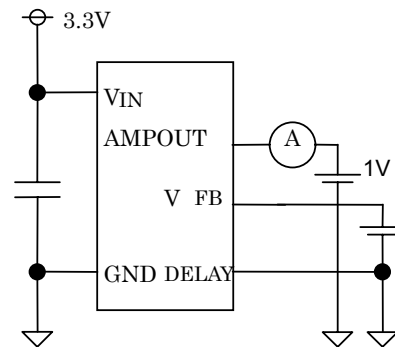
*DELAY Pin Discharge Current



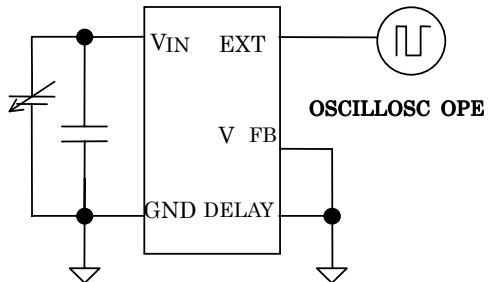
*DELAY Pin Detector Threshold Voltage Test



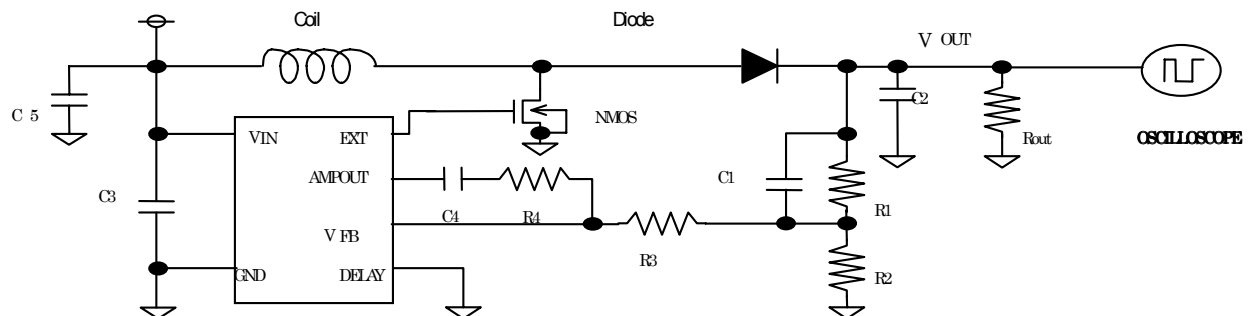
*AMP "H" Output Current/"L" Output Current Test



*UVLO Detector Threshold/Hysteresis Range Test



*Soft-start Time Test

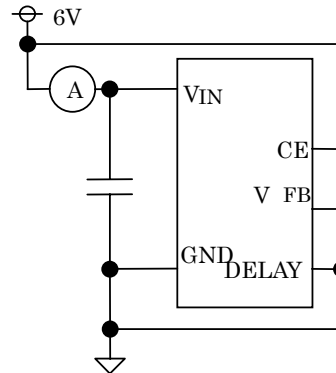
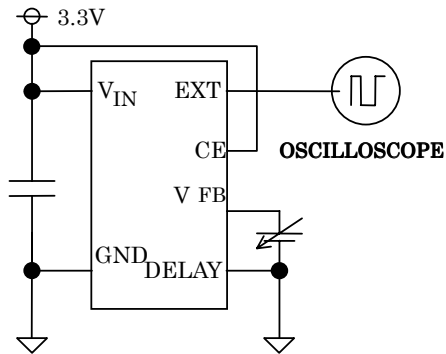


<Components>

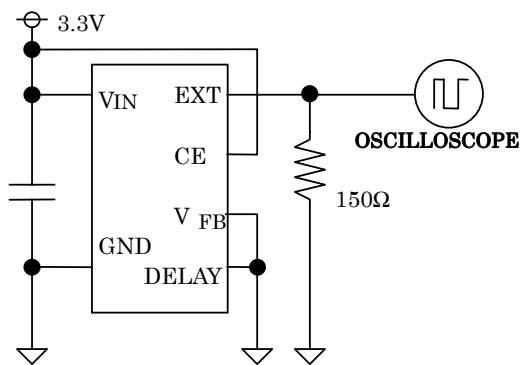
- Inductor (L) : 22 μ H (TDK LDR655312T-220)
- Diode (SD) : CRS02 (Toshiba)
- Capacitors : C1: 680pF(Ceramic), C2: 22 μ F (Tantalum)+2.2 μ F (Ceramic), C3: 68 μ F (Tantalum)+2.2 μ F (Ceramic), C4: 2200pF(Ceramic), C5: 22 μ F(Tantalum)
- NMOS Transistor : IRF7601 (International Rectifier)
- Resistors : R1: 90k Ω , R2:10k Ω , R3:30k Ω , R4:30k Ω , Rout:1k Ω /330 Ω

● R1211X002B/R1211X002D

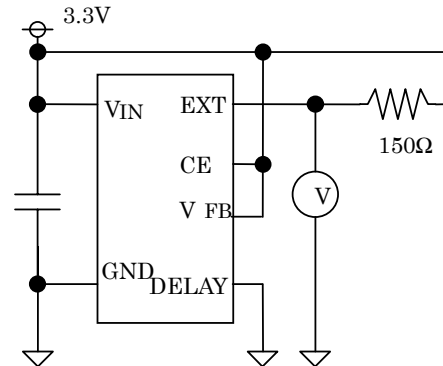
*Oscillator Frequency, Maximum Duty Cycle, VFB Voltage Test *Consumption Current Test



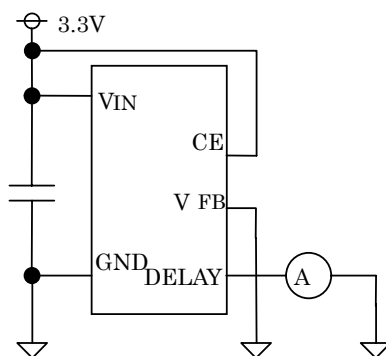
*EXT "H" ON Resistance



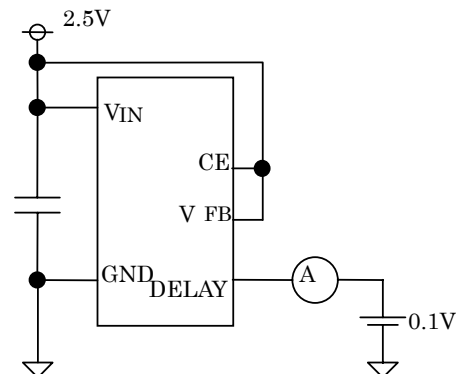
*EXT "L" ON Resistance



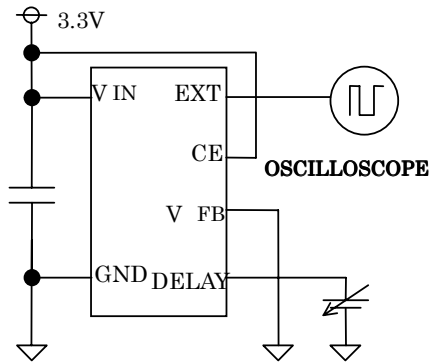
*DELAY Pin Charge Current



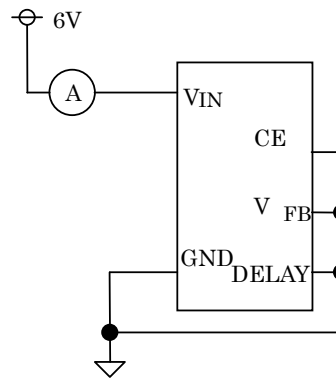
*DELAY Pin Discharge Current



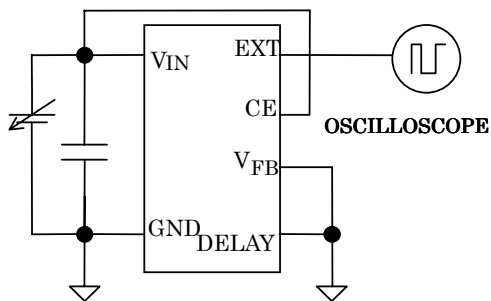
*DELAY Pin Detector Threshold Voltage Test



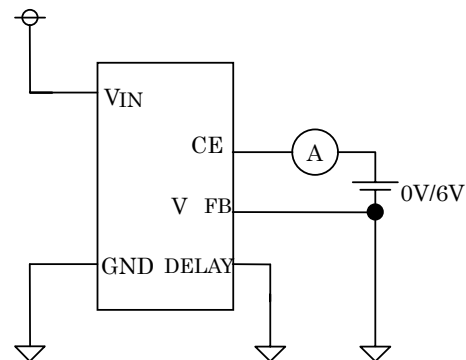
*Standby Current Test



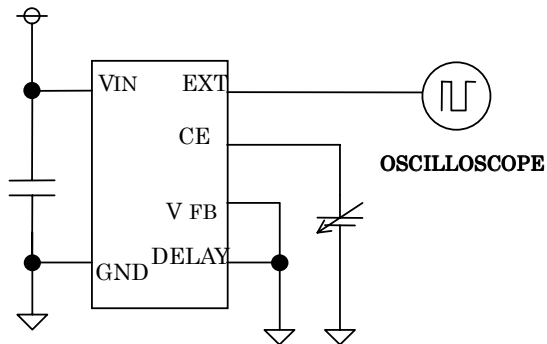
*UVLO Detector Threshold/Hysteresis Range Test



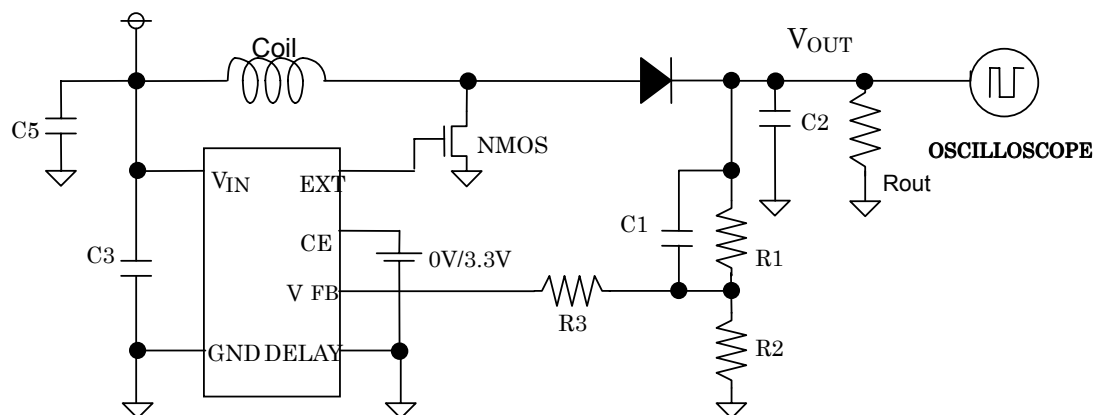
*CE "L" Input Current/"H" Input Current Test



*CE "L" Input Voltage/"H" Input Voltage Test



*Soft-start Time Test



<Components>

Inductor (L) : 22 μ H (TDK LDR655312T-220)

Diode (SD) : CRS02 (Toshiba)

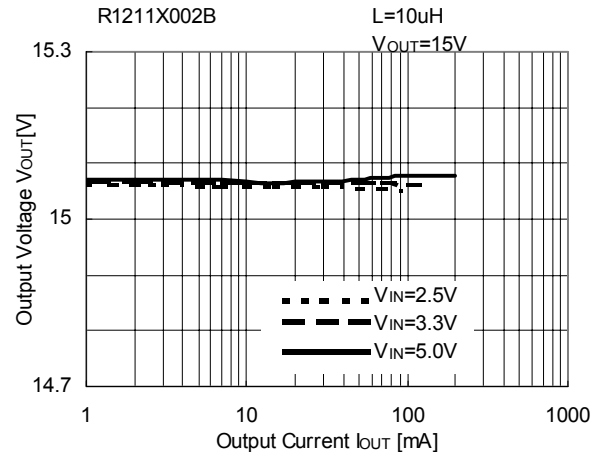
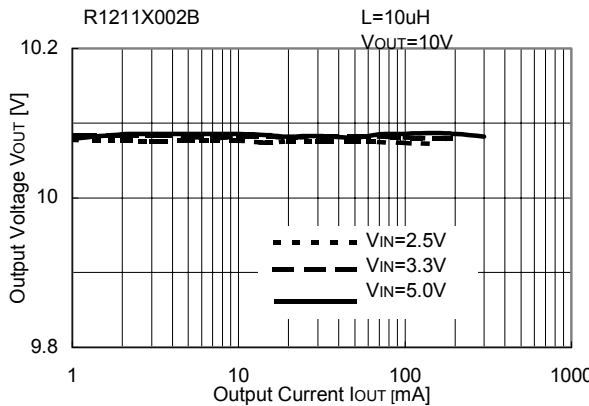
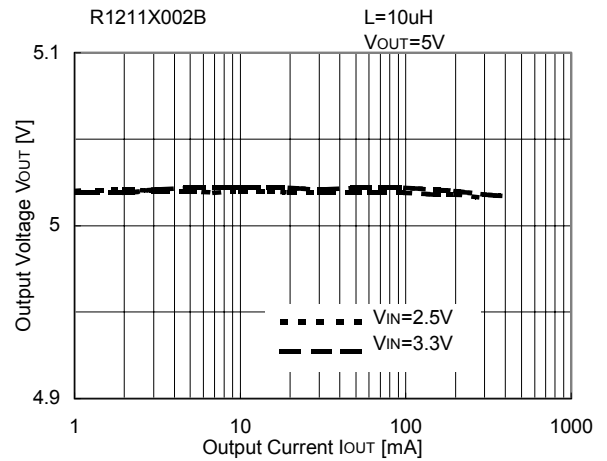
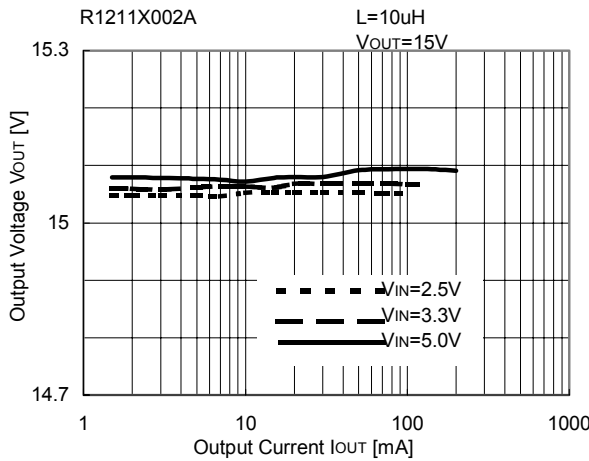
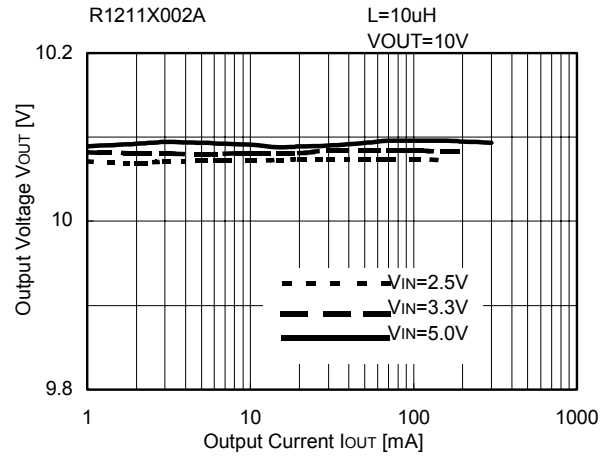
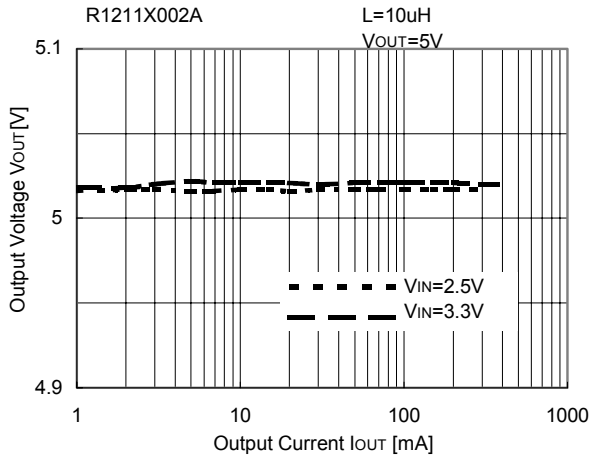
Capacitors C1: 680pF (Ceramic), C2: 22 μ F (Tantalum)+2.2 μ F (Ceramic),
C3: 68 μ F (Tantalum)+2.2 μ F (Ceramic), C5: 22 μ F (Tantalum)

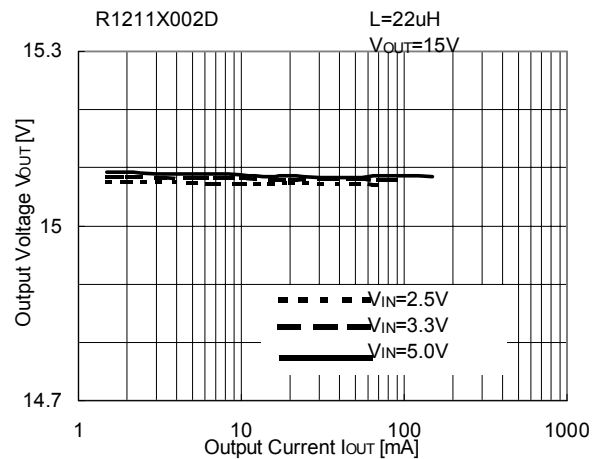
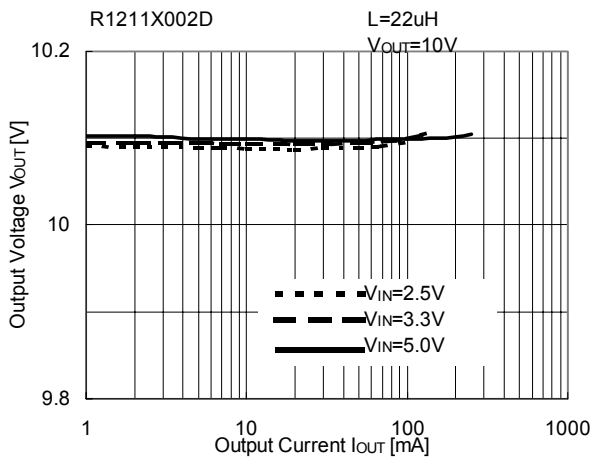
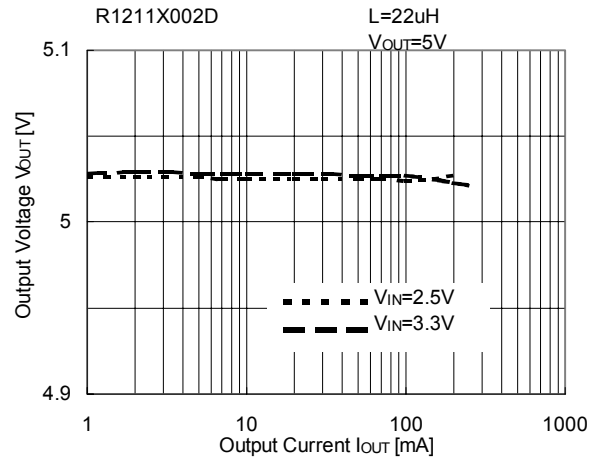
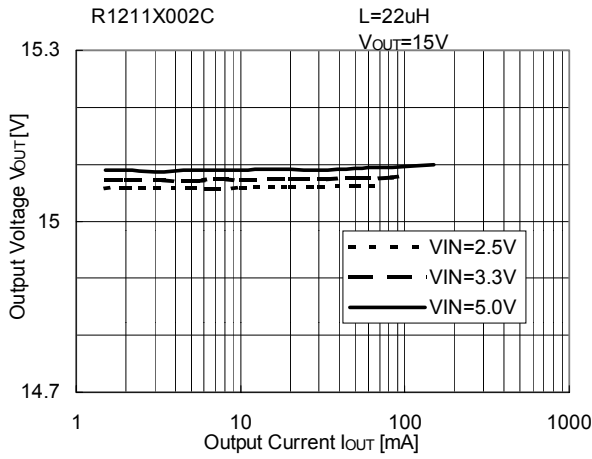
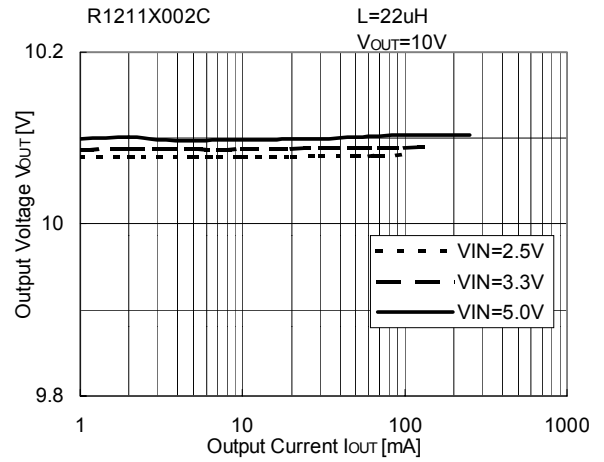
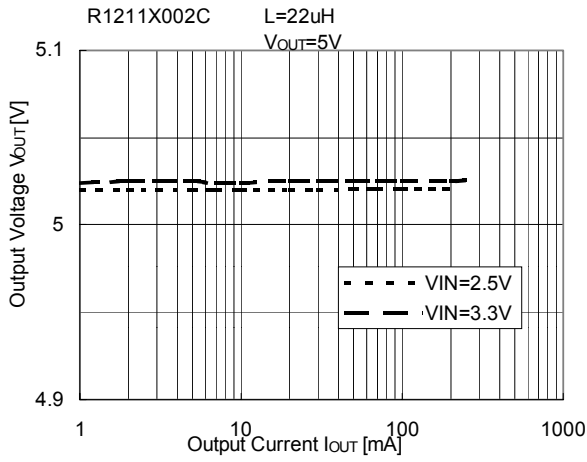
NMOS Transistor : IRF7601 (International Rectifier)

Resistors : R1: 90k Ω , R2: 10k Ω , R3: 30k Ω

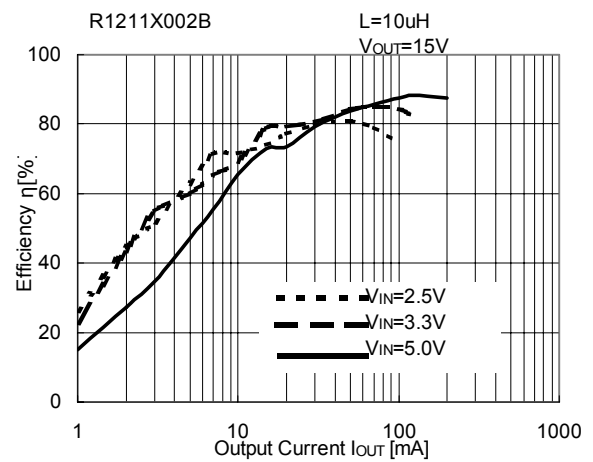
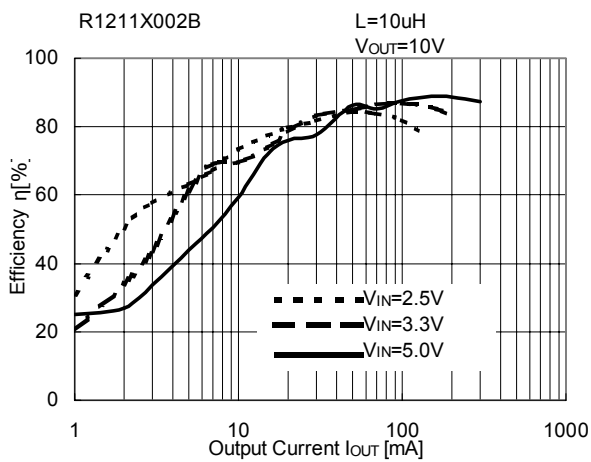
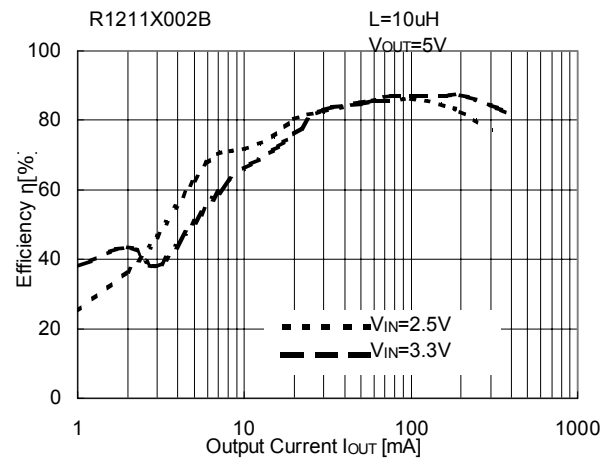
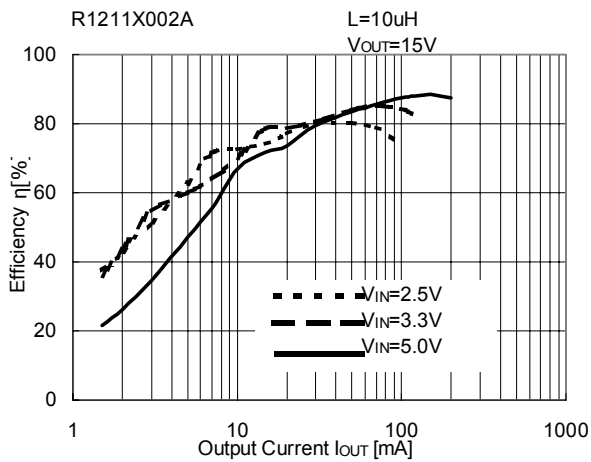
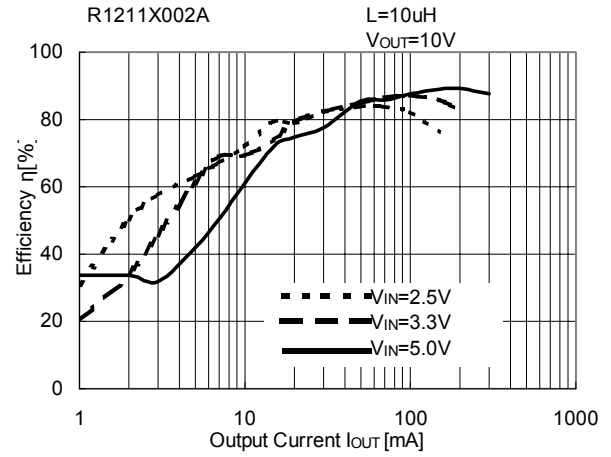
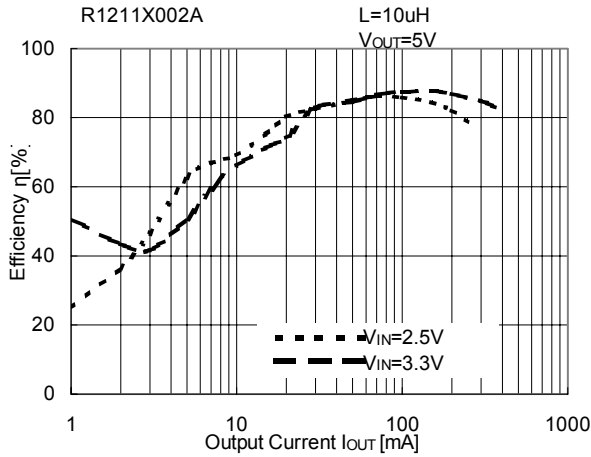
■ TYPICAL CHARACTERISTICS

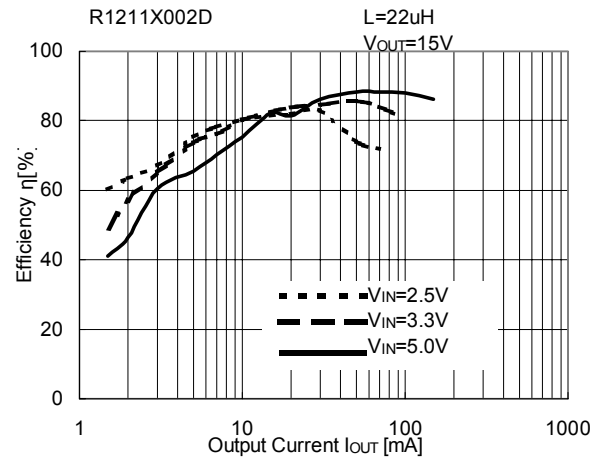
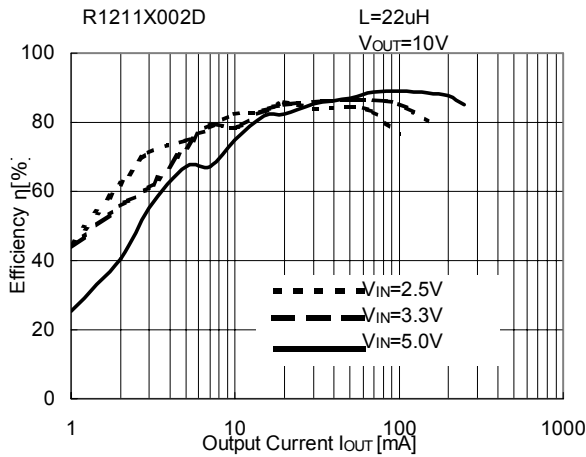
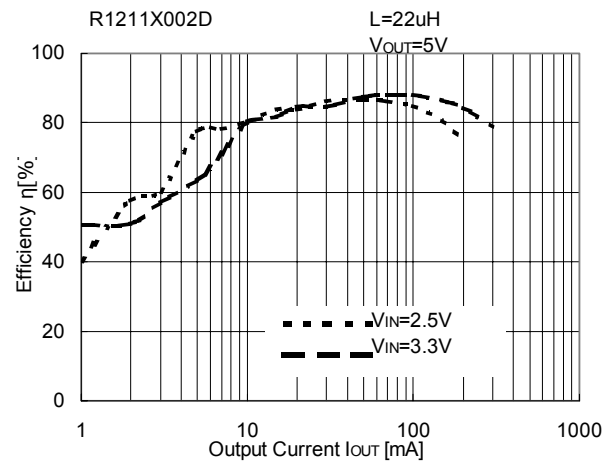
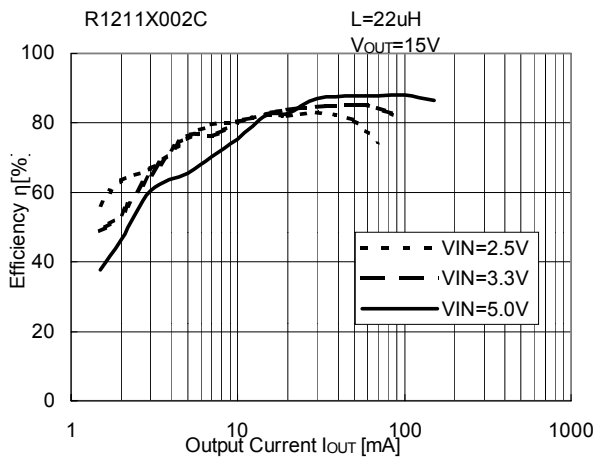
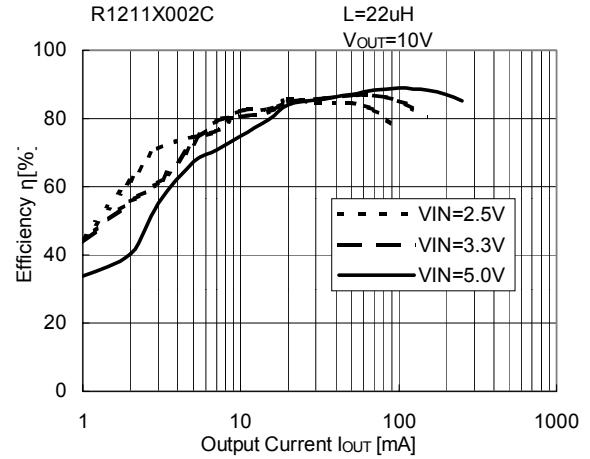
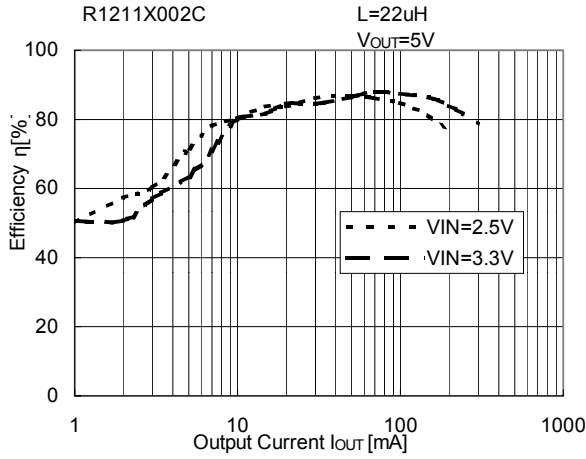
1) Output Voltage vs. Output Current



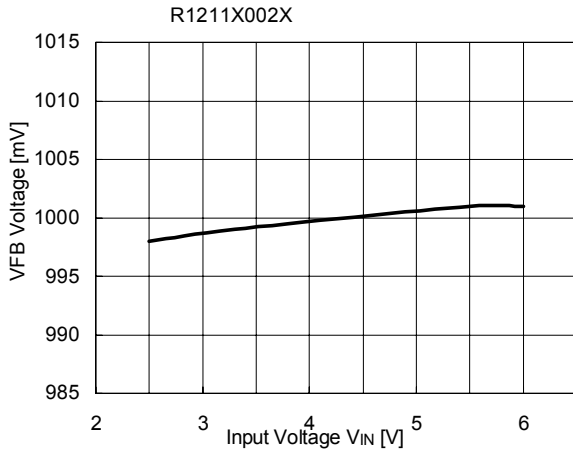


2) Efficiency vs. Output Current

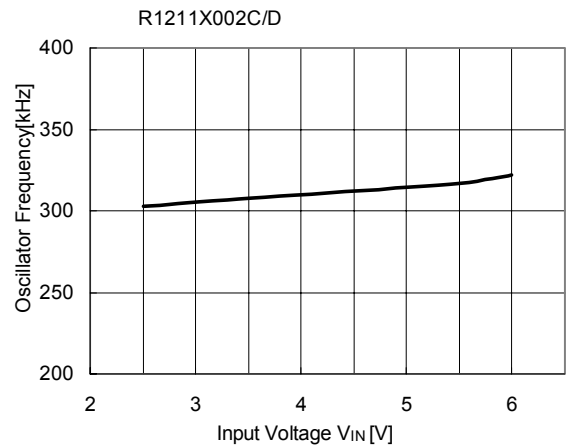
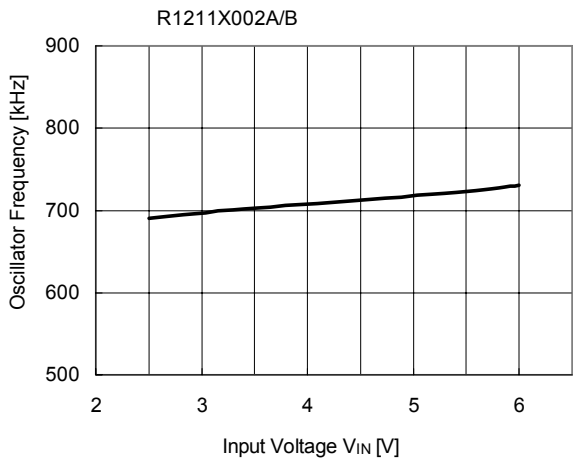




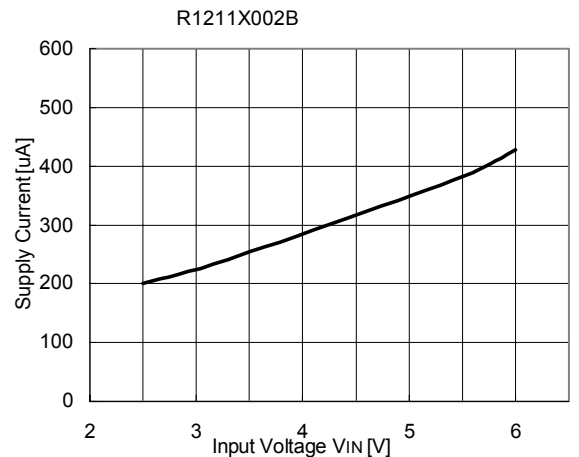
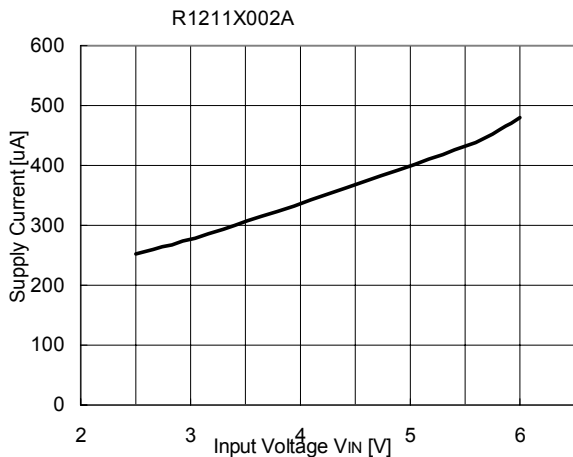
3) VFB Voltage vs. Input Voltage (T_{opt} =25°C)

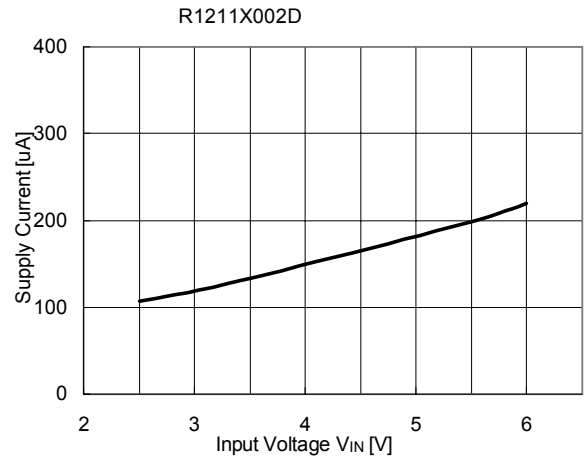
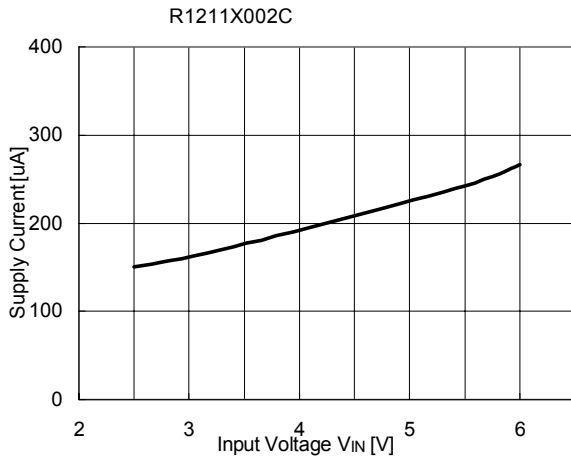


4) Oscillator Frequency vs. Input Voltage (T_{opt}=25°C)

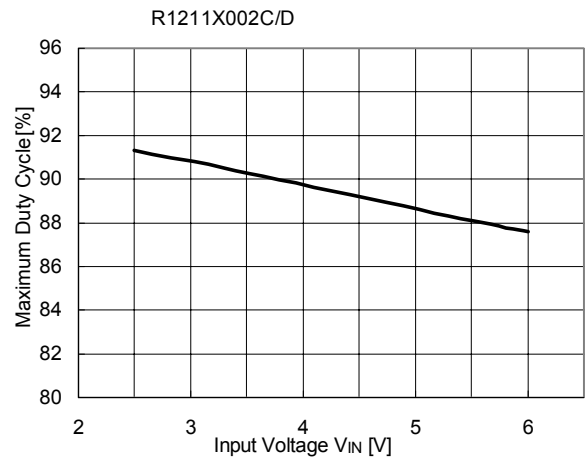
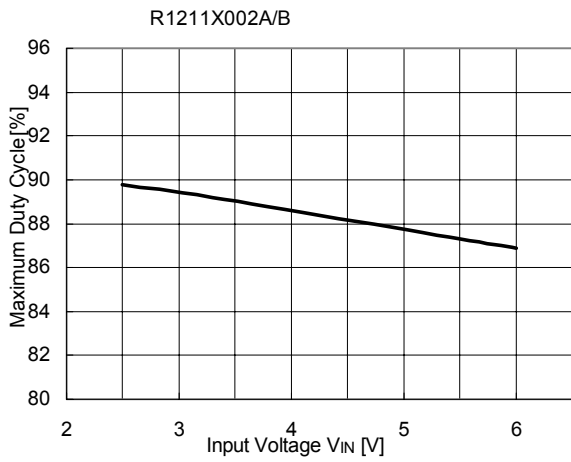


5) Supply Current vs. Input Voltage (T_{opt}=25°C)

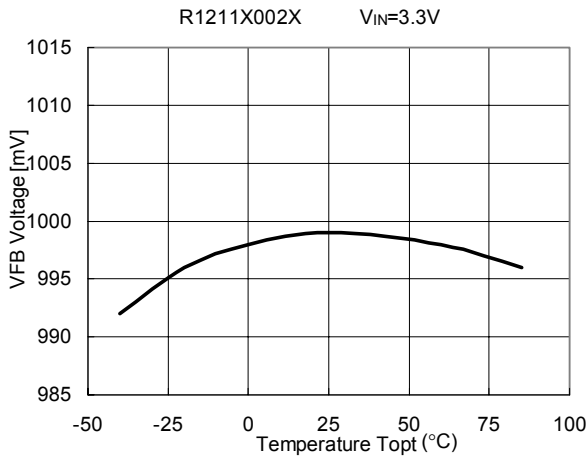




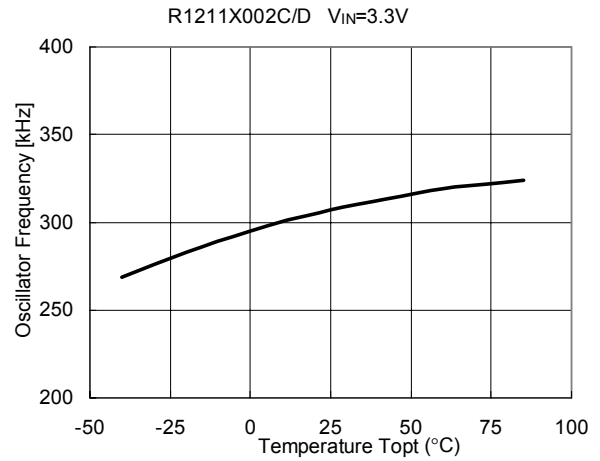
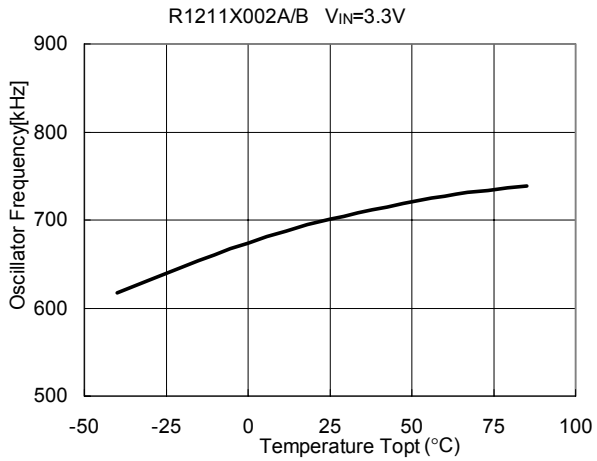
6) Maximum Duty Cycle vs. Input Voltage (Topt=25°C)



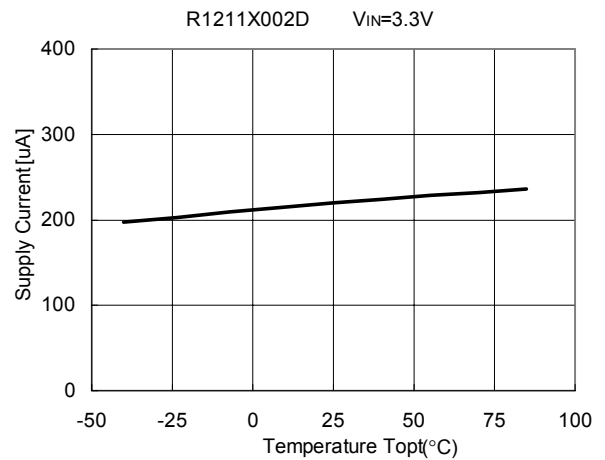
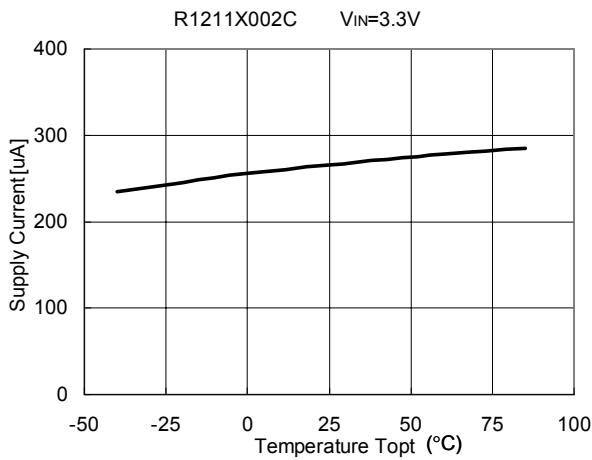
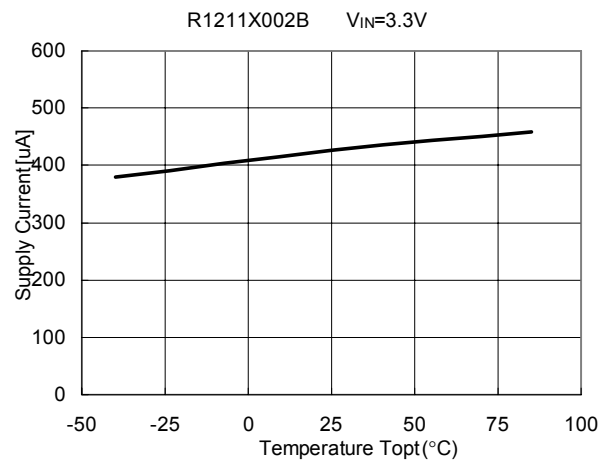
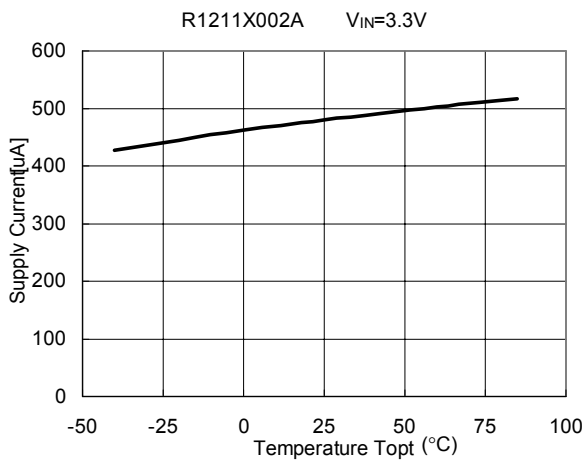
7) VFB Voltage vs. Temperature



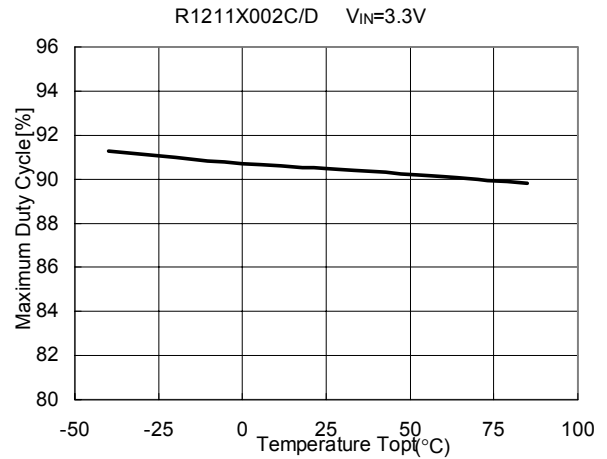
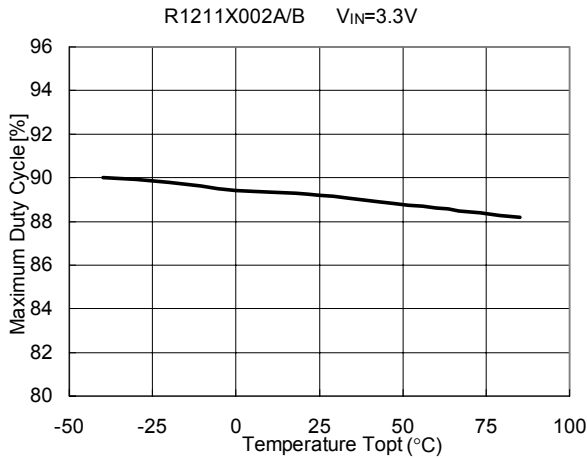
8) Oscillator Frequency vs. Temperature



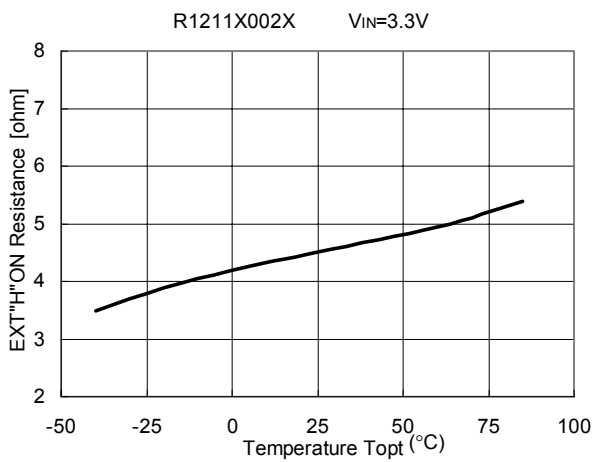
9) Supply Current vs. Temperature



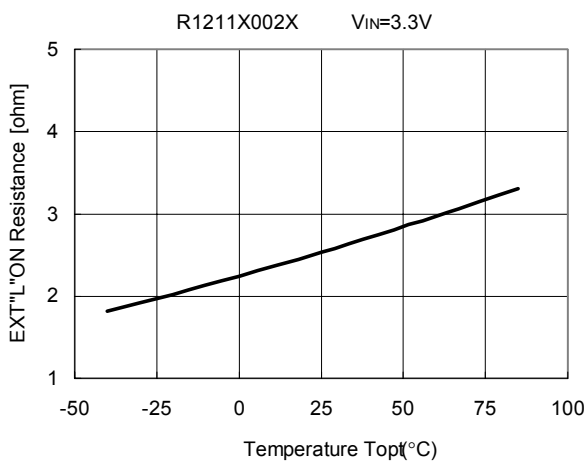
10) Maximum Duty Cycle vs. Temperature



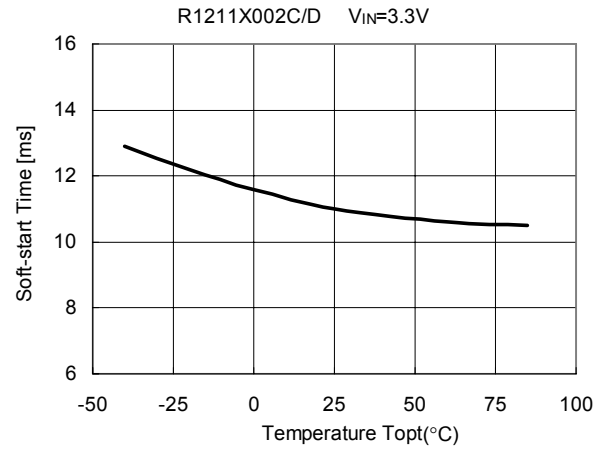
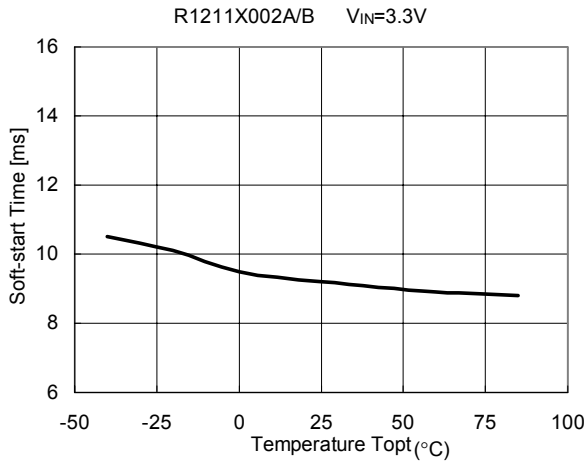
11) EXT"H" Output Current vs. Temperature



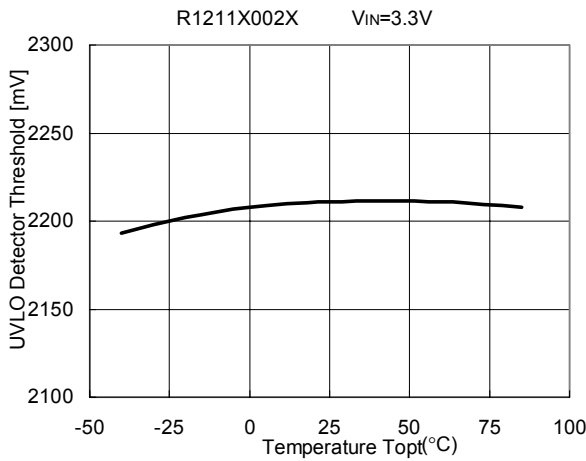
12) EXT"L" Output Current vs. Temperature



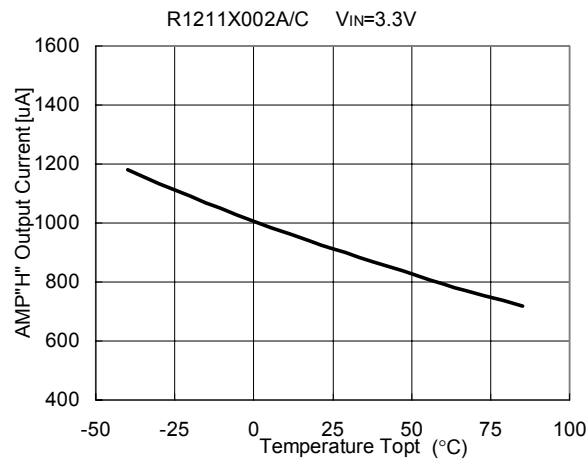
13) Soft-start Time vs. Temperature



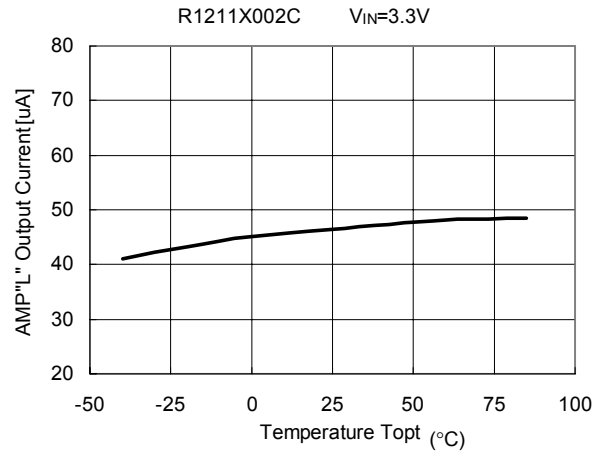
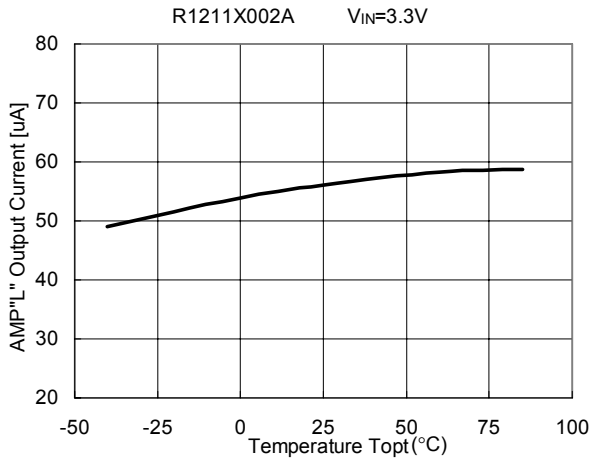
14) UVLO Detector Threshold vs. Temperature



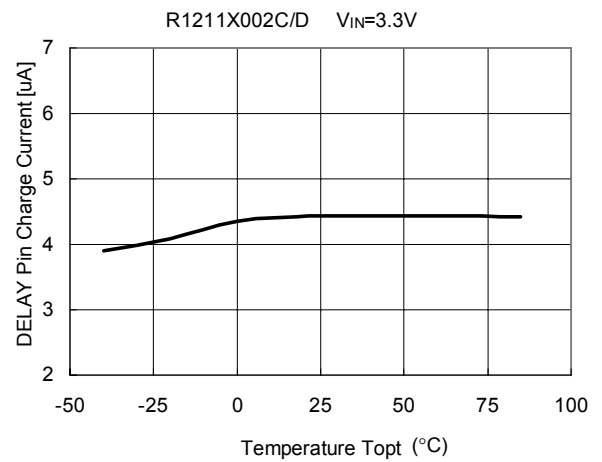
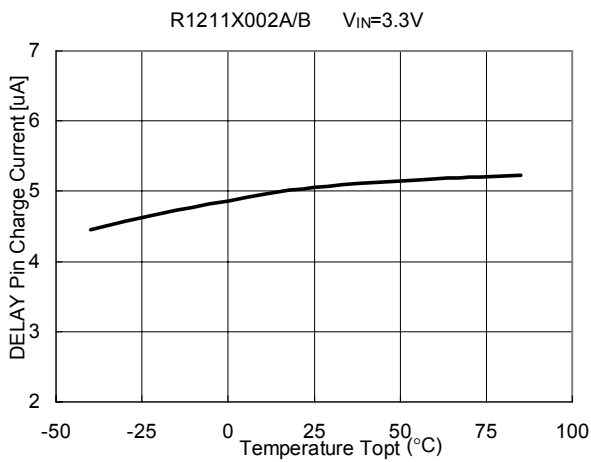
15) AMP "H" Output Current vs. Temperature



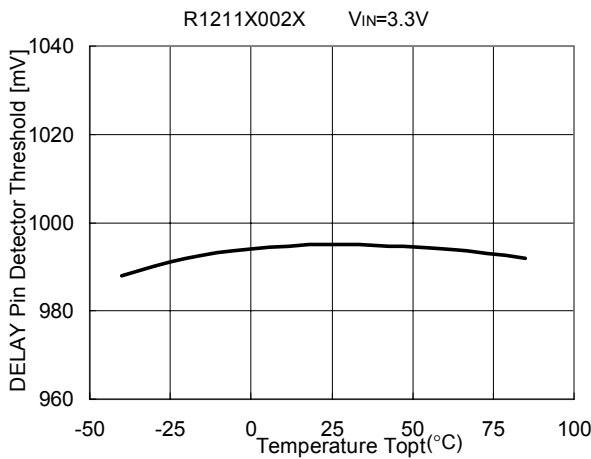
16) AMP "L" Output Current vs. Temperature



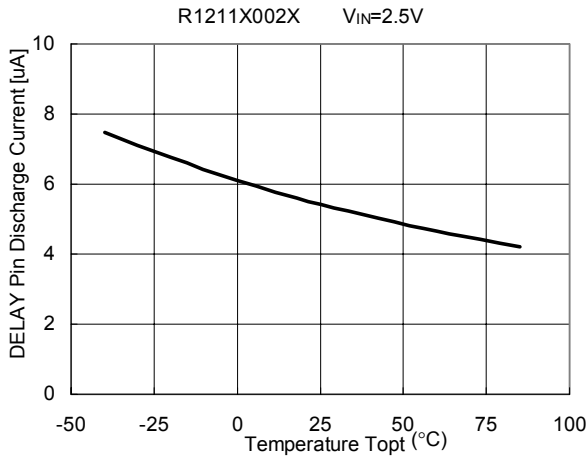
17) DELAY Pin Charge Current vs. Temperature



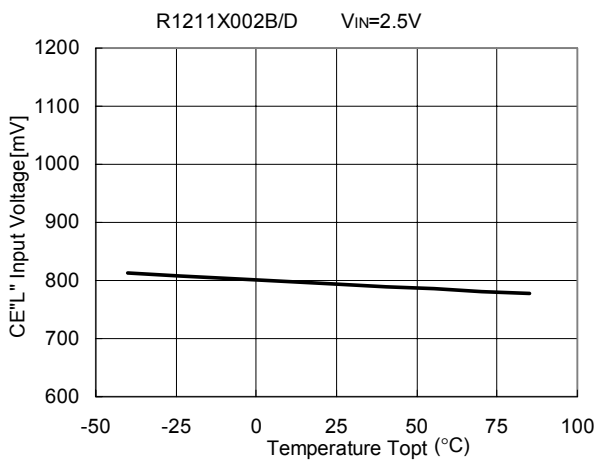
18) DELAY Pin Detector Threshold vs. Temperature



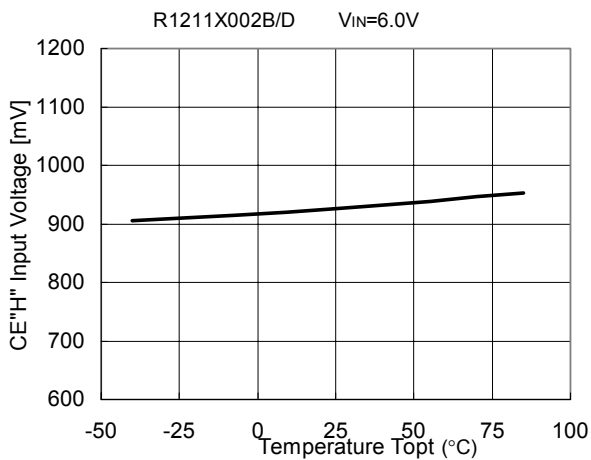
19) DELAY Pin Discharge Current vs. Temperature



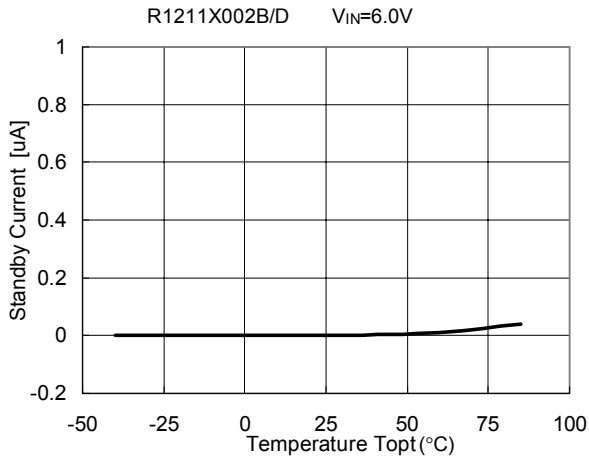
20) CE "L" Input Voltage vs. Temperature



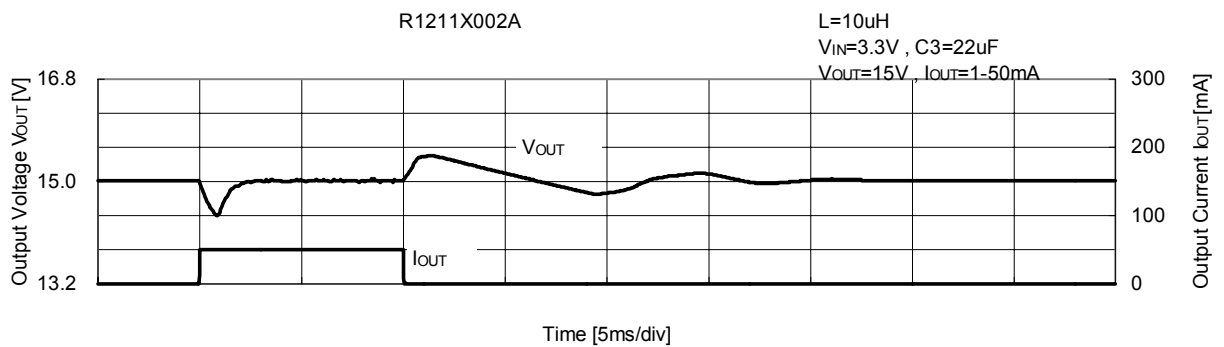
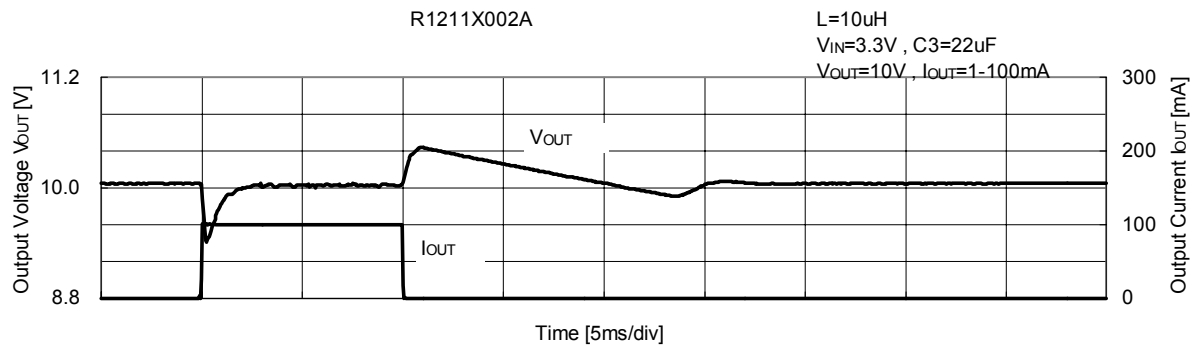
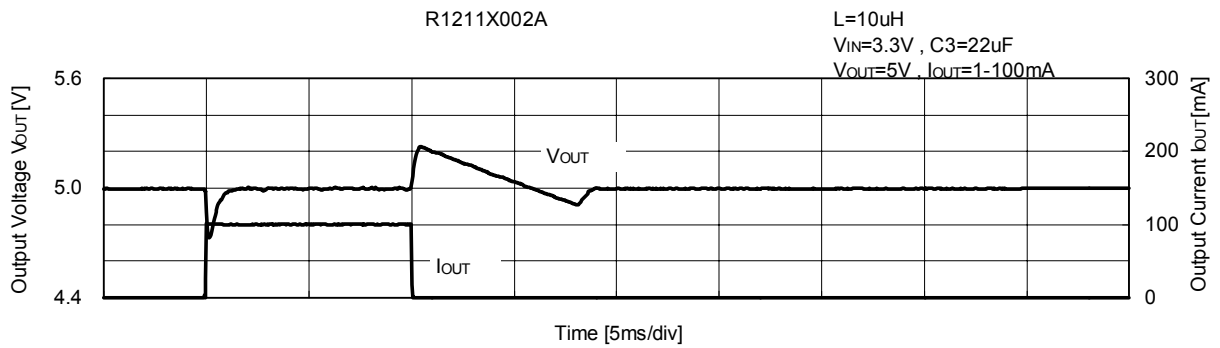
21) CE "H" Input Voltage vs. Temperature

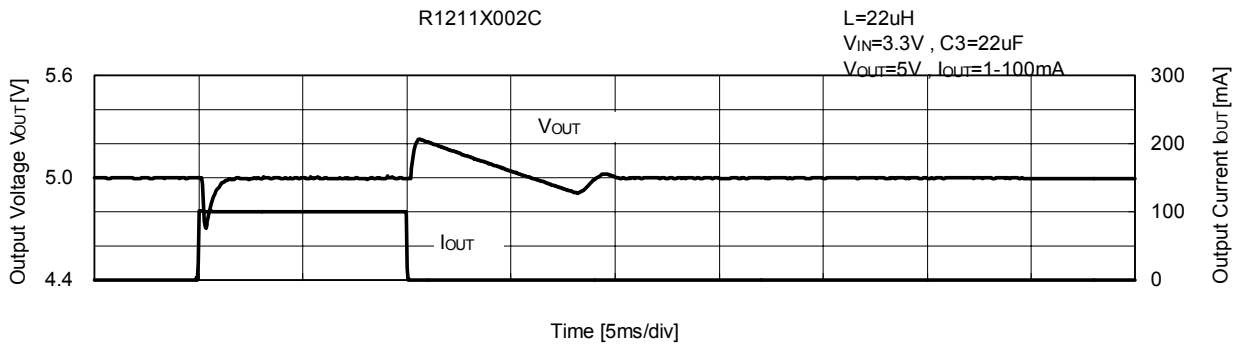
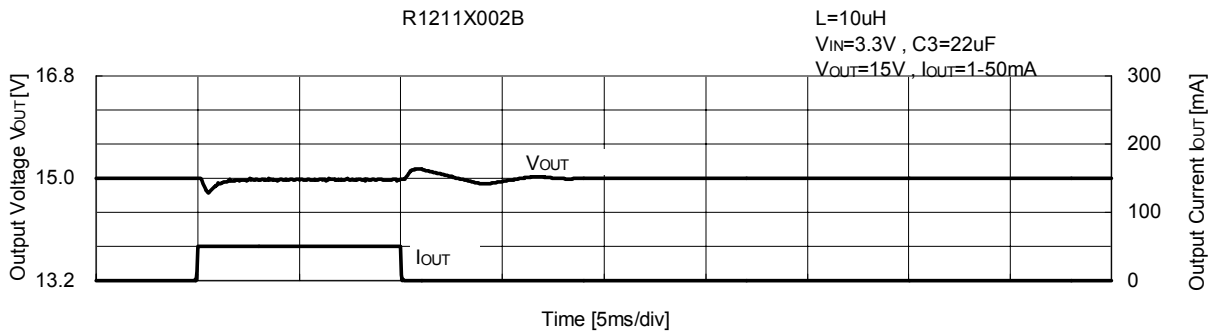
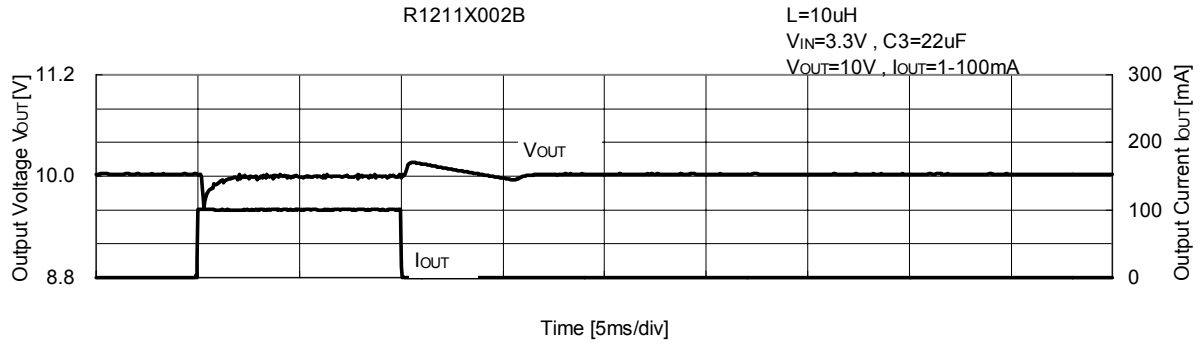
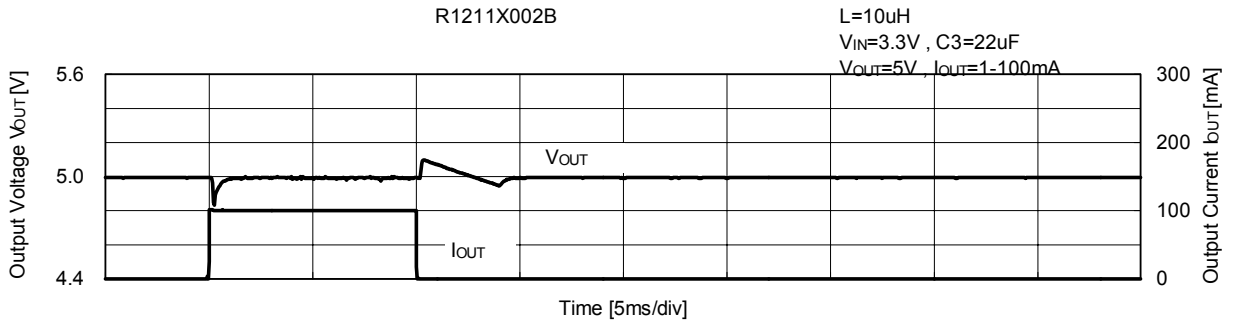


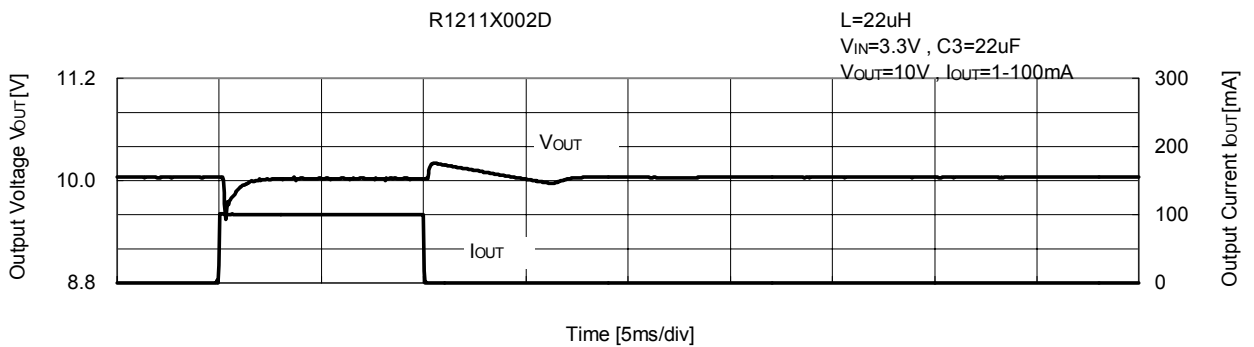
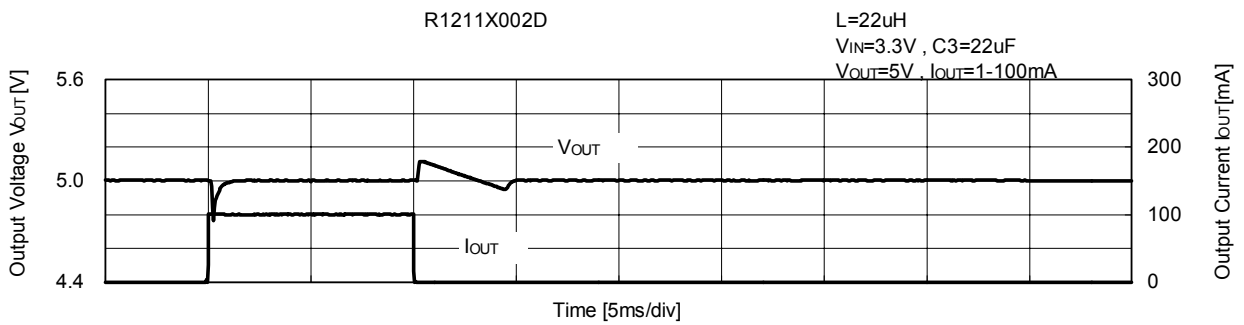
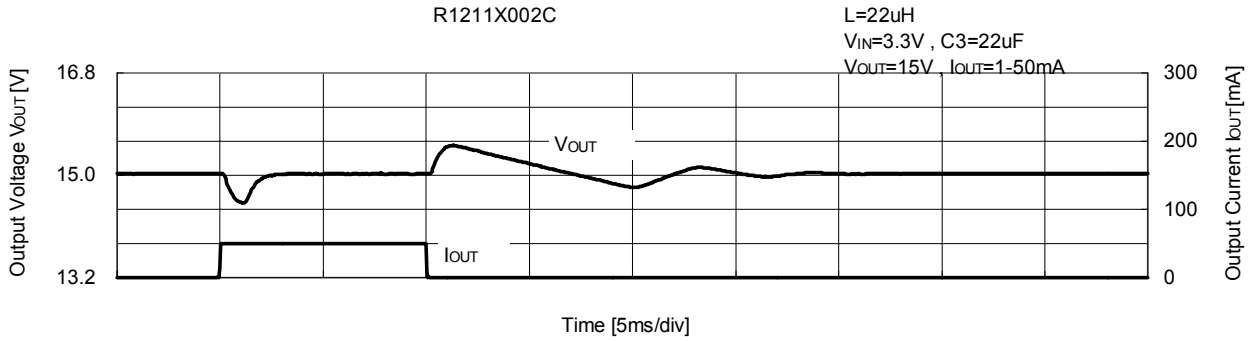
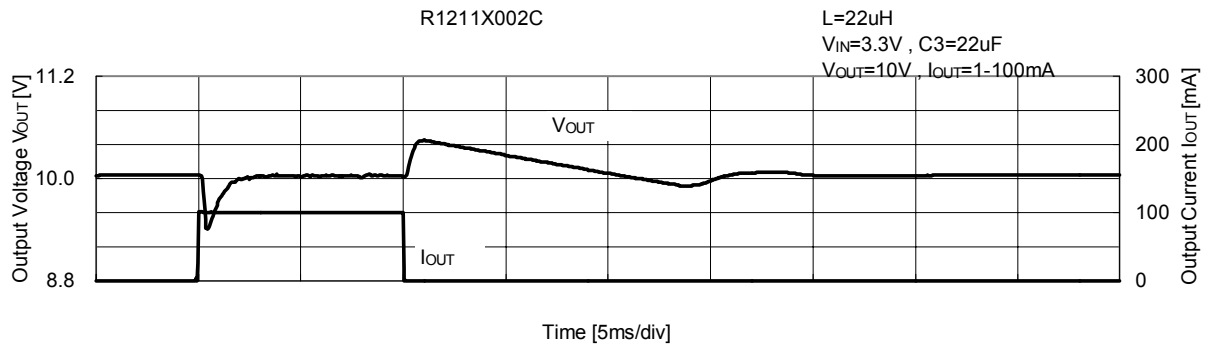
22) Standby Current vs. Temperature

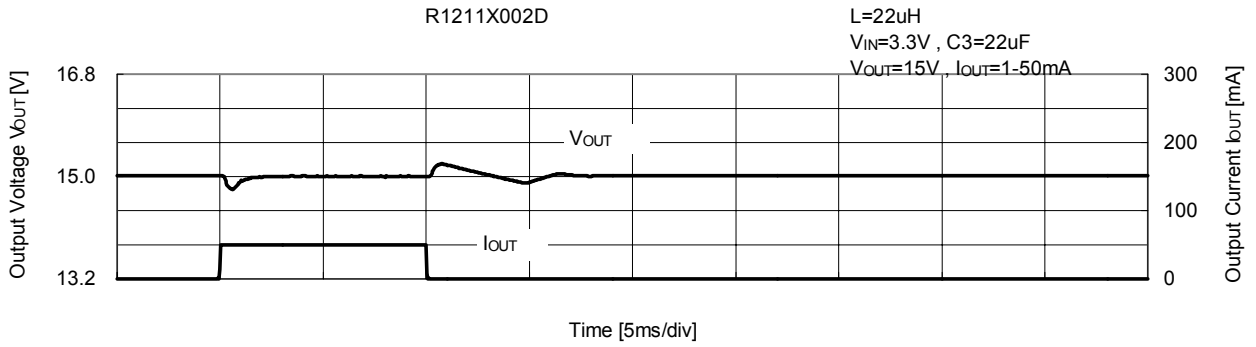


23) Load Transient Response

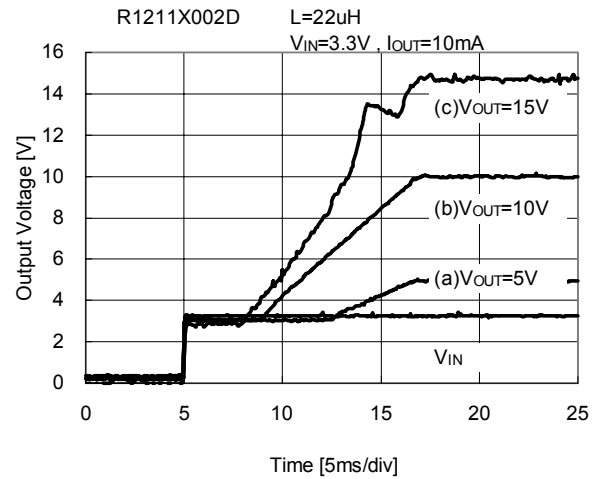
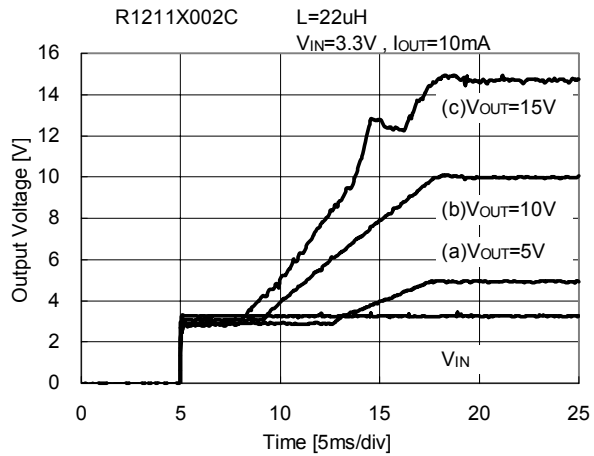
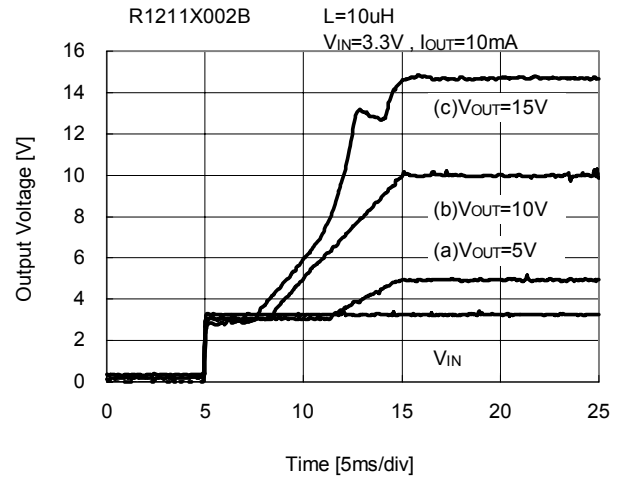
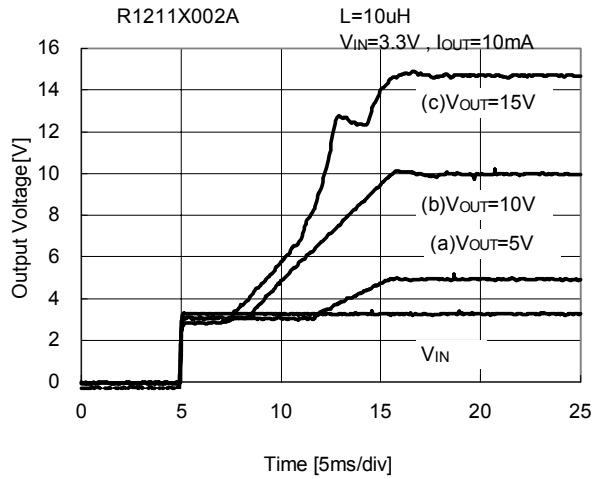








24) Power-on Response



25) Turn-on speed with CE pin

