

EMIF02-MIC01F2

2-line EMI filter including ESD protection

Main application

Mobile phones (differential microphone filtering and ESD protection).

Description

The EMIF02-MIC01F2 is a highly integrated device designed to suppress EMI / RFI noise for microphone line filtering.

The EMIF02-MIC01F2 Flip-chip packaging means the package size is equal to the die size. This is why the EMIF02-MIC01F2 is a very small device.

Additionally, the filter includes an ESD protection circuit to prevent the protected device from destruction when subjected to ESD surges up to 15 kV.

Benefits

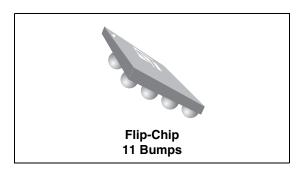
- 2-line symetrical low-pass filter
- Lead-free package
- High-density capacitor
- High-efficiency EMI filtering
- Very small PCB footprint: 1.42 mm x 1.92 mm
- Very thin package: 0.65 mm
- High-efficiency ESD suppression (IEC 61000-4-2 level 4)
- High reliability offered by monolithic integration

Complies with the standards:

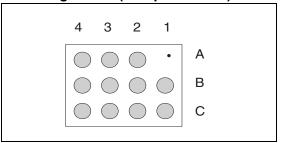
IEC 61000-4-2

on input and output pins 15 kV (air discharge) 8 kV (contact discharge)

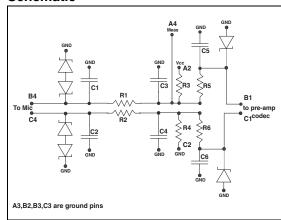
TM: IPAD is a trademark of STMicroelectronics.



Pin configuration (Bump side view)



Schematic



Order code

Part number	Marking		
EMIF02-MIC01F2	GB		

October 2006 Rev 5 1/6

1 Electrical characteristics

Table 1. Absolute maximum ratings ($T_{AMB} = 25^{\circ} C$)

Symbol	Parameter	Value	Unit
VPP	ESD IEC 61000-4-2, input and output pins - air discharge ESD IEC 61000-4-2, in put and output pins - contact discharge	15 8	kV
Tj	Junction temperature	125	° C
Тор	Operating temperature range	-40 to +85	° C
Tstg	Storage temperature range	-55 to +150	°C

Table 2. Electrical characteristics ($T_{amb} = 25^{\circ} C$)

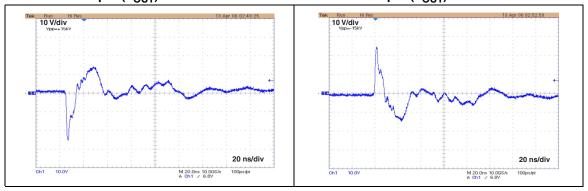
Symbol	Parameter		· · · · ·	1
V _{BR}	Breakdown voltage			
I _{RM}	Leakage current @ V _{RM}			
V_{RM}	Stand-off voltage	V _{CL}	V _{BR} V _{RM}	\(\begin{align*}
V _{CL}	Clamping voltage			I _R
Rd	Dynamic impedance		1.46	
I _{PP}	Peak pulse current		slope: 1 / Rd	I _{PP}

Symbol	Test condition	Min	Тур	Max	Unit
V _{BR}	I _R = 1 mA per line	14		18	V
I _{RM}	V _{RM} = 3 V per line			0.5	μΑ
C1, C2, C3 C4, C5, C6	V _{LINE} = 0 V, V _{OSC} = 30 mV, F = 1 MHz	0.8	1.0	1.2	nF
R1, R2	Tolerance ±5%		50		Ω
R3, R4	Tolerance ±5%		1.00		kΩ
R5, R6	Tolerance ±5%		2.20		kΩ

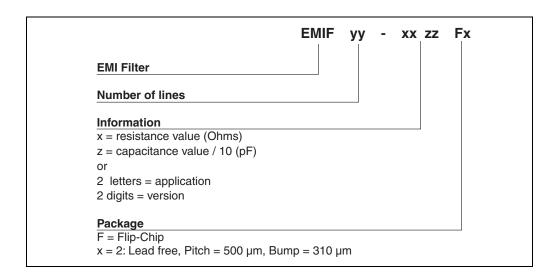
Figure 2. Figure 1. Filter response Analog crosstalk 10 10 -10 -10 -20 -20 -30 -30 -40 -40 -50 -50 -60 -60 -70 -70 -80 -80 F (Hz) -90 -90 Line C4/C1 -100 -100 1.0 G 100 k 1.0 M 10 M 100 M 1.0 G 10 G 100 k 10.0 M 100 M

Figure 3. ESD response to IEC 61000-4-2 (+15 kV air discharge) on output (V_{OUT})

Figure 4. ESD response to IEC 61000-4-2 (-15 kV air discharge) on output (V_{OUT})

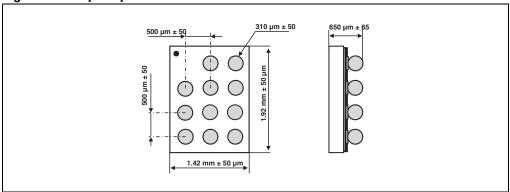


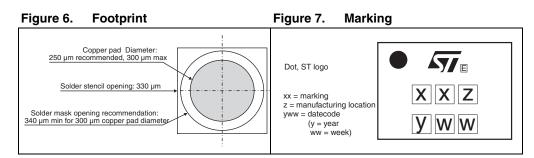
2 Ordering information scheme



3 Package mechanical data

Figure 5. Flip-Chip dimensions





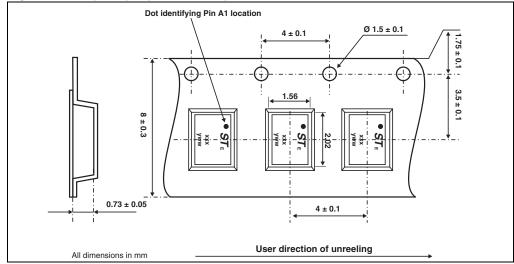


Figure 8. Flip-Chip tape and reel specification

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Note:

More packing information is available in the applications note: AN1235: "Flip-Chip: package description and recommendations for use" AN 1751: "EMI filters: Recomendations and measurements"

4 Ordering information

Ordering code	Marking	Package	Weight	Base qty	Delivery mode
EMIF02-MIC01F2	GB	Flip-chip	3.8 mg	5000	Tape and reel (7")

5 Revision history

Date	Revision	Changes	
Sep-2004	3	Previous issue	
09-Feb-2006	4	Added ECOPACK statement. Updated graphics to current standards.	
06-Oct-2006	5	Reformatted to current standards. updated characteristic curves, removed Aplac information and updated tape and reel pocket dimensions	

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