

SANYO Semiconductors DATA SHEET

LA5311M-

_ Monolithic Linear IC

Variable Divided Voltage Generator for LCD

Overview

The LA5311M is a variable divided voltage generator IC for multiple drive of LCD matrix.

Features

- · Power supply for variable bias LCD drive.
- 4 operational amplifiers deliver 4 voltage outputs.
- · Low current drain (1.0mA max).
- · Miniflat package.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	VCC max		30	٧
Output current	lout		5	mA
Allowable power dissipation	Pd max		300	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage range	ACC ob	VCC-V ₁ >1.0V	11 to 28	V
Recommended output current	I ₁		0 to 3	mA
	l ₂ , l ₃		-3 to +3	mA
	14		-3 to 0	mA

Operating Characteristics at Ta=25°C, VCC=20V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Current drain	Icc	V_{CC} =0V, GND=-20V= V_5 , External R _A =100k Ω			1.0	mA
Output ratio1	R1	V ₂ / V ₁ , V _{CC} =0V, GND=-20V=V ₅ , External R _A =100kΩ	1.94		2.06	-
Output ratio2	R2	V ₅ -V ₃ / V ₅ -V ₄ , V _{CC} =0V, GND=-20V=V ₅ , External R _A =100kΩ	1.94		2.06	-

Continued on next page.

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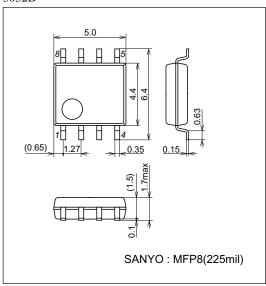
LA5311M

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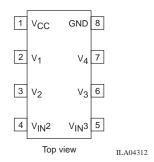
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Output ratio3	R3	V_2 / V_5 - V_3 , V_{CC} =0V, GND=-20V= V_5 , External RA=100k Ω	0.97		1.03	-
Output ratio4	R4	V ₁ / V ₅ -V ₄ , V _{CC} =0V, GND=-20V=V ₅ , External R _A =100kΩ	0.97		1.03	-
Load reguration	ΔV ₁	+100μA <i<sub>OUT<+3mA</i<sub>			20	mV
	ΔV2	+100μA <i<sub>OUT<+3mA</i<sub>			20	mV
	ΔV3	+100μA <i<sub>OUT<+3mA</i<sub>			20	mV
	-∆V2	-3mA <i<sub>OUT<-100μA</i<sub>			20	mV
	-∆V3	-3mA <iout<-100μa< td=""><td></td><td></td><td>20</td><td>mV</td></iout<-100μa<>			20	mV
	-ΔV ₄	-3mA <i<sub>OUT<-100μA</i<sub>			20	mV
R ₁ +R ₂	R	0.5V applied across R ₁ +R ₂	33	40	47	kΩ

Package Dimensions

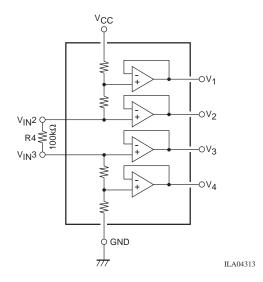
unit : mm 3032D



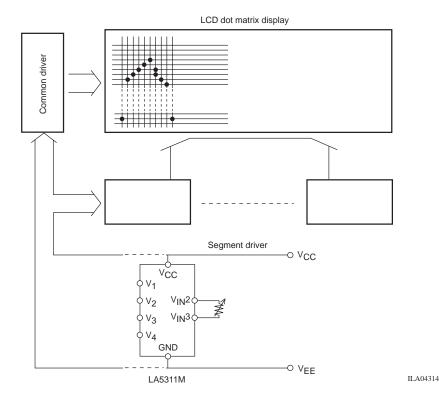
Pin Assignment



Equivalent Circuit



Sample Application Circuit



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