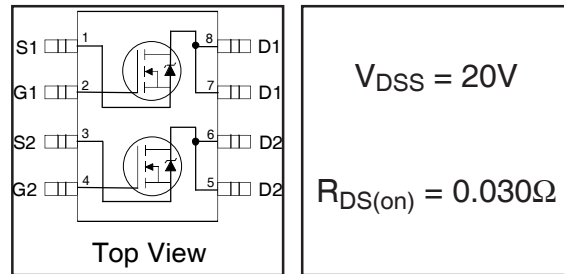


IRF7530PbF

HEXFET® Power MOSFET

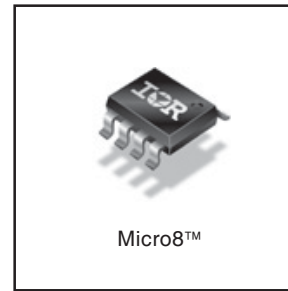
- Trench Technology
- Ultra Low On-Resistance
- Dual N-Channel MOSFET
- Very Small SOIC Package
- Low Profile (<1.1mm)
- Available in Tape & Reel
- Lead-Free



Description

New trench HEXFET® power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The new Micro8™ package has half the footprint area of the standard SO-8. This makes the Micro8 an ideal device for applications where printed circuit board space is at a premium. The low profile (<1.1 mm) of the Micro8 will allow it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards.



Absolute Maximum Ratings

| | Parameter | Max. | Units |
|--------------------------|---|--------------|-------|
| V_{DS} | Drain- Source Voltage | 20 | V |
| $I_D @ T_A = 25^\circ C$ | Continuous Drain Current, $V_{GS} @ 4.5V$ | 5.4 | A |
| $I_D @ T_A = 70^\circ C$ | Continuous Drain Current, $V_{GS} @ 4.5V$ | 4.3 | |
| I_{DM} | Pulsed Drain Current ① | 40 | |
| $P_D @ T_A = 25^\circ C$ | Power Dissipation | 1.3 | W |
| $P_D @ T_A = 70^\circ C$ | Power Dissipation | 0.80 | |
| | Linear Derating Factor | 10 | mW/°C |
| E_{AS} | Single Pulse Avalanche Energy② | 33 | mJ |
| V_{GS} | Gate-to-Source Voltage | ± 12 | V |
| T_J, T_{STG} | Junction and Storage Temperature Range | -55 to + 150 | °C |

Thermal Resistance

| | Parameter | Max. | Units |
|-----------------|------------------------------|------|-------|
| $R_{\theta JA}$ | Maximum Junction-to-Ambient③ | 100 | °C/W |

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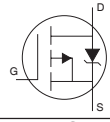
1

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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|---------------------------------|--------------------------------------|------|------|-------|---------------------|---|
| $V_{(BR)DSS}$ | Drain-to-Source Breakdown Voltage | 20 | — | — | V | $V_{GS} = 0V, I_D = 250\mu A$ |
| $\Delta V_{(BR)DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient | — | 0.01 | — | V/ $^\circ\text{C}$ | Reference to $25^\circ\text{C}, I_D = 1\text{mA}$ |
| $R_{DS(on)}$ | Static Drain-to-Source On-Resistance | — | — | 0.030 | Ω | $V_{GS} = 4.5V, I_D = 5.4A$ ② |
| | | — | — | 0.045 | | $V_{GS} = 2.5V, I_D = 4.6A$ ② |
| $V_{GS(th)}$ | Gate Threshold Voltage | 0.60 | — | 1.2 | V | $V_{DS} = V_{GS}, I_D = 250\mu A$ |
| g_{fs} | Forward Transconductance | 13 | — | — | S | $V_{DS} = 10V, I_D = 5.4A$ |
| I_{DSS} | Drain-to-Source Leakage Current | — | — | 1.0 | μA | $V_{DS} = 16V, V_{GS} = 0V$ |
| | | — | — | 25 | | $V_{DS} = 16V, V_{GS} = 0V, T_J = 70^\circ\text{C}$ |
| I_{GSS} | Gate-to-Source Forward Leakage | — | — | 100 | nA | $V_{GS} = 12V$ |
| | Gate-to-Source Reverse Leakage | — | — | -100 | | $V_{GS} = -12V$ |
| Q_g | Total Gate Charge | — | 18 | 26 | nC | $I_D = 5.4A$ |
| Q_{gs} | Gate-to-Source Charge | — | 3.4 | 5.1 | | $V_{DS} = 16V$ |
| Q_{gd} | Gate-to-Drain ("Miller") Charge | — | 3.4 | 5.1 | | $V_{GS} = 4.5V$ ② |
| $t_{d(on)}$ | Turn-On Delay Time | — | 8.5 | — | ns | $V_{DD} = 10V$ |
| t_r | Rise Time | — | 11 | — | | $I_D = 1.0A$ |
| $t_{d(off)}$ | Turn-Off Delay Time | — | 36 | — | | $R_G = 6.0\Omega$ |
| t_f | Fall Time | — | 16 | — | | $R_D = 10\Omega$ ② |
| C_{iss} | Input Capacitance | — | 1310 | — | pF | $V_{GS} = 0V$ |
| C_{oss} | Output Capacitance | — | 180 | — | | $V_{DS} = 15V$ |
| C_{rss} | Reverse Transfer Capacitance | — | 150 | — | | $f = 1.0\text{MHz}$ |

Source-Drain Ratings and Characteristics

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|----------|---|------|------|------|-------|--|
| I_S | Continuous Source Current (Body Diode) | — | — | 1.3 | A | MOSFET symbol showing the integral reverse p-n junction diode.  |
| I_{SM} | Pulsed Source Current (Body Diode) ① | — | — | 40 | | |
| V_{SD} | Diode Forward Voltage | — | — | 1.2 | V | $T_J = 25^\circ\text{C}, I_S = 1.3A, V_{GS} = 0V$ ② |
| t_{rr} | Reverse Recovery Time | — | 19 | 29 | ns | $T_J = 25^\circ\text{C}, I_F = 1.3A$ |
| Q_{rr} | Reverse Recovery Charge | — | 13 | 20 | nC | $di/dt = 100A/\mu s$ ② |

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.
- ③ When mounted on 1 inch square copper board, $t < 10$ sec
- ④ Starting $T_J = 25^\circ\text{C}, L = 2.6\text{mH}$
 $R_G = 25\Omega, I_{AS} = 5.0A$. (See Figure 10)

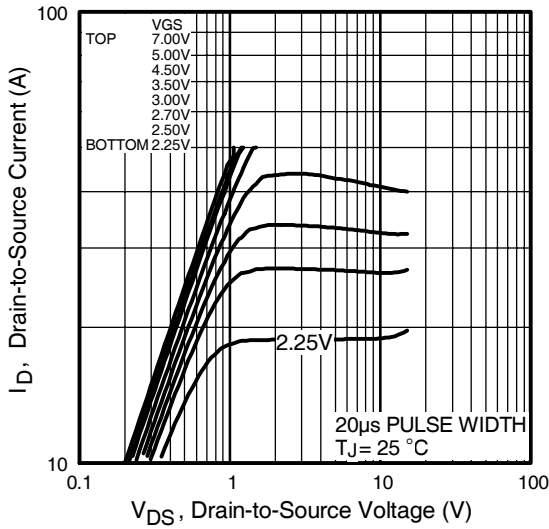


Fig 1. Typical Output Characteristics

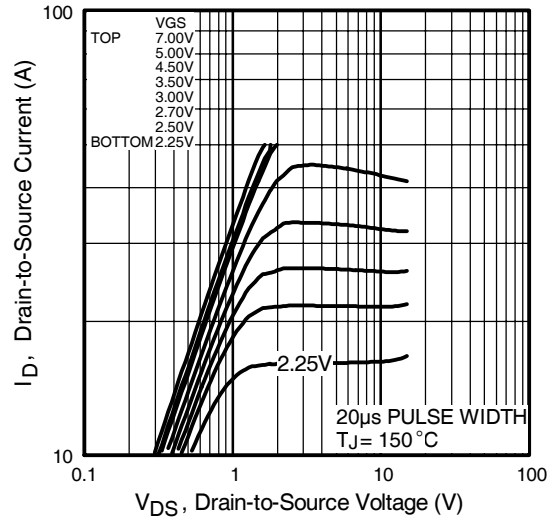


Fig 2. Typical Output Characteristics

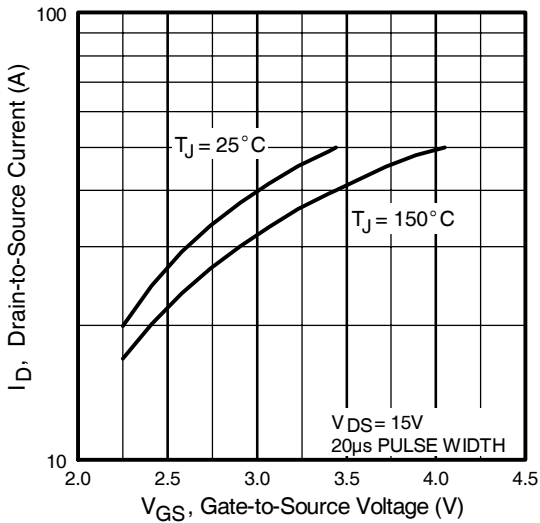


Fig 3. Typical Transfer Characteristics

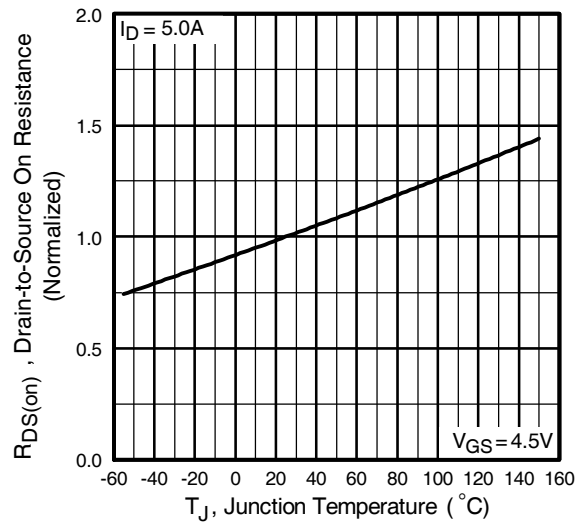


Fig 4. Normalized On-Resistance Vs. Temperature

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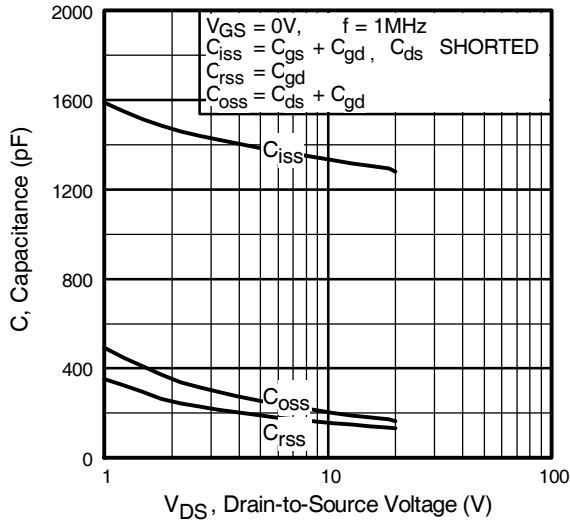


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

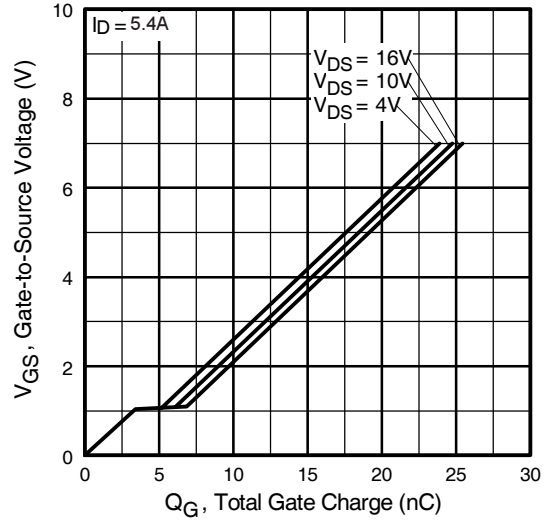


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

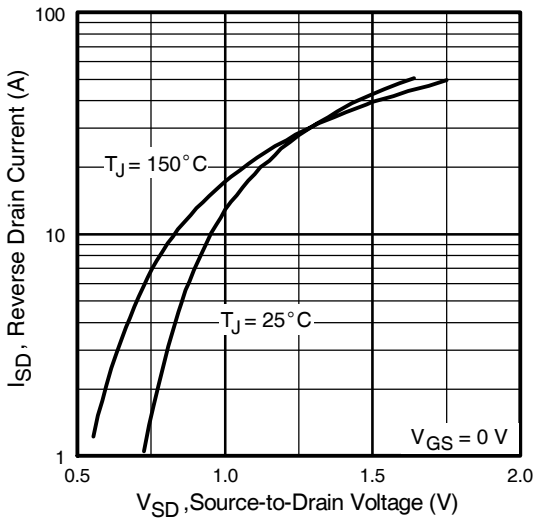


Fig 7. Typical Source-Drain Diode Forward Voltage

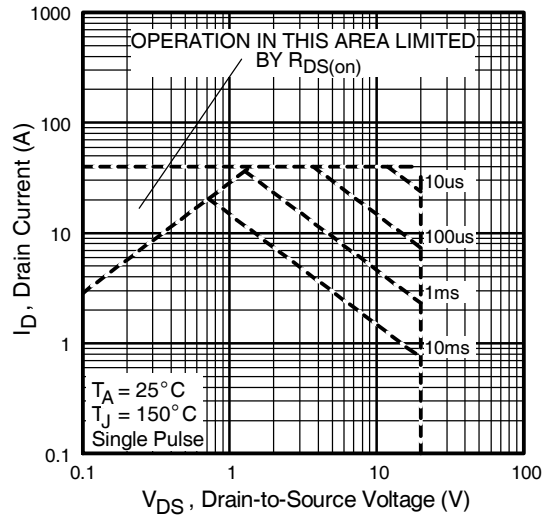


Fig 8. Maximum Safe Operating Area

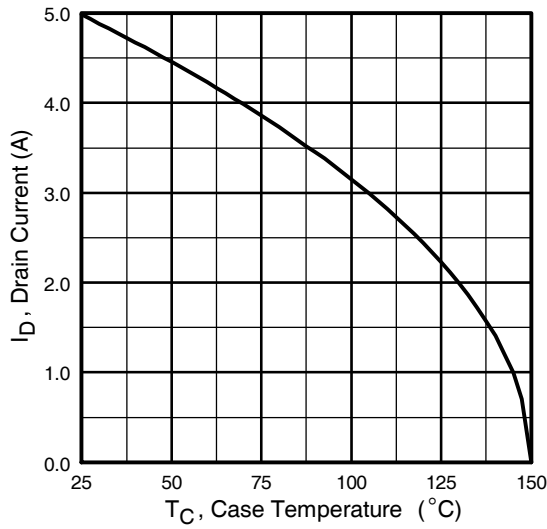


Fig 9. Maximum Drain Current Vs. Case Temperature

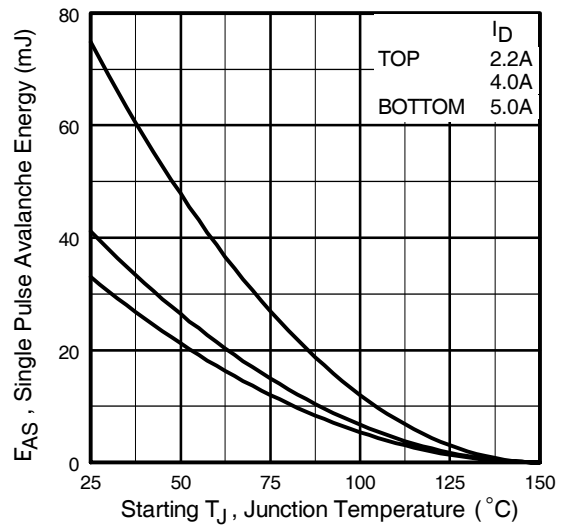


Fig 10. Maximum Avalanche Energy Vs. Drain Current

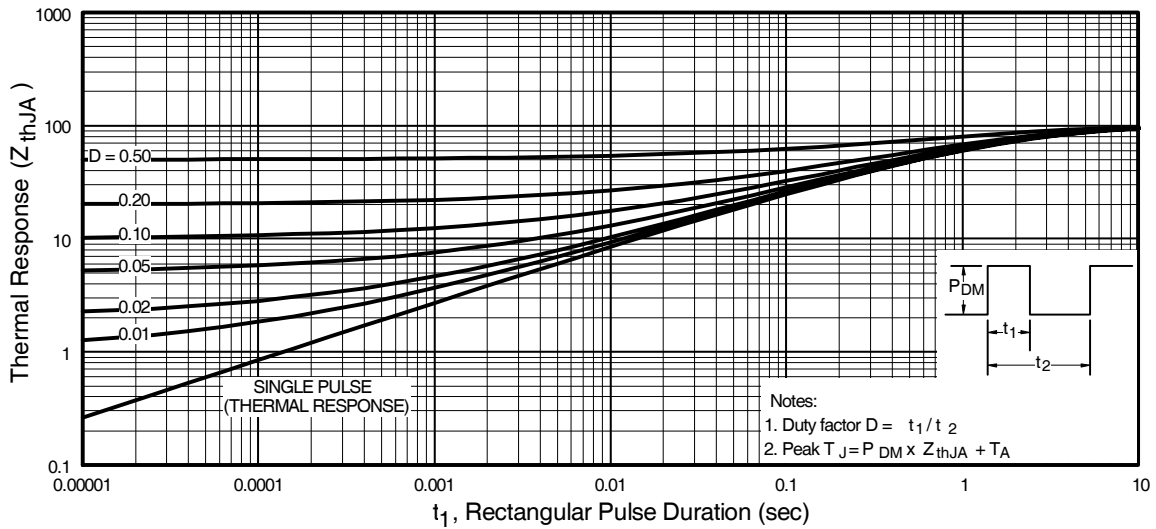


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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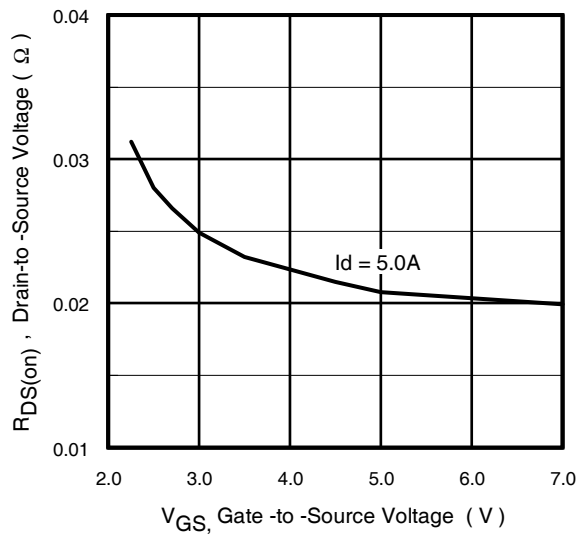


Fig 12. On-Resistance Vs. Gate Voltage

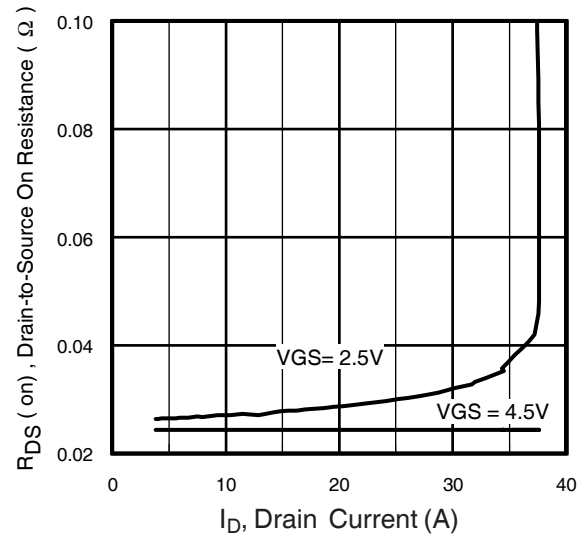
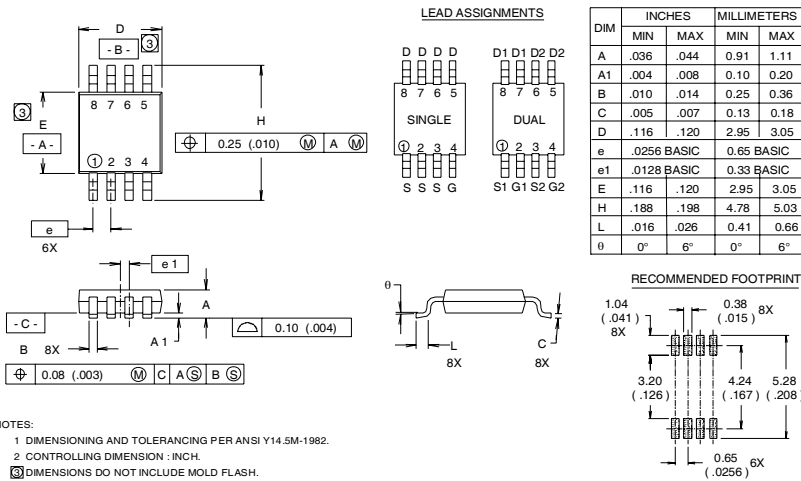


Fig 13. On-Resistance Vs. Drain Current

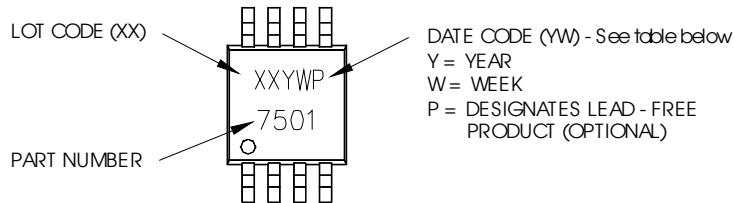
Micro8 Package Outline

Dimensions are shown in millimeters (inches)



Micro8 Part Marking Information

EXAMPLE: THIS IS AN IRF7501



WW = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR

| YEAR | Y | WORK WEEK | W |
|------|---|-----------|---|
| 2001 | 1 | 01 | A |
| 2002 | 2 | 02 | B |
| 2003 | 3 | 03 | C |
| 2004 | 4 | 04 | D |
| 2005 | 5 | | |
| 2006 | 6 | | |
| 2007 | 7 | | |
| 2008 | 8 | | |
| 2009 | 9 | | |
| 2010 | 0 | 24 | X |
| | | 25 | Y |
| | | 26 | Z |

WW = (27-52) IF PRECEDED BY A LETTER

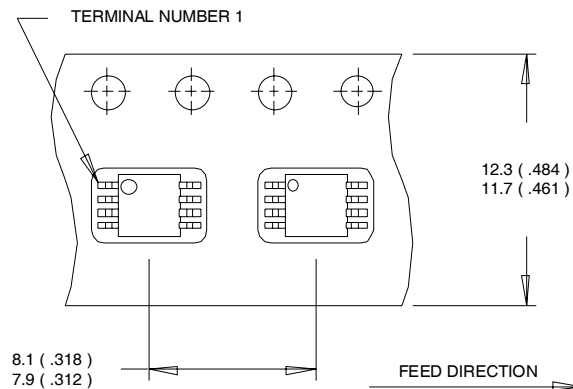
| YEAR | Y | WORK WEEK | W |
|------|---|-----------|---|
| 2001 | A | 27 | A |
| 2002 | B | 28 | B |
| 2003 | C | 29 | C |
| 2004 | D | 30 | D |
| 2005 | E | | |
| 2006 | F | | |
| 2007 | G | | |
| 2008 | H | | |
| 2009 | J | | |
| 2010 | K | 50 | X |
| | | 51 | Y |
| | | 52 | Z |

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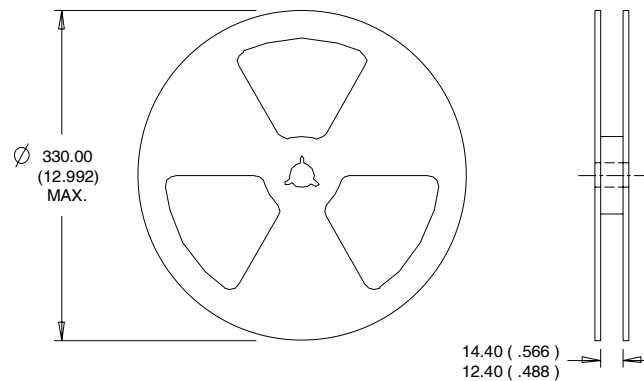
Micro8 Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.
2. CONTROLLING DIMENSION : MILLIMETER.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.
This product has been designed and qualified for the Consumer market.
Qualification Standards can be found on IR's Web site.

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TAC Fax: (310) 252-7903

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