100mA / 50V Digital transistors (with built-in resistors)

DTC114YM / DTC114YE / DTC114YUA / DTC114YKA / DTC114YSA

Applications

Inverter, Interface, Driver

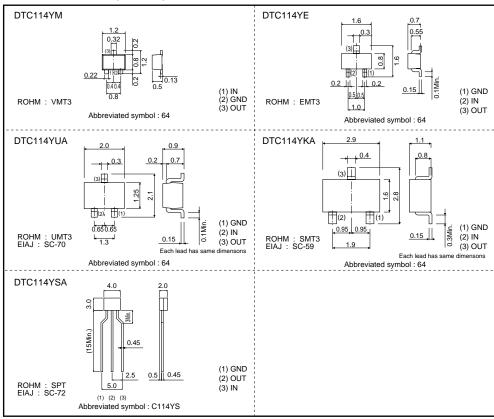
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in types)

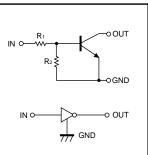
●External dimensions (Unit : mm)



Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTC114YM		0	-	-	-	-
DTC114YE		-	0	-	-	-
DTC114YUA		-	-	0	-	-
DTC114YKA		-	-	-	0	-
DTC114YSA		-	-	-	-	0

●Equivalent circuit



R₁=10k Ω R₂=47k Ω

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits				
- raiaillelei		DTC114YM DTC114YE	DTC114YUA	DTC114YKA	DTC114YSA	Unit
Supply voltage	Vcc	50			V	
Input voltage	VIN	−6 to +40				V
Output ourrent	lo	70				mA
Output current	IC(Max.)	100				
Power dissipation	Pp	150 200		300	mW	
Junction temperature	Tj	150				°C
Storage temperature	Tstg	−55 to +150				°C

●Electrical characteristics (Ta=25°C)

	•	,				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lance college	VI(off)	-	-	0.3	.,	Vcc=5V, Io=100μA
Input voltage	VI(on)	1.4	-	-	V	Vo=0.3V, Io=1mA
Output voltage	Vo(on)	-	0.1	0.3	V	Io/I⊫5mA/0.25mA
Input current	lı	-	-	0.88	mA	Vi=5V
Output current	IO(off)	-	-	0.5	μА	Vcc=50V, V⊫0V
DC current gain	Gı	68	-	_	_	Vo=5V, Io=5mA
Input resistance	R ₁	7	10	13	kΩ	_
Resistance ratio	R2/R1	3.7	4.7	5.7	-	-
Transition frequency	f⊤ *	-	250	-	MHz	VcE=10V, IE=-5mA, f=100MHz

^{*} Characteristics of built-in transistor

•Electrical characteristic curves

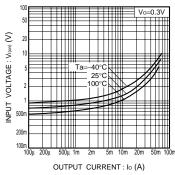


Fig.1 Input voltage vs. output current (ON characteristics)

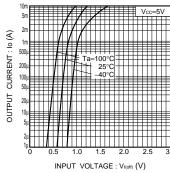


Fig.2 Output current vs. input voltage (OFF characteristics)

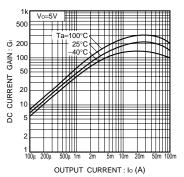


Fig.3 DC current gain vs. output current

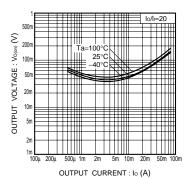


Fig.4 Output voltage vs. output current

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