

**IL1084T2**
**CHIP LOW DROPOUT POSITIVE REGULATOR 5A**
**Features**

- ◆ Output Current - 5A
- ◆ Maximum Input Voltage – 12V
- ◆ Adjustable Output Voltage or Fixed  
1.5V, 1.8V, 2.5V, 2.85V, 3.3V, 3.6V, 5V
- ◆ Current Limiting and Thermal Protection
- ◆ Standard 3-Pin Power Packages

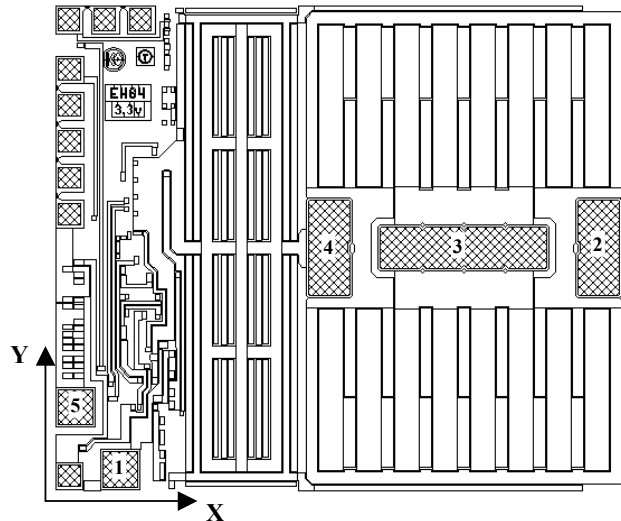
**Applications**

- ◆ Post Regulator for Switching DC/DC Converter
- ◆ High Efficiency Linear Regulators
- ◆ Battery Charger

**Physical Characteristics:**

- Chip size.....1.9 x 2.2 mm
- ◆ Wafer Diameter ..... 100 ± 0.5 mm
- ◆ Wafer thickness ..... 280 ± 20 μm
- ◆ Scribe width ..... 100 μm
- ◆ Metallization bottom... Ti-Ni-Ag  
Ti-Ni – 0.5-0.7μm  
Ag – 0.6-0.1μm

- ◆ Passivation ..... PSG


**Pad Location Coordinates**

N	Pad names	Pad size	X(μm)	Y (μm)
1	NC	130x130	251	88
2	INPUT	150x340	1961	780
3	OUTPUT	600x150	1251	875
4	INPUT	150x340	991	780
5	GND	130x130	88	311

**Absolute Maximum Ratings (Note 1)**

Power Dissipation (Note 2) Internally Limited  
 Junction Temperature (Note 3) 150°C  
 Storage Temperature Range -65°C to 150°C  
 Storage Temperature Range -65°C to 150°C

**Operating Ratings**

Junction Temperature Range (Note 3) -10°C to 125°C

**ELECTRICAL CHARACTERISTICS**

Typicals and limits appearing in normal type apply for T<sub>j</sub>= +25°C.

Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

Symbol	Parameter	Conditions	Min (Note 5)	Typ (Note 4)	Max (Note 5)	Units
V <sub>OUT</sub>	Output Voltage (Note 6) IL1084-Adj BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =4.25V	1.237	1.250	1.263	V
		0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 2.75V ≤ V <sub>IN</sub> ≤ 10V	1.232	1.250	1.268	
	IL1084-1.5 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =4.5V	1.485	1.500	1.515	
		0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 3.0V ≤ V <sub>IN</sub> ≤ 10V	1.478	1.500	1.522	
	IL1084-1.8 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =4.8V	1.782	1.800	1.818	
		0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 3.3V ≤ V <sub>IN</sub> ≤ 10V	1.773	1.800	1.827	
	IL1084-2.5 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =5.5V	2.475	2.500	2.525	
		0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 4.0V ≤ V <sub>IN</sub> ≤ 10V	2.463	2.500	2.537	
IL1084-2.85 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =5.85V	2.820	2.850	2.880		
	0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 4.35V ≤ V <sub>IN</sub> ≤ 10V	2.805	2.850	2.895		
IL1084-3.3 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =6.3V	3.270	3.300	3.330		
	0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 4.8V ≤ V <sub>IN</sub> ≤ 10V	3.250	3.300	3.350		
IL1084-3.6 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =6.6V	3.564	3.600	3.636		
	0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 5.1V ≤ V <sub>IN</sub> ≤ 10V	3.546	3.600	3.654		
IL1084-5.0 BT2	I <sub>OUT</sub> =10mA, V <sub>IN</sub> =8.0V	4.950	5.000	5.050		
	0 ≤ I <sub>OUT</sub> ≤ I <sub>FULL LOAD</sub> , 6.5V ≤ V <sub>IN</sub> ≤ 10V	4.925	5.000	5.075		

**IL1084 BT2**
**CHIP LOW DROPOUT POSITIVE REGULATOR 5A**
**ELECTRICAL CHARACTERISTICS**

 Typical values and limits appearing in normal type apply for  $T_j = +25^\circ\text{C}$ .

 Limits appearing in **Boldface** type apply over the entire junction temperature range for operation.

$\Delta V_{\text{OUT}}$	Line Regulation (Note 7)		-	-	0.3	%	
	IL1084-Adj BT2	$I_{\text{OUT}}=10\text{mA}, 2.75\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	<b>0.4</b>		
	IL1084-1.5 BT2	$I_{\text{OUT}}=10\text{mA}, 3.0\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	6		mV
	IL1084-1.8 BT2	$I_{\text{OUT}}=10\text{mA}, 3.3\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	<b>10</b>		
	IL1084-2.5 BT2	$I_{\text{OUT}}=10\text{mA}, 4.0\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	6		mV
	IL1084-2.85 BT2	$I_{\text{OUT}}=10\text{mA}, 4.35\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	<b>10</b>		
	IL1084-3.3 BT2	$I_{\text{OUT}}=10\text{mA}, 4.8\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	6		mV
	IL1084-3.6 BT2	$I_{\text{OUT}}=10\text{mA}, 5.1\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	<b>10</b>		
IL1084-5.0 BT2	$I_{\text{OUT}}=10\text{mA}, 6.5\text{V}\leq V_{\text{IN}}\leq 10\text{V}$	-	-	6	mV		
				<b>10</b>			
$\Delta V_{\text{OUT}}$	Load Regulation (Note 7)		-	-	0.3	%	
	IL1084-Adj BT2	$V_{\text{IN}}=4.25\text{V}, 0\leq I_{\text{OUT}}\leq I_{\text{FULL LOAD}}$	-	-	<b>0.4</b>		
	IL1084-1.5 BT2	$V_{\text{IN}}=5.0\text{V}, 0\leq I_{\text{OUT}}\leq I_{\text{FULL LOAD}}$	-	-	12		mV
	IL1084-1.8 BT2		-	-	<b>20</b>		
	IL1084-2.5 BT2						
	IL1084-2.85 BT2						
IL1084-3.3 BT2	$V_{\text{IN}}=5.0\text{V}, 0\leq I_{\text{OUT}}\leq I_{\text{FULL LOAD}}$	-	-	15	mV		
IL1084-3.6 BT2	$V_{\text{IN}}=5.3\text{V}, 0\leq I_{\text{OUT}}\leq I_{\text{FULL LOAD}}$	-	-	<b>25</b>			
IL1084-5.0 BT2	$V_{\text{IN}}=8.0\text{V}, 0\leq I_{\text{OUT}}\leq I_{\text{FULL LOAD}}$	-	-	20	mV		
				<b>35</b>			
$\Delta V$	Dropout Voltage (Note 8)	$\Delta V_{\text{REF}}=1\%, I_{\text{OUT}}=5\text{A}$	-	-	1.5	V	
$I_{\text{O(MIN)}}$	Minimum Load Current	$V_{\text{IN}}=10\text{V}$	-	-	10	mA	
$I_{\text{LIMIT}}$	Current Limit	$V_{\text{IN}}=6.25\text{V}$	5.5	-	-	A	
$I_{\text{ADJ}}$	Adjust Pin Current	$V_{\text{IN}}=2.75\div 10\text{V}, I_{\text{OUT}}=10\text{mA}$	-	-	120	$\mu\text{A}$	
$\Delta I_{\text{ADJ}}$	Adjust Pin Current Change	$I_{\text{OUT}}=10\text{mA}\div 5\text{A}, V_{\text{IN}}=2.75\div 10\text{V}$	-	-	5	$\mu\text{A}$	
RR	Ripple Rejection	$f_{\text{RIPPLE}} = 120\text{Hz}, C_{\text{OUT}}=25\mu\text{F}$ Tantalum, $I_{\text{out}}=5\text{A}; V_{\text{IN}}=4.25\text{V}$	60	-	-	dB	
S	Temperature Stability		-	<b>0.5</b>	-	%	

**NOTES 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Rating indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

**NOTES 2:** Power Dissipation is kept in a safe range by current limiting circuitry. Refer to Overload Recovery in Application Notes.

**NOTES 3:** The maximum power dissipation is a function of  $T_{j(\text{MAX})}$ ,  $\Theta_{jA}$  and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D=(T_{j(\text{MAX})}-T_A)\Theta_{jA}$ .

**NOTES 4:** Typical Values represent the most likely parametric norm

**NOTES 5:** All limits are guaranteed by testing or statistical analysis

**NOTES 6:**  $I_{\text{FULL LOAD}}$  is defined in the current limit curves. The  $I_{\text{FULL LOAD}}$  curve defines the current limit as a function of input-to-output voltage.

**NOTES 7:** Load and Line regulation are measured at constant junction temperature, and are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

**NOTES 8:** Dropout voltage is specified over the full output current range of the device.