To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

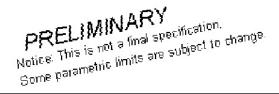
The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp. Customer Support Dept. April 1, 2003



MITSUBISHI SOUND PROCESSORS



M61500FP

TONE CONTROL/VOLUME CONTROL

DESCRIPTION

The M61500FP is the sound controller powerd by "QSurround" system. The "QSurround" system decodes and virtualize multi-speaker surround sound from various matrix surround encoded sources: Dolby Surround, stereo downmixed AC-3, stereo downmixed DTS. Produces normal and wide 3D sound expansion from any stereo input signal.

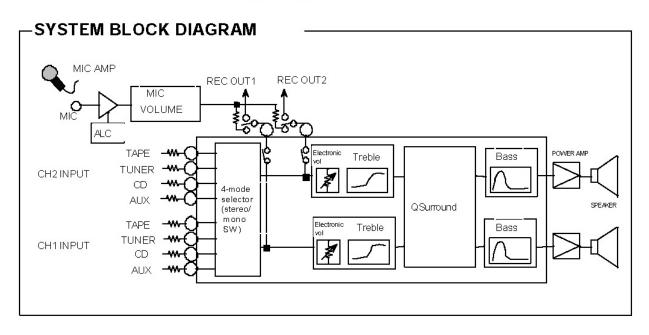
(Note) This device is producted under license from QSound Lab, Inc. (Canada).

FEATURES

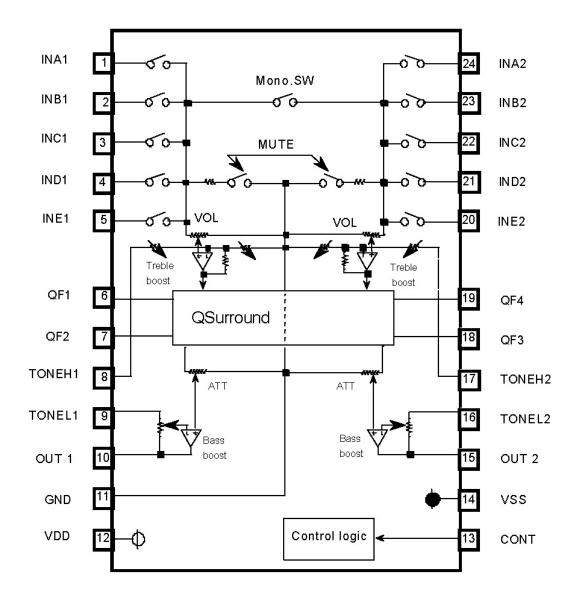
 Built-in "QSurround" sound technology
 Electronic volume. 0 ~ -84dB, infinitesimal
 2-band tone control Bass(0 ~ +21dB/3dB STEP) Treble(0 ~ +9dB/3dB STEP)
 5 input selector(The fifth input can be used as REC OUT or MIC MIX.)

RECOMMENDED OPERATING CONDITIONS

Supply voltage range <u>+</u> 2.25~+ 2.75V



BLOCK DIAGRAM



Units Resistance :ohm Capacitance: F

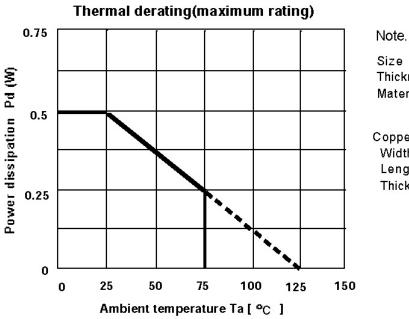


PIN DESCRIPTION

Pin No.	Name	Function
1	IN A1	
2	IN B1	
3	IN C1	INPUTs of the channel 1
4	IN D1	The swich of INE can be controlled independently.
5	IN E1	Please set"ALL OFF"mode when the switch of E is only ON.
6	QF1	QSurround filter 1
7	QF2	QSurround filter 2
8	TONEH1	Treble control adjustment of the channel 1
9	TONEL1	Bass control adjustment of the channel 1
10	OUT1	OUTPUT of the channel 1
11	GND	Ground
12	VDD	Supply voltage(+)
13	CONT	Control data input from a microcontroller
14	VSS	Supply voltage(-)
15	OUT2	OUTPUT of the channel 2
16	TONEL2	Bass control adjustment of the channel 2
17	TONEH2	Treble control adjustment of the channel 2
18	QF3	QSurround filter 3
19	QF4	QSurround filter 4
20	IN E2	The swich of INE can be controlled independently. Please set "ALL OFF" mode when the switch of E is only
21	IN D2	ON.
22	IN C2	INPUTs of the channel 2
23	IN B2	
24	IN A2	

ABSOLUTE MAXIMUM RATONGS

Symbol	Parameter	Test conditions	Rating	Unit
VDD-VSS	Supply voltage		6.0	V
Кø	Thermal derating	Note:1	5	mW/ ° C
Pd	Power dissipation		500	mW
Topr	Operating temperature		-20 ~ 75	°C
Tstg	Storage temperature		-40 ~ 125	°C



Note.1 reference PC Board Size :70mmX70mm

Thickness:1.6mm Material :glass epoxy

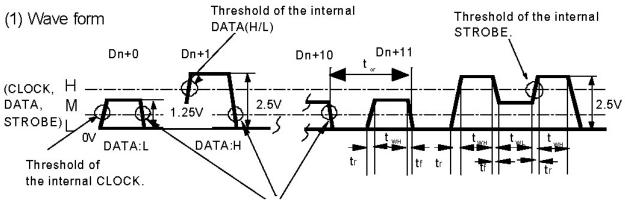
Copper pattern dimension Width :0.25mm Length :25 ~ 30mm/lead Thickness:18um

Recommended operating conditions

				[Limits		11.3
Symbol	Parameter	Pin No.	Condition	min.	typ.	max.	Unit
VDD	Supply voltage(+)	12		2.25	2.5	2.75	
VSS	Supply voltage(-)	14		-2.75	-2.5	-2.25	V
CONT	Control date input voltage	13		GND		VDD	



CONTROL SIGNALS SPECIFICATION



The internal DATA latch at the falling edges of this clock signal.

(2) Voltage control signal

Digital input signal		Condition		Unit		
Digital Input sign	ai	Condition	min.	typ.	max.	Unit
L signal	L	VDD=2.5V,VSS=-2.5V	GND	_	0.4	
M signal	м	VDD=2.5V,VSS=-2.5V	1.0	1.25 (VDD/2)	1.5	v
H signal	н	VDD=2.5V,VSS=-2.5V	2.1		VDD	

(3) Timing control signal

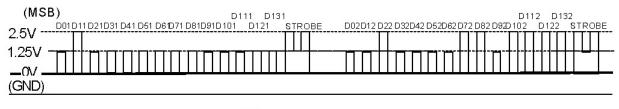
Symbol	Parameter	min	_imits [typ	s (max	Unit
t _{cr}	Cycle time of digital signal	8	-	Ē.	
t _{wh}	Pulse width of digital signal("H"level)	3.6	_	-	
t with	Pulse width of digital signal("L"level)	3.6	I.	ľ.	USEC
t,	Rise time of digital signal	-	1	0.4	
t,	Fall time of digital signal	j.	Ĵ	0.4	

(4) Control signal example(Refer to page 6 on the control data)

An example of the mode control

BYPASS/SURROUND SW:SURROUND VOL/TREBLE SHARE AMP GAIN:20dB INPUT :INA, VOLUME :0dB MUTE :OFF

SURROUND MODE:QSurround SURROUND EFFECT:0dB MODE:STEREO BASS:18dB TREBLE:6dB RECOUT:ON(INE)





CONTROL DATA FORMAT

*It's nessesary to set up the all control data affer power on.

(1) INPUT DATA

(MSB) The input order	(MSB)		— Input order
-----------------------	-------	--	---------------

	10000	mp													
_ Г	D01	D11	D21	D31	D41	D51	D61	D71	D81	D91	D101	D11	11 D12	21 D1:	31
Slot1	0	Bypass/ Surround SW	Vol/Treble shar SV 0:20dB 1:18dB 2:16dB 3:14dB	re amp gain	IN PU 0: IN 1: IN 2: IN 3: IN	IA IB IC	D2 to D6:(condition	(a)Maste	er volume			MUTE ON/OFF D:OFF 1:ON (INPUT / OFF)	0:se = 1:n 2:n 3:n	SLOTSELECT elect o select o select o select	
Г	D02	D12	D22	D32	D42	D52	D62	D72	D82	D92	D102	D11	2 D12	22 D1	32
Slot2	Surroun	id mode	Surround	l effect	Mode O:stere 1:mor	eo no1 only no2 only	Bass 0:0d 2:6d 4:12	e(boost) IB, 1:3d IB, 3:9d 2dB, 5:15 IdB, 7:21	B, B, 5dB,	Treble 0:0dB	e(boost) ,1:3dB ,3:9dB	INE ON/OFF D:OF 1:ON	CHIP O:n T:n F 2:n	ÆLOT SELEC o select o select o select elect	
) Maste	r volu	me						(b) l	nput se	lect					
ATT	-	D61	D7	1 C	081	D91	D101	In	put sele	ct	D41	D51	D111	D112	
-0.0dB -2.0dB -4.0dB -6.0dB		0 1 0	0 0 1 1		0 0 0 0	0 0 0	0 0 0		NA NB NC ND	IN E off	0 1 0 1	0 0 1 1	0	O	
-8.0dB -10.0dB -12.0dB		0 1 0	0		1 1 1	0 0 0	0 0 0	IN Atod IN	A-D lect	IN E	* A:0 (B:1 (C:0	*) 1	1	1 *1 *2 1	
-14.0dB -16.0dB -18.0dB -20.0dB		1 0 1 0	0 0 1		1 0 0 0	0 1 1 1	0 0 0	*1)Th	ie input imp E can be c		D: 1 s about 5		out INE.		
-22.0dB -24.0dB -26.0dB -28.0dB		1 0 1 0	1 0 0		0 1 1 1	1 1 1 1	0 0 0	1	n be used		utput.	(-1) T			
-30.0dB		1	1		1	1	0		ode co			(a)	reble co		
		<u> </u>			~ T	~									D10
-32.0dB -34.0dB		0	0		0	0	1	Ň	1ode	D42	D52		Treble	D92	
-34.0dB -36.0dB		1	0 0 1		0	0	1	stere		D42 0 1	D52 0 0		Treble OdB 3dB	D92 0 1	0
-34.0dB		1	0		0	0	1	stere mon mon	o o1 only o2 only	0 1 0	0		OdB 3dB 6dB	0 1 0	0
-34.0dB -36.0dB -40.0dB -44.0dB -48.0dB		1 0 1 0	0 0 1 1 0 0		0 0 0 1 1	0 0 0 0	1 1 1 1	stere mon	o o1 only o2 only	0	0		0dB 3dB	0	0
-34.0dB -36.0dB -40.0dB -44.0dB		1 0 1 0	0 0 1 1 0		0 0 0 1	0 0 0 0	1 1 1 1	stere mon mon	o o1 only o2 only	0 1 0 1	0		0dB 3dB 6dB 9dB	0 1 0 1	0 0 1
-34.0dB -36.0dB -40.0dB -44.0dB -48.0dB -52.0dB		1 0 1 0 1 0 1 0	0 0 1 1 0 0 0 1 1 1 0		0 0 1 1 1 1 1 0	0 0 0 0 0 0 0 1	1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col	0 1 0 1	0 0 1 1		0dB 3dB 6dB 9dB	0 1 0	0 0 1
-34.0dB -36.0dB -40.0dB -44.0dB -48.0dB -52.0dB -52.0dB -56.0dB -60.0dB -64.0dB		1 0 1 0 1 0 1 0 1 0	0 0 1 0 0 0 1 1 1 0 0 0 0 0		0 0 1 1 1 1 1 0 0	0 0 0 0 0 0 0 1 1	1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col	0 1 0 1 ntrol D62	0 0 1 1 D72	D82	0dB 3dB 6dB 9dB f)Chip/	0 1 0 1	0 0 1 1
-34.0dB -36.0dB -40.0dB -44.0dB -52.0dB -56.0dB -60.0dB -64.0dB -68.0dB		1 0 1 1 0 1 1 0 1 0 1 0	0 0 1 0 0 0 1 1 1 0 0 0 1 1 1		0 0 1 1 1 1 1 0 0 0	0 0 0 0 0 0 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col Bass 0dB	0 1 0 1	0 0 1 1 D72 0	D82	0dB 3dB 6dB 9dB	0 1 0 1	0 0 1
-34.0dB -36.0dB -40.0dB -44.0dB -48.0dB -52.0dB -56.0dB -60.0dB -64.0dB -68.0dB -72.0dB		1 0 1 0 1 1 0 1 1 0 1 1 0	0 0 1 0 0 0 1 1 0 0 0 1 1 1 1		0 0 1 1 1 1 0 0 0 0	0 0 0 0 0 0 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col Bass OdB 3dB	0 1 0 1 ntrol D62 0 1	0 0 1 1 0 0 0	D82	0dB 3dB 6dB 9dB f)Chip/	0 1 0 1 'Slot cc	0 0 1 1
-34.0dB -36.0dB -40.0dB -44.0dB -48.0dB -52.0dB -55.0dB -60.0dB -64.0dB -68.0dB -72.0dB -72.0dB		1 0 1 0 1 0 1 0 1 0 1 0 1 0	0 0 1 1 0 0 1 1 0 0 1 1 1 0 0 0 0 0 0 0		0 0 1 1 1 1 0 0 0 0 0 1	0 0 0 0 0 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass COI ass OdB 3dB 6dB	0 1 0 1 0 0 0 1 0	0 0 1 1 0 0 0 1	D82 0 0 0 0	0dB 3dB 6dB 9dB f)Chip/Sl	0 1 0 1 'Slot cc	0 0 1 1 2*
-34.0dB -36.0dB -40.0dB -44.0dB -52.0dB -52.0dB -56.0dB -64.0dB -64.0dB -68.0dB -72.0dB -76.0dB -76.0dB		1 0 1 0 1 0 1 0 1 0 1 0 0 1 1	0 0 1 0 0 0 1 1 0 0 0 1 1 0 0 0 0 0 0 0		0 0 1 1 1 1 0 0 0 0 0 1 1	0 0 0 0 0 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col ass 0dB 3dB 6dB 9dB	0 1 0 1 0 1 0 0 1 0 1	0 0 1 1 0 0 0 1 1 1	D82 0 0 0 0 0	0dB 3dB 6dB 9dB f)Chip/Sl chip/Sl	0 1 0 1 'Slot cc ot D1 ct 1)	0 0 1 1 2* □
-34.0dB -36.0dB -40.0dB -44.0dB -52.0dB -52.0dB -56.0dB -64.0dB -64.0dB -68.0dB -72.0dB -72.0dB -76.0dB -80.0dB -84.0dB		1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0	0 0 1 1 0 0 1 1 0 0 1 1 0 0 0 0 1 1		0 0 1 1 1 1 0 0 0 0 1 1 1 1	0 0 0 0 0 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		o o1 only o2 only o1+2 ass col ass 0dB 3dB 6dB 9dB 12dB	0 1 0 1 0 1 0 0 1 0 1 0	0 0 1 1 0 0 0 1 1 0	D82 0 0 0 0	0dB 3dB 6dB 9dB f)Chip/Sle select(sl no sele	0 1 0 (Slot cc (Slot cc (D1) (ct) (ct) (ct) (ct) (ct) (ct) (ct) (ct	0 0 1 1 2* 0 1
-34.0dB -36.0dB -40.0dB -44.0dB -52.0dB -52.0dB -56.0dB -64.0dB -64.0dB -68.0dB -72.0dB -76.0dB -76.0dB	esimal	1 0 1 0 1 0 1 0 1 0 1 0 0 1 1	0 0 1 0 0 0 1 1 0 0 0 1 1 0 0 0 0 0 0 0		0 0 1 1 1 1 0 0 0 0 0 1 1	0 0 0 0 0 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	stere mon mon (e)B	o o1 only o2 only o1+2 ass col ass 0dB 3dB 6dB 9dB	0 1 0 1 0 1 0 0 1 0 1	0 0 1 1 0 0 0 1 1 1	D82 0 0 0 0 0	0dB 3dB 6dB 9dB f)Chip/Sl Chip/Sl select(sl no sele no sele	0 1 0 (Slot cc (Slot cc (D1) (ct) (ct) (ct) (ct) (ct) (ct) (ct) (ct	0 0 1 1 2* 0 1

(g)Treble amp gain SW

Gain SW	D21	D31
20dB	0	0
18dB	1	0
16dB	0	1
14dB	1	1

(h)Bypass/

Surround SW

Bypass/	D11
Surround SW	.
Bypass	0
Surround	1

(i)Surround mode

Mode	D02	D12
QSurround	0	0
	1	0
Wide surround	0	1
Norm al surround	1	1

(j)Surround effect

(Valid in the surround mode. Set 0dB at QSurround.)

Effect	D22	D32
+3dB	0	0
0dB	1	0
-3dB	0	1
-6dB	1	1



(2) NOTICE OF CONTROL DATA

1. Input only the control data at (1) INPUT DATA.

- 2. The interval of data transmission from the microcontroller is over 0.1 sec. This is waiting time for soft-switching to reduce the shocknoise.
- 3.It's necessary to set up the all control data after poweron,although the internal circuit is forced as belows,when (VDD-VSS)<3.3V(TYP).

Parameter	Condition
Gain SW	18dB
Input select	ALL OFF
Master volume	infinitesimal
MUTE	ON(Input ALLOFF)
Surround effect	- 6dB
Surround	OFF
Surround mode	QSurround
Mode select	stereo
Bass	0dB
Treble	0dB
IN E	ON



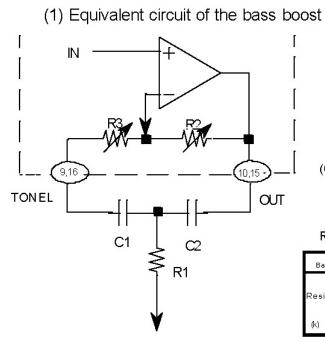
ELECTRICAL CHARACTERISTICS

(VDD=2.5V,VSS=-2.5V,f=1kHz,Vi=100mV(rms),VOL=0dB,BASS=0dB,TREBLE=0dB,VOL/TREBLE
SHARE AMP=18dB,SURROUND=BYPASS,RL=10K,Ta=25℃,unless otherwise noted)

					Limits		
Symbol	Parameter	Co	ondition	min.	typ.	max.	Unit
IDD	Circuit current of positive power supply	Quiescent			30	45	mA
ISS	Circuit current of negative power supply	Quiescent			-30	-45	mA
Gv1	Voltage gain (selector)	Vol/Treble sha Bypass	re amp gain=18dB	16	18	20	dB
Gv2	Voltage gain (tone control)		e amp gain=18dB de Vi=20mVrms	25.5	27.5	29.5	dB
Vomax	Maximum output voltage	RL=10k,THD=	=1%	1.2	1.6	_	Vrms
THD	Total harmonic distortion	BVV=400~30	kHz		0.02	0.08	%
No1	Output noise voltage	JIS-A,Rg=5.1k BYPASS	,VOL=the infinitesim al		6	15	uVrms
No2	Ouput hoise voitage	JIS-A,Rg=5.11 QSurround mo	<,VOL=the infinitesimal de		11	30	uVırmıs
ATTmax	Maximum attenuation	Output referencelevel(Vo=1Vrms), ATT= the infinitesimal ,JIS-A			-95	-90	dB
GB1		3dB	f=1kHz,	1.5	3	4.5	
GB2		6dB	Vo=80mVrms	4.5	6	7.5	
GB3		9dB		7.5	9	10.5	
GB4	Bass boost	12dB		10.5	12	13.5	
GB5		15dB		13.5	15	16.5	- dB
GB6		18dB		16.5	18	19.5	чъ
GB7	Ţ	21dB		19.5	21	22.5	
GT1		3dB	f=1kHz,	1.5	3	4.5	
GT2	Treble boost	6dB Vo=80mVrms		4.5	6	7.5	
GT3		9dB	<u> </u>	7.5	9	10.5	

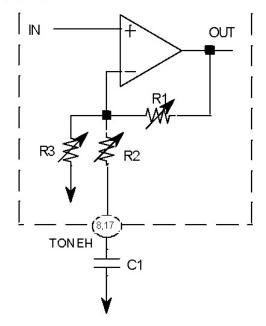


FUNCTION DESCRIPTION



031	F	o= -	2π√	1 R1(R2	2+R3)0	C1C2		(Hz)
	G)⊨	1 1+C2	_ <u>\[</u>	C2R2 R1	_		
(C1=	G) 6v=20l (typica		<u>R2+F</u> <u>R'</u> <u>R</u> R	1 3 - +2	+2	(dB)
Bassboo	st	3dB	6dB	9dB	12dB	15dB	18dB	21dB
Resistor	R2	15.4	25.7	32.9	38.7	41.6	44.2	46
(K)	R3	30.6	20.3	13.1	7.3	4.4	1.8	0

(2) Equivalent circuit of the treble boost



Fc=
$$\frac{1}{2\pi R^2 C1}$$
 (Hz)

$$Gv=20log \qquad \frac{R1+ \{(R2+Zc)//R3\}}{(R2+Zc)//R3} \quad (dB)$$

$$Zc= \frac{1}{j_{W}C1} \quad (ohm)$$

R2 (typical)

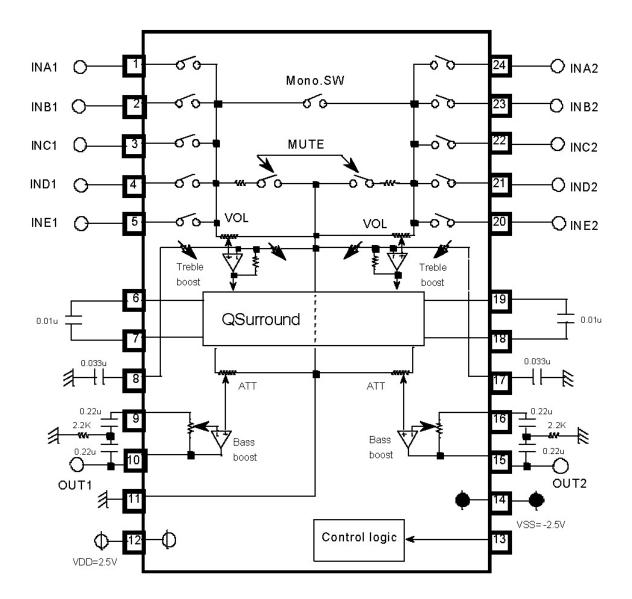
Treble boost	3dB	6dB	9dB
R2 (k)	5.3	2.2	1.2

R1,R3 (typical)

Gain	14dB	16dB	18dB	20dB
R1 (k)	10.88	13.65	17.21	21.60
R3 (k)	2.72	2.57	2.48	2.40

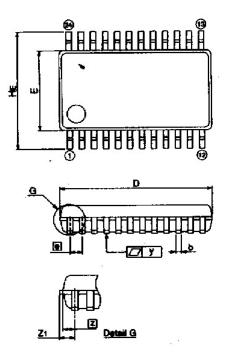


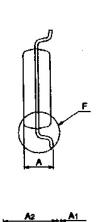
APPLICATION EXAMPLE

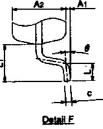


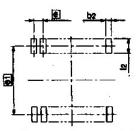
Units Resistor : ohm Capacitor: F











Recommended Mount Pad

D	Dimens	ion in Mill	meters
Symbol	Min	Nom	Max
A	-	1	2.1
A1	0	0.1	0.2
A2		1.8	-
b	0.3	0.35	0.45
C	0.18	0.2	0.25
D	10.0	10.1	10.2
E	5.2	5.3	5.4
•	-	0.8	-
HE	7.5	7.8	8.1
L	0.4	0.0	0.8
Li	-	1.25	-
2	÷	0.65	-
Zi	-	-	0.8
y	-	-	0.1
9	0 °	-	8*
p 5	-	0.5	-
e1	-	7.62	
12	1.27		-

