

June 2007



- Pletronics' SM55 Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 0.8 to 165 MHz
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- Low Jitter
- Optimized for fastest Trise & Tfall

# Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.064 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

### **Absolute Maximum Ratings:**

Parameter	Unit
V <sub>cc</sub> Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V <sub>cc</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>cc</sub> + 0.5V
lo Output Current	+25 mA to -25 mA

### **Thermal Characteristics**

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



June 2007

#### Part Number:

SM55	45	G	E	X	- 75.0M	-XX	Packaging code or blank T250 = 250 per Tape and Reel	Part Marking:
							T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel	P <i>FF.FFF</i> M • <i>YMDxx</i>
							Frequency in MHz	or PFF.FFF M • YYWWxx
							<b>Supply Voltage V<sub>cc</sub></b> <b>x</b> = 1.8V <u>+</u> 10%	or PLE SM55
							<b>Optional Enhanced OTR</b> <b>Blank</b> = Temp. range -10 to +70°C <b>E</b> = Temp. range -40 to +85°C	• YMDxx or P5xYWWx
							Series Model	• FF.FF Mxxx or 5xYWWxx
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	FF.FFF M •PLE xxx
							Series Model	

#### Marking Legend:

PLE = Pletronics

*FF.FFF* M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day) All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Code	6	7		8		9		0	1			2			
Year	2006	2007		2008	3	2009		2010	20	11		2012			
Code		A   E	3	С	D		Е	F	G	н		J	К	L	М
Month	JA	N FE	В	MAR	AP	RI	MAY	JUN	JUL	AU	G	SEP	OCT	NOV	DEC
Code	1	2		3	4	5		6	7	8		9	Α	В	С
Day	1	2		3	4	5		6	7	8		9	10	11	12
Code	D	Е		F	G	Н		J	K	L		М	Ν	Р	R
Day	13	14		15	16	17	7	18	19	20		21	22	23	24
Code	Т	U		V	W	Х		Y	Z						
Day	25	26		27	28	29	)	30	31						

#### Codes for Date Code YMD

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June 2007

### Electrical Specification for 1.80V ±10% over the specified temperature range

Item	Min	Max	Unit	Condition
Frequency Range	0.8	165	MHz	
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for 1
" <b>44</b> "	-25	+25	1	year, shock, vibration and temperatures
" <mark>20</mark> "	-20	+20	1	
Output Waveform		CMOS		
Output High Level	90	-	%	of V <sub>CC</sub> for I <sub>OH</sub> = +2 mA <35 MHz
	70	-		of V <sub>CC</sub> for I <sub>OH</sub> = +8 mA $\geq$ 35 MHz
Output Low Level	-	10	%	of $V_{CC}$ for $I_{OL}$ = -2 mA <35 MHz
	-	30		of V <sub>CC</sub> for I <sub>OL</sub> = -8 mA $\geq$ 35 MHz
Output Symmetry	45	55	%	at 50% point of $V_{\mbox{\tiny CC}}$ (See load circuit)
Jitter Output: 1 to 15 MHz	-	6.0	pS RMS	10 Hz to 1 MHz from the output frequency
Output: 15 to 35 MHz	-	5.0	pS RMS	
Output: 35 to 50 MHz	-	4.0	pS RMS	
Output: 50 to 70 MHz	-	3.0	pS RMS	
Output: > 70 MHz	-	2.5	PS RMS	
Output: 25 to 70 MHz	-	0.7	pS RMS	12 KHz to 20 MHz from the output frequency
Output: > 70 MHz	-	0.6	pS RMS	
E/D Internal Pull-up	50	500	Kohm	to V <sub>cc</sub>
V disable	-	30	%	of $V_{cc}$ applied to pin 1
V enable	70	-	%	
Output leakage V <sub>OUT</sub> = V <sub>CC</sub>	-10	+10	uA	Pin 1 low, device disabled
V <sub>OUT</sub> = 0V	-10	+10	uA	
Standby Current I <sub>cc</sub>	-	4	uA	< 35 MHz
	-	100	uA	<u>&gt;</u> 35 MHz
Enable time	-	250	nS	Time for output to reach a logic state
Disable time	-	250	nS	Time for output to reach a high Z state
Start up time	-	10	mS	Time for output to reach specified frequency
Operating Temperature	-10	+70	°C	Standard Temperature Range
Range	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	С°	



June 2007

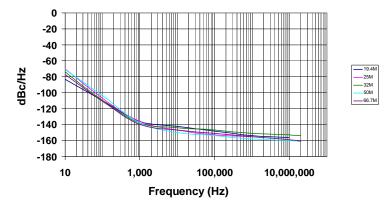
Electrical Specificati	on for	1.80v <u>+</u>	10% 0	ver the specified temp	erature range
Item	Тур	Max	Unit	Condition	
Output $T_{RISE}$ and $T_{FALL}$	1.5	3	nS	< 35 MHz	$C_{LOAD} = 15 \text{ pF}$
	1.7	3.5	nS	<u>&gt;</u> 35 MHz and < 70 MHz	20% to 80% of V <sub>cc</sub> See Load Circuit
	1.5	2.5	nS	<u>&gt;</u> 70 MHz	
	4	7	nS	< 35 MHz	C <sub>LOAD</sub> = 30 pF 20% to 80% of V <sub>CC</sub>
	2	7	nS	<u>&gt;</u> 35 MHz < 70 MHz	See Load Circuit
	6	12	nS	< 35 MHz	$C_{LOAD} = 50 \text{ pF}$
	6	11	nS	<u>&gt;</u> 35 MHz and < 45 MHz	20% to 80% of V <sub>cc</sub> See Load Circuit
$V_{cc}$ Supply Current (I <sub>cc</sub> )	2	4	mA	< 8 MHz	C <sub>LOAD</sub> = 15 pF
	2.5	5	mA	<u>&gt;</u> 8 MHz and < 16 MHz	]
	5	8	mA	<u>&gt;</u> 16 MHz and < 35 MHz	1
	-	18	mA	<u>&gt;</u> 35 MHz and < 70 MHz	1
	17	27	mA	<u>&gt;</u> 70 MHz and < 120 MHz	1
	23	37	mA	<u>&gt;</u> 120 MHz	1
	2.5	4.5	mA	< 8 MHz	C <sub>LOAD</sub> = 30 pF
	3	5	mA	<u>&gt;</u> 8 MHz and < 16 MHz	
	4	8	mA	<u>&gt;</u> 16 MHz and < 35 MHz	
	10	20	mA	<u>&gt;</u> 35 MHz and < 70 MHz	1
	2.5	4	mA	< 8 MHz	C <sub>LOAD</sub> = 50 pF
	4	6	mA	<u>&gt;</u> 8 MHz and < 16 MHz	]
	5	9	mA	<u>&gt;</u> 16 MHz and < 35 MHz	]
	13	23	mA	<u>&gt;</u> 35 MHz and < 45 MHz	]

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Specifications with Pad 1 E/D open circuit

NOTE: Not specified for 50 pF loads above 45 MHz, or 30 pF loads above 70 MHz

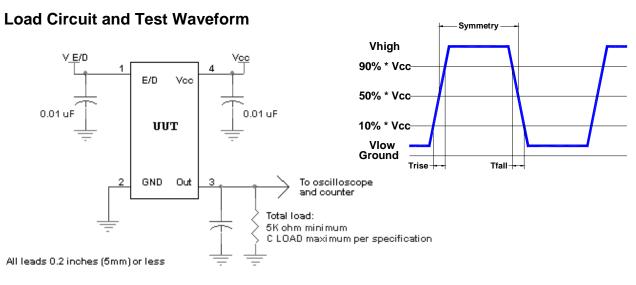
#### Typical phase noise plot for 5 oscillators at different output frequencies.



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June 2007



#### **Reliability:** Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

### **ESD** Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

#### Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant 2nd LvL Interconnect Category=e4 Max Safe Temp=260C for 10s 2X Max

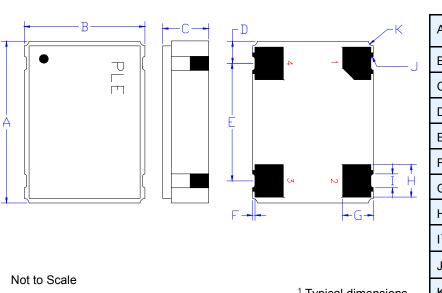
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425-776-1880



#### June 2007

#### **Mechanical:**



	1	
	Inches	mm
А	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
В	0.126 <u>+</u> 0.006	3.20 <u>+</u> 0.15
С	0.045 <u>+</u> 0.004	1.15 <u>+</u> 0.10
$D^1$	0.048	1.23
E <sup>1</sup>	0.100	2.54
F <sup>1</sup>	0.004	0.10
G <sup>1</sup>	0.050	1.27
$H^1$	0.055	1.40
I <sup>1</sup>	0.024	0.60
$J^1$	0.004	0.10R
K <sup>1</sup>	0.008	0.020R

<sup>1</sup> Typical dimensions

#### Contacts :

Gold 11.8 µinches 0.3 µm minimum over Nickel 50 to 350 µinches 1.27 to 8.89 µm

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V <sub>cc</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.

### Layout and application information



For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

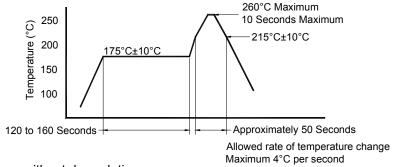
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425-776-1880



June 2007

### Reflow Cycle (typical for lead free processing)



#### The part may be reflowed 2 times without degradation.

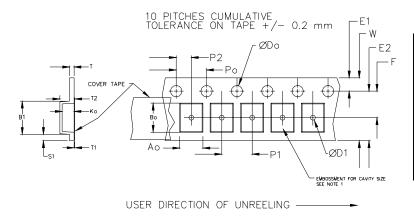
### Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

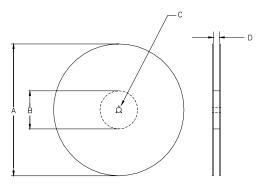
Constant Dimensions Table 1										
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max		
8mm		1.0			2.0					
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05					
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1		
24mm		1.5			<u>+</u> 0.1					

	Variable Dimensions Table 2										
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko				
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1				

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





		REE							
А	inches	7.0	10.0	13.0					
	mm	177.8	254.0	330.2					
в	inches	2.50	4.00	3.75					
	mm	63.5	101.6	95.3	Tape Width				
С	mm	13	13.0 +0.5 / -0.2						
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0				

Reel dimensions may vary from the above

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June 2007

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