

TOSHIBA CCD LINEAR IMAGE SENSOR CCD (Charge Coupled Device)

TCD1708D

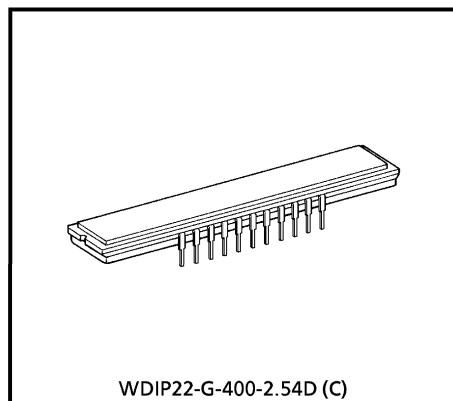
The TCD1708D is a high sensitive and low dark current 7450 elements CCD image sensor.

The sensor is designed for facsimile, imagescanner and OCR.

The device contains a row of 7450 elements photodiodes which provide a 24 lines/mm (600DPI) across a A3 size paper. The device is operated by 5 V (pulse), and 5 V power supply.

FEATURES

- Number of Image Sensing Elements : 7450 elements
- Image Sensing Element Size : 4.7 μm by 4.7 μm on 4.7 μm centers
- Photo Sensing Region : High sensitive and low voltage dark signal pn photodiode
- Clock : 2 phase (5 V)
- Package : 22 pin Cerdip



WDIP22-G-400-2.54D (C)

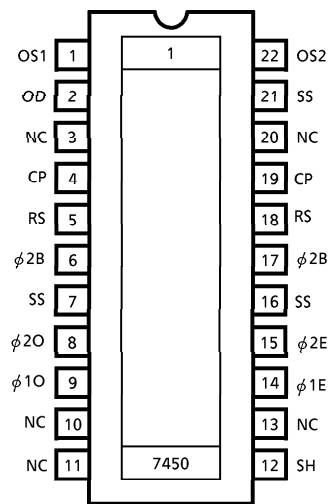
Weight : 5.2 g (Typ.)

MAXIMUM RATINGS (Note 1)

| CHARACTERISTIC | SYMBOL | RATING | UNIT |
|-----------------------|------------|--------|------|
| Clock Pulse Voltage | V_{ϕ} | -0.3~7 | V |
| Shift Pulse Voltage | V_{SH} | | |
| Reset Pulse Voltage | V_{RS} | | |
| Clamp Pulse Voltage | V_{CP} | | |
| Power Supply Voltage | V_{OD} | -0.3~7 | |
| Operating Temperature | T_{opr} | 0~60 | °C |
| Storage Temperature | T_{stg} | -25~85 | °C |

(Note 1) : All voltage are with respect to SS terminals (Ground).

PIN CONNECTIONS



(TOP VIEW)

000707EBA2

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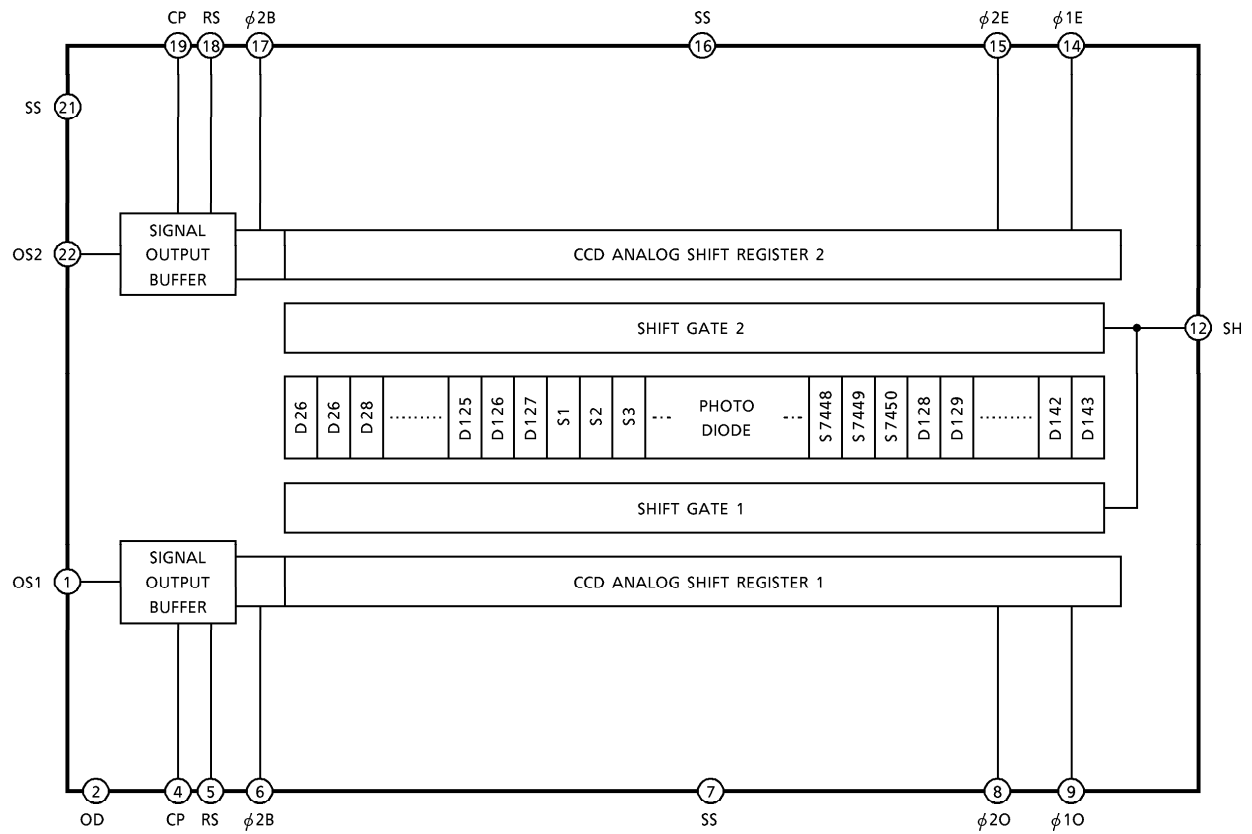
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CIRCUIT DIAGRAM



PIN NAME

| | |
|--------------|-----------------------------|
| $\phi 1E, O$ | Clock (Phase 1) |
| $\phi 2E, O$ | Clock (Phase 2) |
| $\phi 2B$ | Final Stage Clock (Phase 2) |
| SH | Shift Gate |
| RS | Reset Gate |
| CP | Clamp Gate |
| OS1 | Signal Output 1 |
| OS2 | Signal Output 2 |
| OD | Power |
| SS | Ground |
| NC | Non Connection |

OPTICAL / ELECTRICAL CHARACTERISTICS

($T_a = 25^\circ\text{C}$, $V_{OD} = 5\text{ V}$, $V_\phi = V_{SH} = V_{RS} = V_{CP} = 5\text{ V}$ (PULSE), $f_\phi = 1\text{ MHz}$,
 t_{INT} (INTEGRATION TIME) = 10 ms, LIGHT SOURCE = DAYLIGHT FLUORESCENT LAMP,
 LOAD RESISTANCE = 100 k Ω)

| CHARACTERISTIC | SYMBOL | MIN. | TYP. | MAX. | UNIT | NOTE |
|-------------------------------|---------------------|------|------|------|------------|----------|
| Sensitivity | R | 12 | 15 | 18 | V/lx·s | |
| Photo Response Non Uniformity | PRNU | — | 4 | 10 | % | (Note 2) |
| | PRNU (3) | — | 6 | 12 | mV | (Note 8) |
| Saturation Output Voltage | V_{SAT} | 1.0 | 1.3 | — | V | (Note 3) |
| Saturation Exposure | SE | 0.05 | 0.08 | — | lx·s | (Note 4) |
| Dark Signal Voltage | V_{DRK} | — | 0.5 | 3 | mV | (Note 5) |
| Dark Signal Non Uniformity | DSNU | — | 0.5 | 10 | mV | (Note 5) |
| DC Power Dissipation | P_D | — | 80 | 160 | mW | |
| Total Transfer Efficiency | TTE | 92 | 98 | — | % | |
| Output Impedance | Z_o | — | 0.2 | 1 | k Ω | |
| Dynamic Range | DR | — | 2600 | — | — | (Note 6) |
| DC Signal Output Voltage | V_{OS1} | 2.0 | 2.5 | 3.5 | V | (Note 7) |
| | V_{OS2} | 2.0 | 2.5 | 3.5 | | |
| DC Differential Error Voltage | $ V_{OS1}-V_{OS2} $ | — | — | 300 | mV | |
| Random Noise | ND_σ | — | 1.0 | — | mV | (Note 9) |

(Note 2) : Measured at 50% of SE (Typ.)

$$\text{Definition of PRNU : PRNU} = \frac{\Delta\bar{x}}{\bar{x}} \times 100 (\%)$$

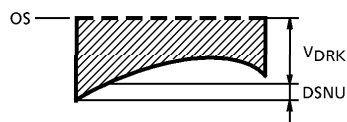
Where \bar{x} is average of total signal outputs and $\Delta\bar{x}$ is maximum deviation from \bar{x} under uniform illumination. (Channel 1)

In the case of 3725 elements (Channel 2), the condition is the same as above too.

(Note 3) : V_{SAT} is defined as minimum saturation output voltage of all effective pixels.

$$\text{(Note 4) : Definition of SE : SE} = \frac{V_{SAT}}{R} (\text{lx}\cdot\text{s})$$

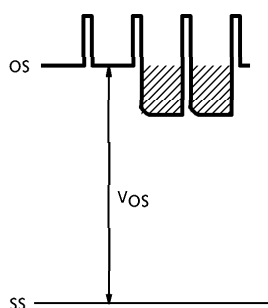
- (Note 5) : V_{DRK} is defined as average dark signal voltage of all effective pixels.
 $DSNU$ is defined as different voltage between V_{DRK} and V_{MDK} when V_{MDK} is maximum dark signal voltage.



- (Note 6) : Definition of DR : $DR = \frac{V_{SAT}}{V_{DRK}}$

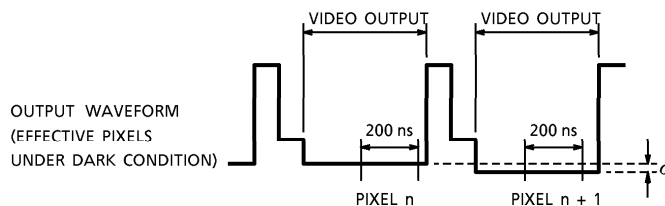
V_{DRK} is proportional to t_{INT} (Integration Time).
 So the shorter t_{INT} condition makes wider DR values.

- (Note 7) : DC signal output voltage and DC compensation output voltage are defined as follows:



- (Note 8) : PRNU (3) is defined as maximum voltage with next pixel, where measured 5% of SE (Typ.)

(Note 9) : Random noise is defined as the standard deviation (sigma) of the output level difference between two adjacent effective pixels under no illumination (i.e. dark condition) calculated by the following procedure.



- 1) Two adjacent pixels (pixel n and n + 1) in one reading are fixed as measurement points.
- 2) Each of the output levels at video output periods averaged over 200 nanosecond period to get V_n and V_{n+1} .
- 3) V_{n+1} is subtracted from V_n to get ΔV .

$$\Delta V = V_n - V_{n+1}$$
- 4) The standard deviation of ΔV is calculated after procedure 2) and 3) are repeated 30 times (30 readings).

$$\overline{\Delta V} = \frac{1}{30} \sum_{i=1}^{30} |\Delta V_i| \quad \sigma = \sqrt{\frac{1}{30} \sum_{i=1}^{30} (|\Delta V_i| - \overline{\Delta V})^2}$$

- 5) Procedure 2), 3) and 4) are repeated 10 times to get 10 sigma values.

$$\overline{\sigma} = \frac{1}{10} \sum_{j=1}^{10} \sigma_j$$

- 6) $\overline{\sigma}$ value calculated using the above procedure is observed $\sqrt{2}$ times larger than that measured relative to the ground level. So we specify the random noise as follows.

$$\text{Random noise} = \frac{1}{\sqrt{2}} \overline{\sigma}$$

OPERATING CONDITION

| CHARACTERISTIC | | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|-------------------------------|-----------|------------------|----------------------------|---------------------|---------------------|------|
| Clock Pulse Voltage | "H" Level | $V_{\phi 1E, O}$ | 4.75 | 5 | 5.25 | V |
| | "L" Level | $V_{\phi 2E, O}$ | 0 | — | 0.3 | |
| Final Stage Clock Voltage | "H" Level | $V_{\phi 2B}$ | 4.75 | 5 | 5.25 | V |
| | "L" Level | | 0 | — | 0.3 | |
| Shift Pulse Voltage (Note 10) | "H" Level | V_{SH} | $V_{\phi 0, E "H"} - 0.25$ | $V_{\phi 0, E "H"}$ | $V_{\phi 0, E "H"}$ | V |
| | "L" Level | | 0 | — | 0.3 | |
| Reset Pulse Voltage | "H" Level | V_{RS} | 4.75 | 5 | 5.25 | V |
| | "L" Level | | 0 | — | 0.3 | |
| Clamp Pulse Voltage | "H" Level | V_{CP} | 4.75 | 5 | 5.25 | V |
| | "L" Level | | 0 | — | 0.3 | |
| Power Supply Voltage | | V_{OD} | 4.75 | 5 | 5.25 | V |

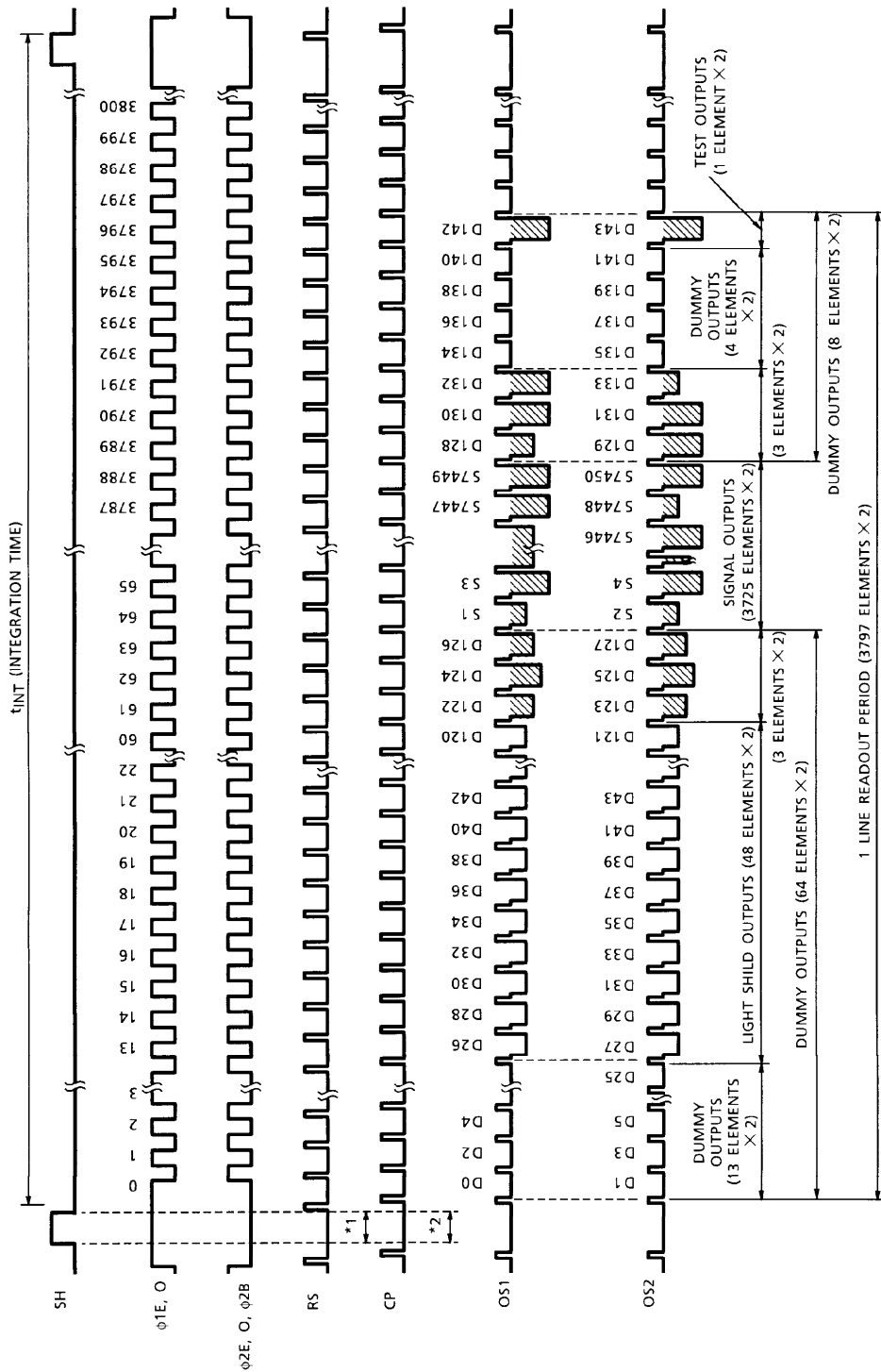
(Note 10) : $V_{\phi 0, E "H"}$ means the high level voltage of $V_{\phi 0}$ and $V_{\phi E}$ when SH pulse is high level.

CLOCK CHARACTERISTICS (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|-------------------------------|--------------|------|------|------|------|
| Clock Pulse Frequency | f_{ϕ} | — | 1 | 15 | MHz |
| Reset Pulse Frequency | f_{RS} | — | 1 | 15 | MHz |
| Clock Capacitance (Note 11) | $C_{\phi E}$ | — | 300 | — | pF |
| | $C_{\phi O}$ | — | 300 | — | |
| Final Stage Clock Capacitance | $C_{\phi B}$ | — | 10 | — | pF |
| Shift Gate Capacitance | C_{SH} | — | 300 | — | pF |
| Reset Gate Capacitance | C_{RS} | — | 10 | — | pF |
| Clamp Gate Capacitance | C_{CP} | — | 10 | — | pF |

(Note 11) : $V_{OD} = 5V$

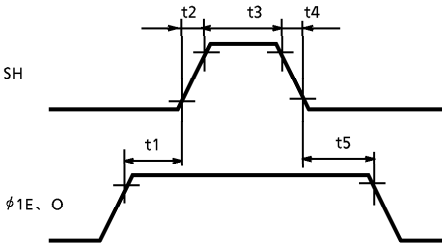
TIMING CHART



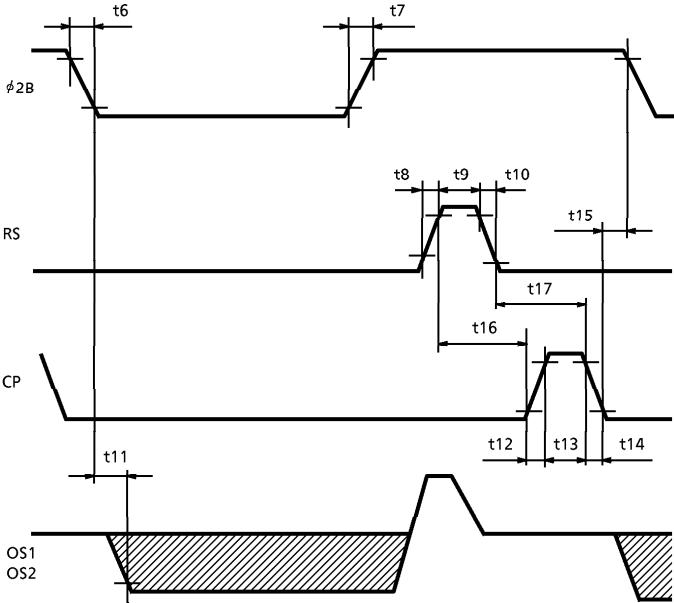
*1 RS Period
*2 CP Period

TIMING REQUIREMENTS

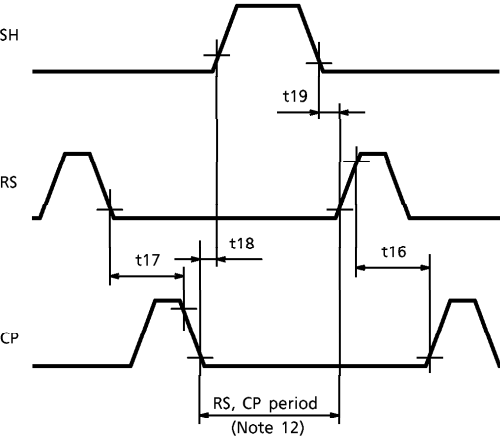
SH, $\phi 1$ Timing



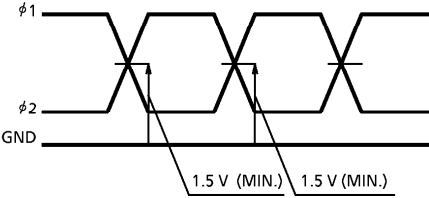
$\phi 2$, RS, CP, OS Timing



SH, RS, CP Timing



$\phi 1$, $\phi 2$ CROSS POINT



(Note 12) : Each RS and CP pins put to Low level during this period.

| CHARACTERISTIC | SYMBOL | MIN. | TYP. (Note 13) | MAX. | UNIT |
|--|----------|------|-------------------|------|------|
| Pulse Timing of SH and $\phi 1E$, $\phi 1O$ | t1, t5 | 200 | 500 | — | ns |
| SH Pulse Rise Time, Fall Time | t2, t4 | 0 | 50 | — | ns |
| SH Pulse Width | t3 | 1000 | 1500 | 5000 | ns |
| $\phi 2B$ Pulse Rise Time, Fall Time | t6, t7 | 0 | 20 | — | ns |
| RS Pulse Rise Time, Fall Time | t8, t10 | 0 | 20 | — | ns |
| RS Pulse Width | t9 | 10 | 100 | — | ns |
| Video Data Delay Time (Note 14) | t11 | — | 20 | — | ns |
| CP Pulse Rise Time, Fall Time | t12, t14 | 0 | 20 | — | ns |
| CP Pulse Width | t13 | 10 | 200 | — | ns |
| Pulse Timing of $\phi 2B$ and CP | t15 | 0 | 50 | — | ns |
| Pulse Timing of RS and CP | t16 | 0 | 0 | — | ns |
| | t17 | 10 | 100 | — | |
| Pulse Timing of SH and CP | t18 | 200 | — | — | ns |
| Pulse Timing of SH and RS | t19 | 200 | — | — | ns |

(Note 13) : TYP. is the case of $f_{RS} = 1.0 \text{ MHz}$

(Note 14) : TLoad Resistance is $100 \text{ k}\Omega$

PRECAUTIONS FOR USE OF CCD IMAGE SENSOR**1. Static Electricity**

CCD Image Sensor is protected against static electricity, but inferior puncture mode device due to static electricity is sometimes detected. In handling the device, it is necessary to execute the following static electricity preventive measures, in order to prevent the trouble rate increase of the manufacturing system due to static electricity.

- a. Prevent the generation of static electricity due to friction by making the work with bare hands or by putting on cotton gloves and non-charging working clothes.
- b. Discharge the static electricity by providing earth plate or earth wire on the floor, door or stand of the work room.
- c. Ground the tools such as soldering iron, radio cutting plier or pincette.

It is not necessarily required to execute all precaution items for static electricity.

It is all right to mitigate the precautions by confirming that the trouble rate within the prescribed range.

2 Window Glass

As the dust and station on the glass window of the package will cause black flow on the picture, never fail to clean the glass surface before using. (Blow compressed vapor, and wipe off the dust, and dirt with soft cloth or paper slightly moistened with alcohol).

Fully take care for the handling of the device as the window glass will break or a strong friction is given to the window glass surface.

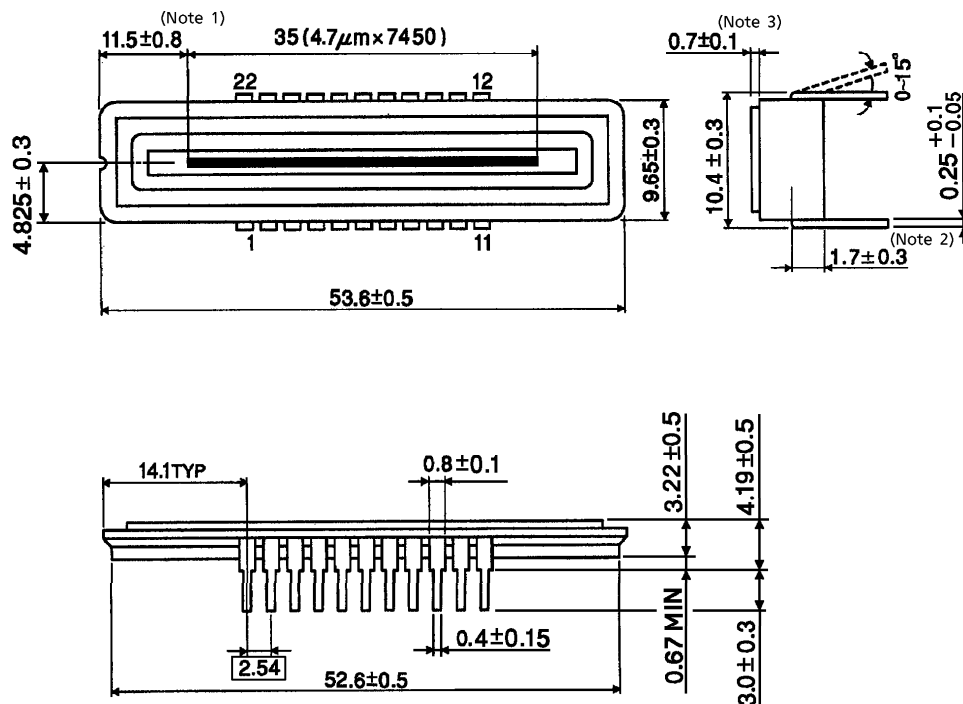
3. Incident Light

CCD image sensor has sensitivity in a wide range zone of light wave length, but its characteristics will sometimes widely change when used with long wave length input light outside the visual light zone.

PACKAGE DIMENSIONS

WDIP22-G-400-2.54D (C)

Unit in mm



- (Note 1) : No. 1 SENSOR ELEMENT (S1) TO EDGE OF PACKAGE.
- (Note 2) : TOP OF CHIP TO BOTTOM OF PACKAGE.
- (Note 3) : GLASS THICKNES ($n = 1.5$)

Weight : 5.2 g (Typ.)