MOS FIELD EFFECT TRANSISTOR

μ**ΡΑ503Τ**

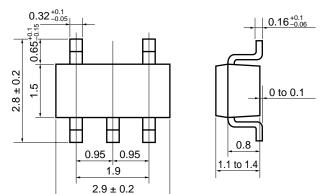
P-CHANNEL MOS FET (5-PIN 2 CIRCUITS)

The μ PA503T is a mini-mold device provided with two MOS FET circuits. It achieves high-density mounting and saves mounting costs.

FEATURES

NEC

- Two source common MOS FET circuits in package the same size as SC-59
- Complement to μPA502T
- Automatic mounting supported



PACKAGE DIMENSIONS

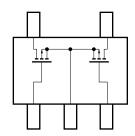
(in millimeters)

ABSOLUTE MAXIMUM RATINGS (T_A = 25 $^{\circ}$ C)

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PARAMETER	SYMBOL	RATINGS	UNIT
Drain to Source Voltage	Vdss	-50	V
Gate to Source Voltage	Vgss	∓16	V
Drain Current (DC)	D(DC)	-100	mA
Drain Current (pulse)	D(pulse)*	-200	mA
Total Power Dissipation	Рт	300 (TOTAL)	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

* PW \leq 10 ms, Duty Cycle \leq 50 %

PIN CONNECTION (Top view)

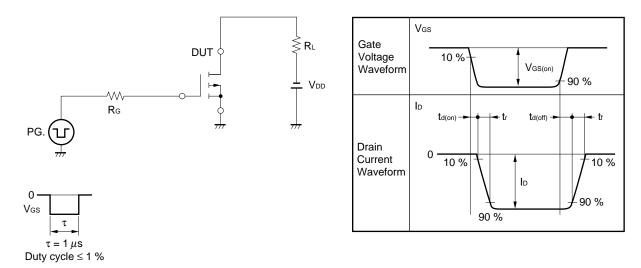


ELECTRICAL	CHARACTERISTICS	(T _A = 25 °C)
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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	Ibss	$V_{DS} = -50 V, V_{GS} = 0$			-1.0	μA
Gate Leakage Current	lgss	V _{GS} = ∓16 V, V _{DS} = 0			∓10	μΑ
Gate Cut-off Voltage	VGS(off)	$V_{DS} = -5.0 \text{ V}, \text{ ID} = -1.0 \ \mu\text{A}$	-1.5	-1.9	-2.5	V
Forward Transfer Admittance	y _{fs}	$V_{DS} = -5.0 \text{ V}, \text{ ID} = -10 \text{ mA}$	15			mS
Drain to Source On-State Resistance	RDS(on)1	$V_{GS} = -4.0 \text{ V}, \text{ ID} = -10 \text{ mA}$		60	100	Ω
Drain to Source On-State Resistance	RDS(on)2	$V_{GS} = -10 \text{ V}, \text{ ID} = -10 \text{ mA}$		40	60	Ω
Input Capacitance	Ciss	$V_{DS} = -5.0 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1.0 \text{ MHz}$		17		pF
Output Capacitance	Coss			9		pF
Reverse Transfer Capacitance	Crss			1		pF
Turn-On Delay Time	td(on)	$V_{\text{GS(on)}} = -4.0 \text{ V}, \text{ R}_{\text{G}} = 10 \ \Omega$		45		ns
Rise Time	tr	$V_{DD} = -5.0 \text{ V}, \text{ ID} = -10 \text{ mA}$		75		ns
Turn-Off Delay Time	t _{d(off)}	RL = 500 Ω		25		ns
Fall Time	tr			80		ns

Marking: CA

SWITCHING TIME MEASUREMENT CIRCUIT AND MEASUREMENT CONDITIONS (RESISTANCE LOADED)



Free air

100

125

 $V_{DS} = -5.0 V$

-10

T_A = −25 °C

-20

25 °C

75 °C 150 °C Pulsed measurement

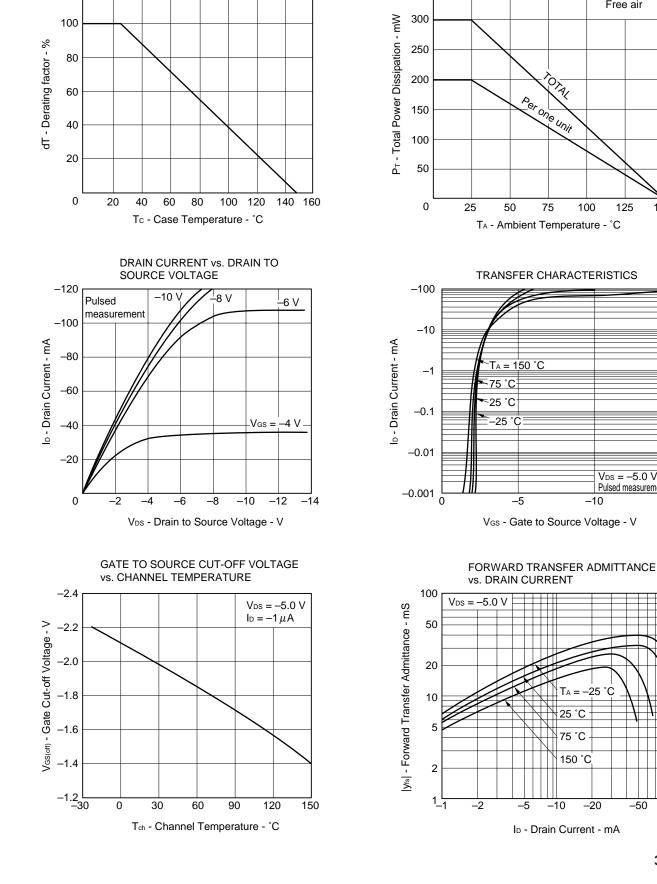
-15

150

TOTAL POWER DISSIPATION vs.

AMBIENT TEMPERATURE

350

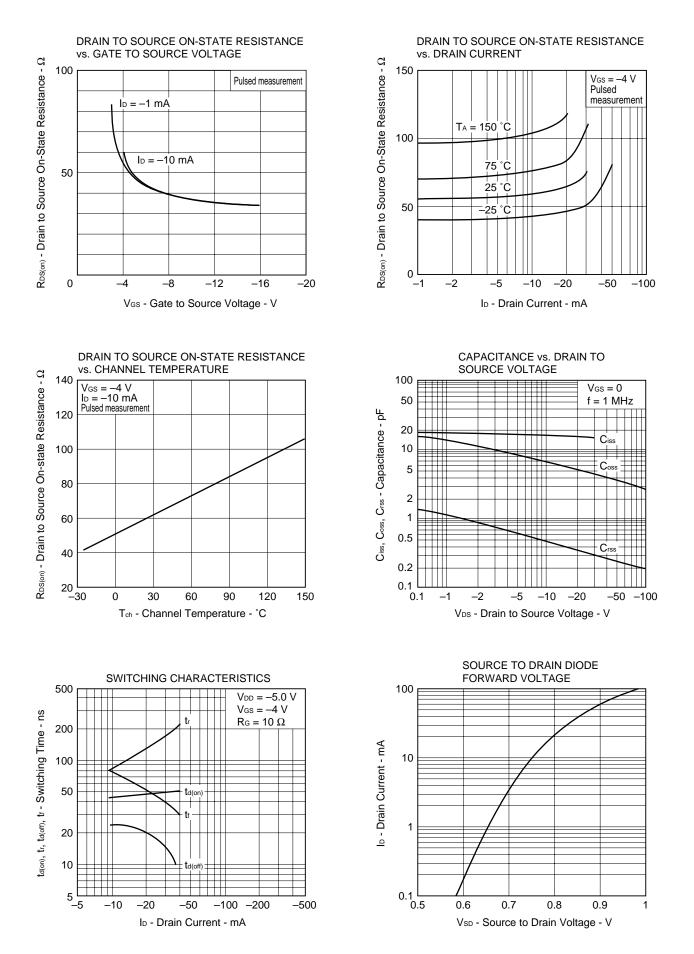


SAFE OPERATING AREA

DERATING FACTOR OF FORWARD BIAS

-100

-50



REFERENCE

Document Name	Document No.	
NEC semiconductor device reliability/quality control system	TEI-1202	
Quality grade on NEC semiconductor devices	IEI-1209	
Semiconductor device mounting technology manual	C10535E	
Guide to quality assurance for semiconductor devices	MEI-1202	
Semiconductor selection guide	X10679E	

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Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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Anti-radioactive design is not implemented in this product.

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