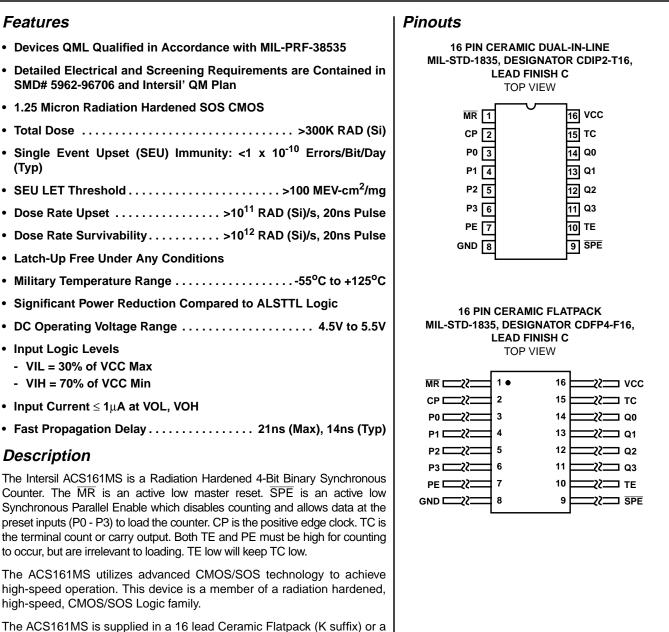


ACS161MS

Radiation Hardened 4-Bit Synchronous Counter

January 1996

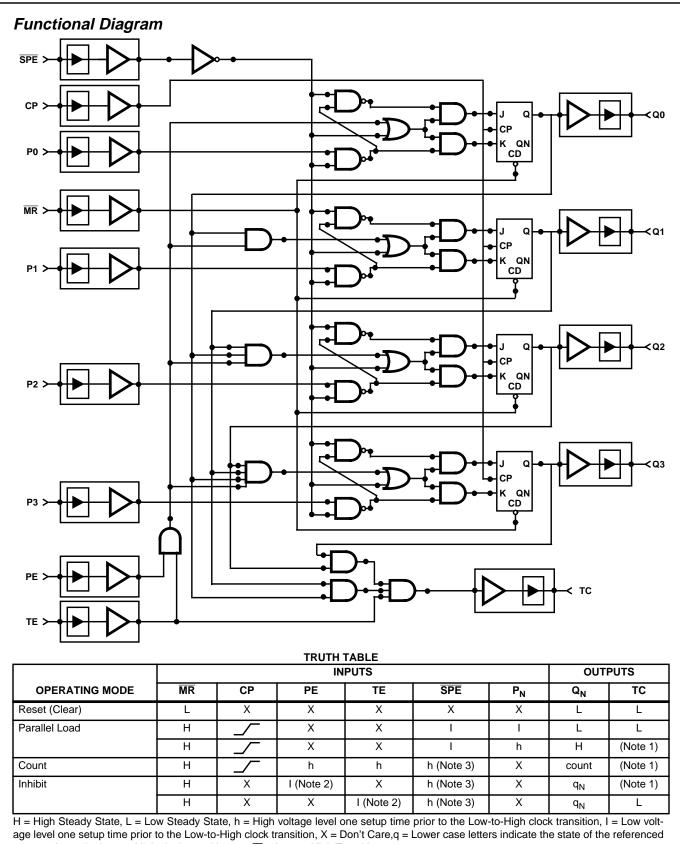


Ceramic Dual-In-Line Package (D suffix).

| PART NUMBER | TEMPERATURE RANGE | SCREENING LEVEL | PACKAGE |
|-----------------|-------------------|-----------------------|--------------------------|
| 5962F9670601VEC | -55°C to +125°C | MIL-PRF-38535 Class V | 16 Lead SBDIP |
| 5962F9670601VXC | -55°C to +125°C | MIL-PRF-38535 Class V | 16 Lead Ceramic Flatpack |
| ACS161D/Sample | 25°C | Sample | 16 Lead SBDIP |
| ACS161K/Sample | 25°C | Sample | 16 Lead Ceramic Flatpack |
| ACS161HMSR | 25°C | Die | Die |

CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. http://www.intersil.com or 407-727-9207 | Copyright © Intersil Corporation 1999 Spec Number 518818 File Number 3600.1

ACS161MS



output prior to the Low-to-High clock transition, ____ = Low-to-High Transition. NOTES:

1. The TC output is High when TE is High and the counter is at Terminal Count (HHHH).

2. The High-to-Low transition of PE or TE should only occur while ZCP is High for conventional operation.

3. The Low-to-High transition of SPE should only occur while CP is High for conventional operation.

4. The TC output is High when TE is High and the counter is at Terminal Count (HHHH).

Die Characteristics

DIE DIMENSIONS:

88 mils x 88 mils 2240mm x 2240mm

METALLIZATION:

Type: AlSi Metal 1 Thickness: 7.125kÅ ±1.125kÅ Metal 2 Thickness: 9kÅ ±1kÅ

GLASSIVATION:

Type: SiO₂ Thickness: 8kÅ ±1kÅ

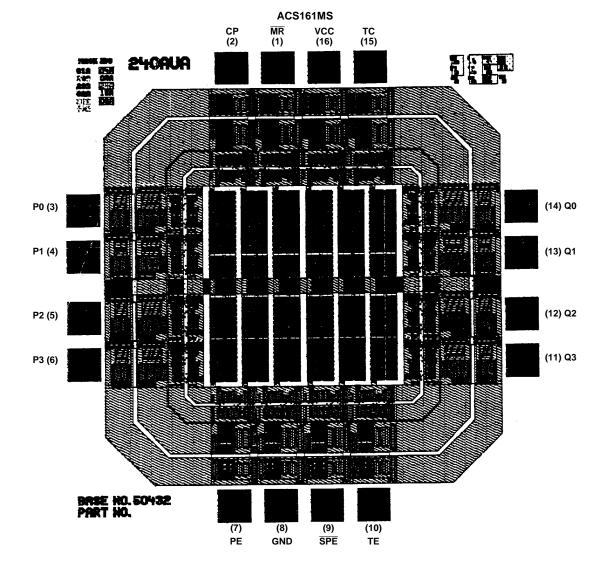
WORST CASE CURRENT DENSITY:

 $< 2.0 \text{ x} 10^{5} \text{A/cm}^{2}$

BOND PAD SIZE:

110μm x 110μm 4.3 mils x 4.3 mils

Metallization Mask Layout



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