



S P E C I	F I C A T I O N S		
Dielectris & Values	NPO X7R Y5V Z5U consult product pages of catalog for cap		
	ranges and voltage rating		
Terminations	Tin / Nickel		
Voltage	16, 25, 50, 63 VDC		
Packing	tape and reel (0402, 0603, 0805, 1206, 1210, 1812, 2220)		
Capacitance	0.5pF ~ 10uF		
Tolerance	$\pm 0.1 \text{pF} \sim +80-20\%$		
Operating Temperature Range	NPO: -55 ~ +125℃		
	X7R: -55 ~ +125℃		
	Y5V: -30 ~ +85°C		
	NPO: The capacitor of this kind dielectric material is considered as Class I capacitor, including general capacitor and high frequency NPO capacitor • The electrical properties of NPO capacitor are the most stable one and have little change with temperature, voltage and time. They are suited for applications where low-losses and high-stability are required, such as filters, oscillators, and timing circuits.		
Types of Capacitor and Dielectric Material	X7R \ X5R: X7R \ X5R material is a kind of material has high dielectric constant. The capacitor made of this kind material is considered as Class II capacitor whose capacitance is higher than that of class I . These capacitors are classified as having a semi-stable temperature characteristic and used over a wide temperature range, such in these kinds of circuits, DC-blocking, decoupling, bypassing, frequency discriminating etc.		
	Y5V: The capacitor made of this kind of material is the highest dielectric constant of all ceramic capacitors. They are used over a moderate temperature range in application where high capacitance is required because of its unstable temperature coefficient, but where moderate losses and capacitance changes can be tolerated. Its capacitance and dissipation factors are sensible to measuring conditions, such as temperature and voltage, etc		



## **Specification and Test Condition:**

### 1. Appearance

Dielectrics	Specification	Testing Condition
NPO/X7R/X5R/Y5V	No defects or abnormalities	Visual inspection.
2 B: :		

#### 2. Dimensions

Dielectrics	Specification	Testing Condition
NPO/X7R/X5R/Y5V	Within the specified dimensions	Using calipers on micrometer

### 3. Capacitance

Dielectrics	Specification	Testing Condition
	Within the specified tolerance	1.0±0.2Vrms, 1MHz±10%
NPO	B:±0.1pF;C:±0.25pF;D:±0.5pF;J:	(C>1000 pF, 1.0±0.2Vrms, 1KHz±10%,)
	±5%	25°C ∘
	Within the specified tolerance	1.0±0.2Vrms, 1KHz±10%
X7R/X5R	J: ±5%; K: ±10%; M: ±20%	(Cp>10uF,0.5±0.1Vrms,120±24Hz)
		at 25°C,48hrs after annealing
	Within the specified tolerance	1.0±0.2Vrms, 1KHz±10%
Y5V	M: ±20%; Z: -20%, +80%	(Cp>10uF,0.5±0.1Vrms,120±24Hz)
		at 25°C, 48hrs after annealing

#### 4. Dissipation Factor

Bissipation i actor		
Dielectrics	Specification	Testing Condition
NPO	Cp<30pF, Q≥400+20Cp;	1.0±0.2Vrms,1MHz±10% ,25°C
	Cp≥30pF, Q≥1000	(Cp>1000pF,1.0±0.2Vrms,1KHz±10%)
	$U_R \ge 25V$ , DF $\le 2.5\%$	1.0±0.2Vrms, 1KHz±10%,
X7R/X5R	$U_R = 16V$ , DF $\leq 3.5\%$	(Cp>10uF,0.5±0.1Vrms,120±24Hz)
	$U_R \le 10V$ , DF $\le 5.0\%$	at 25°C,48hrs after annealing
	$U_R \ge 25V$ , DF $\le 7.0\%$ (C $\le 1.0 \mu$ F)	1.0±0.2Vrms, 1KHz±10%,
Y5V	DF ≤9.0% (C≥1.0 <i>μ</i> F)	(Cp>10uF,0.5±0.1Vrms,120±24Hz)
	$U_R = 16V$ , DF $\leq 9.0\%$	at 25°C,48hrs after annealing
	$U_R \le 10V$ , DF $\le 12.5\%$	



#### 5. Insulation Resistance

Dielectrics	Specification	Testing Condition
NPO/X7R/	More than $10 \text{ G}\Omega$ or $500\Omega \cdot \text{F}$ ,	Rated voltage for 60±5sec, at 25°C
X5R/Y5V	whichever is smaller.	

### 6. Dielectric Strength

Dielectrics	Specification	Testing Condition
		No failure shall be observed when 300%
		(NPO);250%(X7R/X5R/Y5V)of the rated
NPO /X7R/X5R/Y5V	No defects or abnormalities.	voltage is applied between the terminations fo
		1 to 5 seconds, provided the charge /discharge
		current is less than 500mA

## 7. Temperature Coefficient of Capacitance

Dielectrics	Specification	Testing	Testing Condition		
		Measu	re capacitano	ce under fo	ollow table list
	Temperature coefficient within	temper	rature:		
NPO	±30ppm/°C	STEP	NPO, X7R	X5R	Y5V
	Cp drift within $\pm 0.2\%$ or $\pm 0.05$ pF	1	$25\pm2$	$25 \pm 2$	25 ±2
		2	-55±3	-55±3	-30±3
		3	$25\pm2$	$25 \pm 2$	25 ±2
X7R/X5R	Capacitance change within ±15%	4	125±3	85±3	85±3
		5	25 ±2	25 ±2	25 ±2
		1) NPO			
		The capacitance drift is calculated by			
		dividing the differences between the			
Y5V		maxim	num and mini	imum mea	sured values in
	Capacitance change within +22%, -82%	The te	the Capacitar		s determined red in step 3 as
		2) X7R ,X5R and Y5V			
		The ranges of capacitance change compared			
		within the above 25°C value over the			
		temperature ranges shall be within the			
		specifi	ed ranges.		

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#### 8. Adhesion

Dielectrics	Specification	Testing Condition	
NPO X7R/X5R Y5V	No removal of the terminations or other defect shall occur.	The pressurizing force shall be 10N (=1000g*f) and the duration of application shall be 10±1sec.  hooked jig board chip cross-section	

## 9. Solderability of Termination

Dielectrics	Specification	Testing Condition
NPO	95% min. coverage of both terminal	Solder temperature: 230±5°C Dipping time: 2±1 seconds.
X7R/X5R	electrodes and less than 5% have pin	Completely soak both terminal electrodes in
Y5V	holes or rough spots.	solder

## 10. Resistance to leaching

Dielectrics	Specification	Testing Condition
NPO	95% min. coverage of both terminal	Solder temperature: 270±5°C Dipping time: 10±1 seconds.
X7R/X5R	electrodes and less than 5% have pin	Completely soak both terminal electrodes in
Y5V	holes or rough spots.	solder
	No remarkable visual damage.	



## 11. Bending

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage	Solder the capacitor on testing substrate and put it on testing stand. The middle part of
	Cp change $\leq \pm 5\%$ or $\leq 0.5$ pF	substrate shall successively be pressurized
X7R/X5R	No remarkable visual damage Cp change ≤ ±12.5%	by pressuring rod at a rated of about 1.0mm/sec. Until the deflection become means of the 1.0mm.
Y5V	No remarkable visual damage Cp change $\leq \pm 30\%$	R230

### 12. Resistance to Soldering Heat

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change within ±2.5% or ±0.25pF, whichever is larger. DF meets initial standard value. IR meets initial standard value.	Soldering temperature: 270±5°C Preheating: 120~150°C 60sec. Dipping time: 10±1 seconds. Measurement to be made after being kept at room temperature for 24±2 (C0G) or
X7R/X5R	No remarkable visual damage Cp change within ±5% DF meets initial standard value. IR meets initial standard value.	48±4(X7R, X5R, Y5V) hours. Recov ery for the following period under the standard condition after test. *Initial measurement for high dielectric constant type
Y5V	No remarkable visual damage Cp change within ±20% DF meets initial standard value. IR meets initial standard value.	Perform a heat treatment at 140~150°C for 1hr and let sit for 48±4hrs at room temperature. Perform the initial measurement.



## 13. Temperature Cycle

Dielectrics	Specification	Testing Condition		
		To perform 5 cycles of the stated environment:		
		Step	Temperature	Time
NPO	No remarkable visual damage Cp change within ±2.5% or ±0.25pF, whichever is larger.	1		30min
THE C			Min. operating Temp.+0/-3°C	
		2	25°℃	2∼3 min
		3		30 min
			Max. operating Temp.+0/-3°C	
		4	25°℃	2~3 min
X7R/X5R	No remarkable visual damage Cp change within ±7.5%	Measurement to be made after being kept at room temperature for 24±2hrs (C0G) or 48±4hrs (X7R, X5R, Y5V) at room temperature, then measure. *Initial measurement for high dielectric constant type Perform a heat treatment at 140~150°C for 1hr and let sit for 48±4hrs at room temperature.		
		Perform the initial measurement.		

### 14. Moisture Resistance, steady state

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change within $\pm 5\%$ or $\pm 0.5$ pF, whichever is larger. Cp<10pF, Q≥200+10Cp; $10$ $\leq$ Cp<30pF, Q≥275+2.5Cp Cp $\geq$ 30pF, Q $\geq$ 350 R*C $\geq$ 1000M $\Omega$ or $50\Omega$ ·F, whichever is smaller	Test temperature: $40\pm2^{\circ}$ C Humidity: $90\sim95\%$ RH Testing time: $500\pm12$ hrs Measurement to be made after being kept at
X7R/X5R	Cp change within $\pm 12.5\%$ DF:Not more than 2 times of initial value $R*C \ge 1000M\Omega$ or $50\Omega \cdot F$ , whichever is smaller	room temperature for 24±2hrs (COG) or 48±4hrs (X7R, X5R, Y5V)  *Initial measurement for high dielectric constant type  Perform a heat trackment at 140, 150°C, for
Y5V	No remarkable visual damage Cp change within $\pm 30\%$ DF:Not more than 1.5 times of initial value $R*C \ge 1000M\Omega$ or $50\Omega \cdot F$ , whichever is smaller	Perform a heat treatment at 140~150°C for 1hr and let sit for 48±4hrs at room temperature.  Perform the initial measurement.

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## 15. Damp heat with load

Dielectrics	Specification	Testing Condition		
NPO	No remarkable visual damage Cp change≤±7.5% or ±0.75pF, whichever is larger. Cp<30pF, Q≥100+10/3*Cp Cp≥30pF, Q≥200 R*C≥500MΩ or 25Ω·F, whichever is smaller	Test temperature: $40\pm2^{\circ}$ C Humidity: $90\sim95\%$ RH Voltage: $100\%$ of the rated voltage Testing time: $500\pm12$ hrs		
X7R/X5R	No remarkable visual damage Cp change≤±12.5% DF:Not more than 2 times of initial value R*C≥500MΩ or 25Ω·F, whichever is smaller	Measurement to be made after being kept at room temperature for 24±2hrs (C0G) or 48±4hrs (X7R, X5R, Y5V)  *Apply the rated DC voltage for 1 hour at		
Y5V	No remarkable visual damage Cp change≤±30% DF:Not more than 1.5 times of initial value R*C≥500MΩ or 25Ω·F, whichever is smaller	40±2°C. Remove and let sit for 48±4hrs at room temperature. Perform the initial measurement.		

### 16. Life Test

Dielectrics	Specification	Testing Condition
NPO	No remarkable visual damage Cp change≤±3% or ±0.3pF, whichever is larger. Q≥350 (Cp≥30 PF) Q≥275+(2.5* Cp) (10 pF≤Cp<30 PF) Q≥200+10*Cp (Cp<10 PF) R*C≥1000MΩ or 50Ω·F, whichever is smaller	Test temperature: Max. Operating Temp. ±3°C Voltage: 200% of the rated voltage Testing time: 1000 hrs  Measurement to be made after being kept at room temperature for 24±2hrs (C0G) or
X7R/X5R	No remarkable visual damage Cp change≤±12.5% DF:Not more than 2 times of initial value R*C≥1000MΩ or 50Ω·F, whichever is smaller	*Initial measurement for high dielectric constant type Apply 200% of the rated DC voltage for one hour at the maximum operating temperature
Y5V	No remarkable visual damage Cp change≤±30% DF:Not more than 1.5 times of initial value R*C≥1000MΩ or 50Ω·F, whichever is smaller	hour at the maximum operating temperature $\pm 3^{\circ}$ °C. Remove and let sit for 48±4hrs at room temperature. Perform the initial measurement



## **Packing**

1. Tape Packing

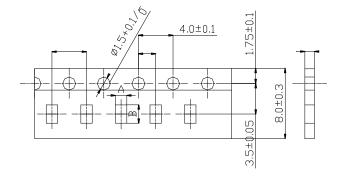
Paper Tape: Standard taping (8mm paper width) suitable to 0603,0805,4Kpcs/reel

To 0402, 10Kpcs/reel.

Plastic Tape: Suitable 0805, 1206 sizes, for chip thickness over 0.95 mm, 4Kpcs/reel

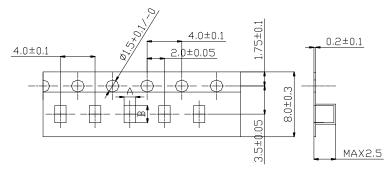
or 3Kpcs/reel are available.

2. Dimensions of Packing Paper:



Туре	A	В	С	D	T
0402	0.65±0.10	1.15±0.10	2.0±0.05	2.0±0.05	0.8max
0603	1.05±0.10	1.85±0.10	4.0±0.10	2.0±0.10	1.1max
0805	1.55±0.15	2.3±0.15	4.0±0.10	2.0±0.10	1.1max
1206	1.95±0.15	3.5±0.15	4.0±0.10	2.0±0.10	1.1max

#### 3. Dimensions of Embossed Packing

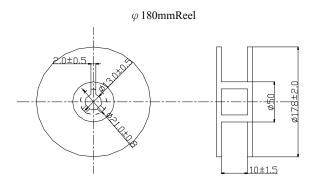


A: 1.45±0.20 B: 2.25±0.20 (0805) A: 1.95±0.20 B: 3.50±0.20 (1206)

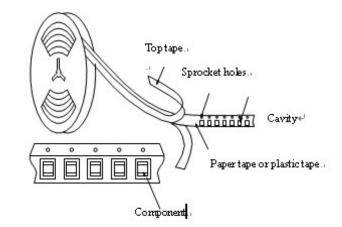
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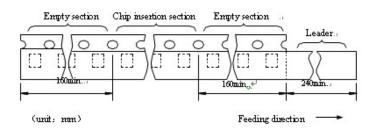


#### 4. Dimensions of Reel:



### 5. Taping Figure

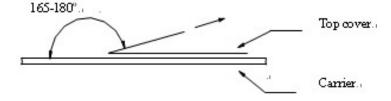






#### 6. Taping Method

- 1) Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.
- ② The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.
- 3 Part of the leader and part of the empty tape shall be attached to the end of the tape as follows.
- (4) Missing capacitors number within 0.1% of the number per reel or 1pc, whichever is greater, and are not continuous.
- ⑤The top tape and bottom tape shall not protrude beyond the edges of the tape and shall not cover sprocket holes.
- 6 Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm.
- 7 Peeling off force: 0.1 to 0.6N in the direction shown down.



Note: Specification are subject to change without notice. For more detail and update, please visit our website.