

Marketing Bulletin

DATE: Tuesday, February 01, 2000
TO: Affected Customers
FROM: Marketing
RE: EC17 Series Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the EC17 series Ecliptek oscillator effective Tuesday, February 01, 2000.

In compliance with our End of Life (EOL) policy, this notice will serve as advanced notice of product termination. New orders will not be accepted after Tuesday, February 01, 2000, with delivery to be conclude by Tuesday, February 01, 2000.

If there are any questions pertaining to this bulletin, please contact your Ecliptek sales representative. Thank you again for your cooperation.

Ecliptek Marketing

STANDARD SPECIFICATIONS

Frequency Range:	70.000MHz to 125.000MHz (See drawing number TEN08-016-002 for a list of qualified frequencies.)
Frequency Tolerance/Stability:	(All Values Inclusive of Temperature, Voltage, and Load)
00	±100ppm Max.
45	±50ppm Max.
25	±25ppm Max. (0°C to +70°C only)
Operating Temperature Range	0°C to +70°C
ET	-40°C to +85°C
Storage Temperature Range	-55°C to +125°C
Aging (@ 25°C)	±5ppm/year Maximum
Supply Voltage (V _{DD})	5.0Vdc ±10%
Input Current (I _{DD})	≤ 100.000MHz 50mA Maximum, > 100.000MHz 65mA Maximum
Output Voltage Logic High (V _{OH})	V _{DD} -0.5Vdc Min.
Output Voltage Logic Low (V _{OL})	0.5Vdc Max.
Duty Cycle (@ 25°C)	50% ±10% (@ 50% of waveform)
T	50% ±5% (@ 50% of waveform)
Rise/Fall Time	3nSec Max. (10% to 90% of waveform)
Load Drive Capability	15pF HCMOS Load Maximum
Pin 1 Connection	
Blank	No Connect
TS	Tri-State (High Impedence)
Tri-State Input Voltage (V _{IH} & V _{IL})	+2.0Vdc. Min. to Enable Output, +0.8Vdc Max. to Disable Output (High Impedence), No Connect to Enable Output
Start Up Time	10 mSec Maximum
Jitter (Absolute)	±100pSec Maximum
Jitter (One Sigma)	±25pSec Maximum

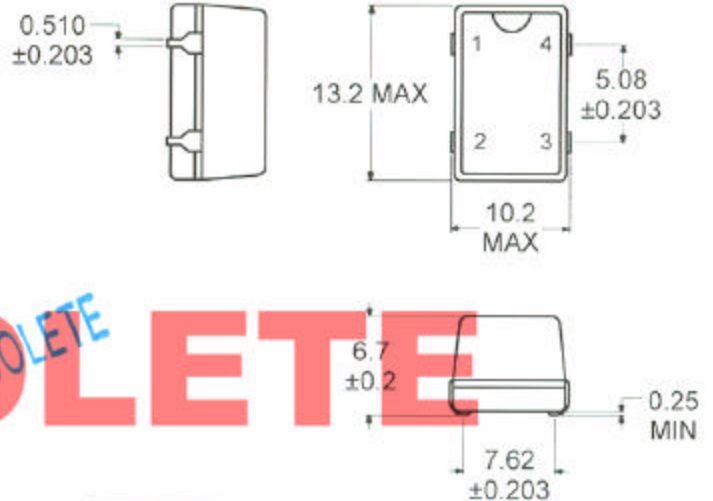
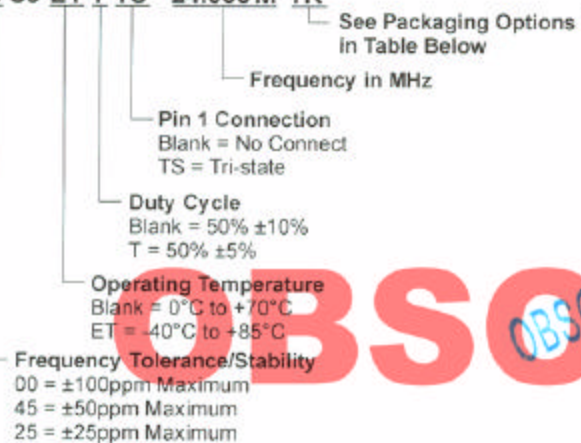
ORIGINAL
IF IN RED

ENVIRONMENTAL & MECHANICAL

Shock:	Conditions and Criteria Listed in TQC41-883-007 ; 50cm
Vibration:	Conditions and Criteria Listed in TQC41-883-008
Seal Integrity:	Conditions and Criteria Listed in TQC41-883-003 (Internal Crystal Only)
Solderability:	Conditions and Criteria Listed in TQC41-883-004 ; 95% coverage
Marking Permanency:	Conditions and Criteria Listed in TQC41-883-001

PART NUMBERING GUIDE

EC17 00 SJ ET T TS - 24.000 M TR



MARKING GUIDE

(Line #1) **ECLIPTEK**

(Line #2) **XX.XXXM**

Frequency in MHz

(Line #3) **XX Y ZZ**

Week of Year
Last Digit of Year

Ecliptek Manufacturing Code (per TEN02-001-000)

NOTE: Marking shall conform to conditions listed in TQC41-001-000.



PIN	CONNECTION
1	No Connect or Tri-State
2	Ground/Case Ground
3	Output
4	Supply Voltage

ALL DIMENSIONS
IN MILLIMETERS

SPECIFICATION CONTROL DRAWING

	Drawing Number CSC16-002-000
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Title
5.0Vdc J-Leaded Plastic Surface Mount Oscillator

PACKAGING OPTIONS

Blank = Bulk (CPA72-001-000)

TR = Tape & Reel (CPA70-411-000)