

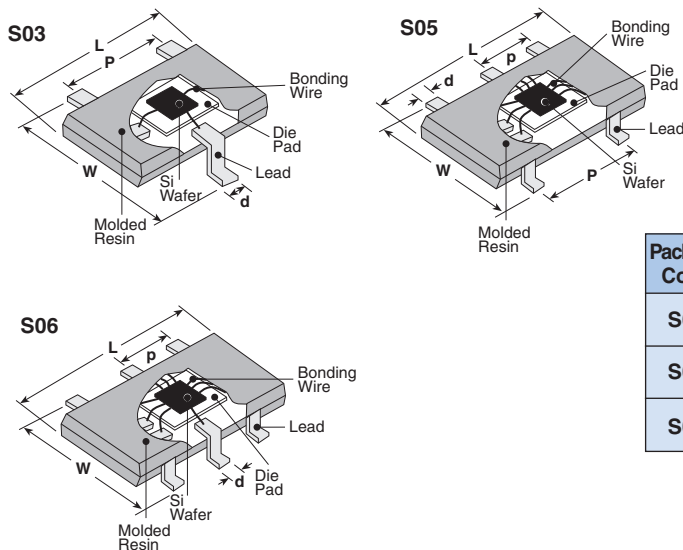
**features**

- (16kV) IEC 61000-4-2 rating
- Surface mount package
- High component density
- Products with lead-free terminations meet EU RoHS and China RoHS requirements

**applications**

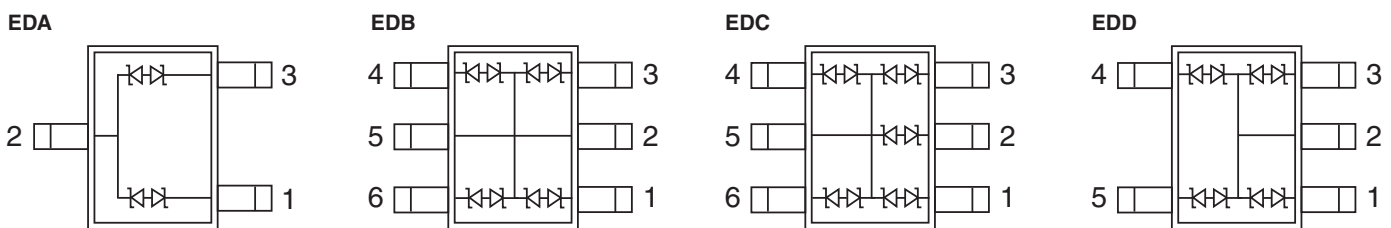
- ESD suppression
- Transient suppression

**dimensions and construction**



Package Code	Total Power	Pins	Dimensions inches (mm)					
			L ±0.2	W ±0.2	p ±0.1	P ±0.1	Pkg Ht ±0.2	d ±0.05
S03	225mw	3	.114 (2.90)	.091 (2.30)	—	.075 (1.90)	.039 (1.0)	.017 (0.43)
S05	225mw	5	.114 (2.90)	.110 (2.80)	.037 (0.95)	.075 (1.90)	.039 (1.0)	.017 (0.43)
S06	225mw	6	.114 (2.90)	.110 (2.80)	.037 (0.95)	—	.039 (1.0)	.017 (0.43)

**circuit schematic**



**ordering information**

New Part #	<b>EDA</b>	<b>S03</b>	<b>T</b>	<b>TE</b>	<b>16V</b>
Type	EDA EDB EDC EDD	Package Code From table above	Lead Plating T: Sn	Packaging TE: 7" embossed plastic tape	Voltage 5V0 16V

For further information on packaging, please refer to Appendix A.

Specifications given herein may be changed at any time without prior notice. Please confirm technical specifications before you order and/or use.

2/24/08

### applications and ratings

Part Designation	Diode Breakdown Voltage $I_F = 1 \text{ ma}$	Leakage Current (Typ.)	Capacitance @ 1 Mhz	Channel Clamp Voltage 8kV H.B.M.	ESD Voltage Capability IEC 61000-4-2	Peak Pulse Current IPP 8/20us
EDA EDB	16V to 27V	$\pm 0.1 \mu\text{A}@14\text{V}$	20pF	$\pm 20\text{V}$	16kV	—
EDC EDD	5.1V to 10V	$\pm 0.1 \mu\text{A}@5\text{V}$	15pF	$\pm 7\text{V}$	16kV	5.1A

circuit protection

### application schematic

