

# M5297P

## AC-DC TOTAL SYSTEM RESET IC

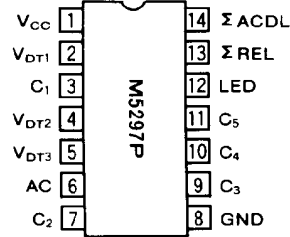
### DESCRIPTION

M5297P is a system reset IC designed for Power Supply System at office Automation, Factory Automation equipment, which watches both AC Power Supply and DC Power Supply at the same time.

This IC watches the AC Power Supply, in shutting off the AC Power Supply, that is to say before dropping of the System Power Supply (DC Power Supply), which detects the abnormal state of the System.

After the AC Power Supply shut off, in normal state of the System Power Supply (DC Power Supply), this IC sends the System Reset Output to the CPU and MPU. Continuously when the trouble of the System Power Supply (DC Power Supply) happen, users have already been able to complete the all emergency measurement.

### PIN CONFIGURATION (TOP VIEW)



Outline 14P4

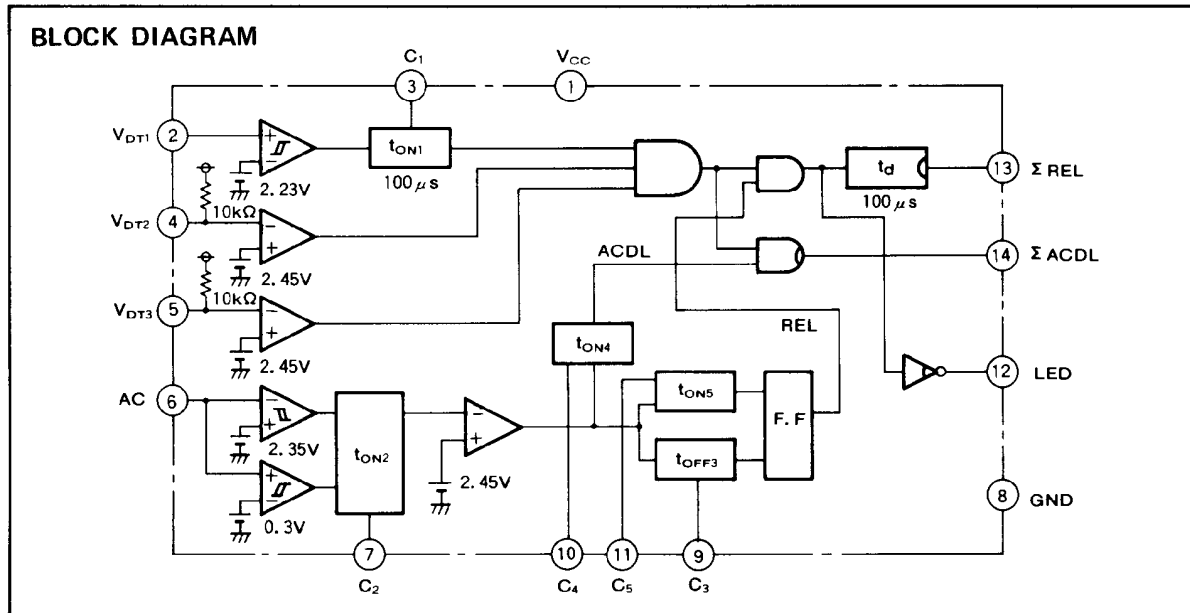
### FEATURES

- WATCH AC Power Supply
- WATCH 3 DC Power Supplies
- 3 Reset Output (open collector)
  - Low Reset . . . . . 2
  - High Reset . . . . . 1
- Variable delay time for using capacitor and Resistor

### APPLICATION

Power Supply System

### BLOCK DIAGRAM



**AC-DC TOTAL SYSTEM RESET IC****ABSOLUTE MAXIMUM RATINGS** (Ta = 25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CC</sub>	Supply voltage		7	V
V <sub>O</sub>	Applied voltage		20	V
I <sub>O</sub>	Output current		10	mA
P <sub>D</sub>	Power dissipation		700	mW
K <sub>θ</sub>	Thermal derating		7	mW/°C
T <sub>opr</sub>	Operating temperature		-20 ~ +85	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +125	°C

**ELECTRICAL CHARACTERISTICS** (Ta = 25 °C, V<sub>CC</sub> = 5V, unless otherwise noted)

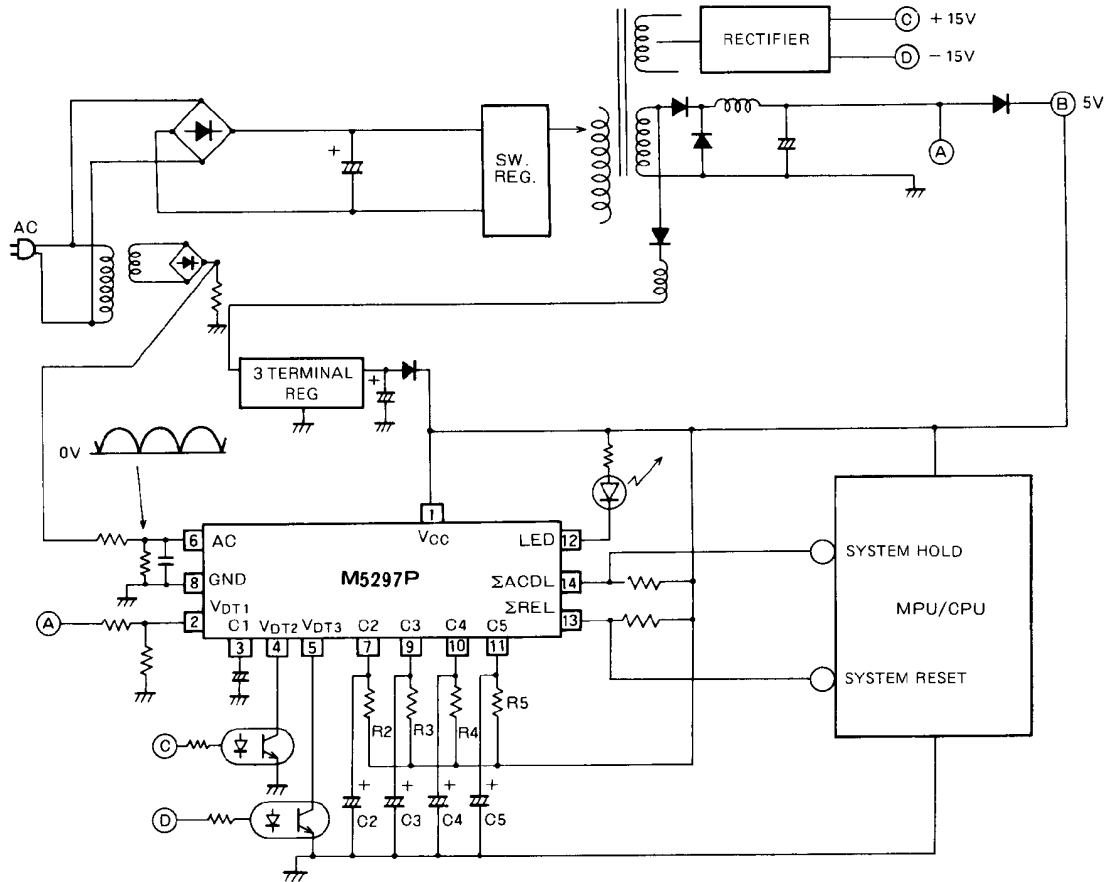
Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>CC</sub>	Circuit current			10	15	mA
V <sub>DT1</sub>	Detect voltage1		2.12	2.23	2.34	V
ΔV <sub>DT1</sub>	Hysteresis VDT1		0.05	0.1	0.2	V
V <sub>DT2</sub>	Detect voltage 2		2.0	2.45	2.9	V
V <sub>DT3</sub>	Detect voltage 3		2.0	2.45	2.9	V
V <sub>TAC1</sub>	REAC(H) Detect voltage		2.22	2.35	2.46	V
ΔV <sub>AC1</sub>	Hysteresis VTAC1			0.2		V
V <sub>TAC2</sub>	REAC(L) Detect voltage		0.15	0.3	0.6	V
ΔV <sub>AC2</sub>	Hysteresis VTAC2			0.1		V
V <sub>REF·TH</sub>	AC Reset threshold		2.31	2.45	2.57	V
T <sub>1</sub>	VDT1 delay time	C1 = 0	0.02	0.1	0.4	ms
T <sub>ON1</sub>	VDT1 delay time	C1 = 0.22 μF	20	50	100	ms
T <sub>OFF2</sub>	ΣACDL [OFF] time	C2 = 0.47 μF, R2 = 18k Ω	2.8	5.6	11.2	ms
T <sub>D</sub>	ΣACDL → ΣREL delay time		0.05	0.1	0.4	ms
T <sub>OFF3</sub>	ΣREL [OFF] time	C3 = 4.7 μF, R3 = 20k Ω	13.5	27	54	ms
T <sub>4</sub>	ΣACDL [ON] time	C4 = 0 μF, R4 = 20k Ω		1		μs
T <sub>ON4</sub>	ΣACDL [ON] time	C4 = 3.3 μF, R4 = 20k Ω	9.5	19	38	ms
T <sub>5</sub>	ΣREL [ON] time	C5 = 0 μF, R5 = 20k Ω		5		μs
T <sub>ON5</sub>	ΣREL [ON] time	C5 = 4.7 μF, R5 = 20k Ω	13.5	27	54	ms
V <sub>ACDL</sub>	ΣACDL saturation voltage	I <sub>O</sub> = 5mA		0.2	0.5	V
V <sub>REL</sub>	ΣREL saturation voltage	I <sub>O</sub> = 5mA		0.2	0.5	V
V <sub>LED</sub>	LED saturation voltage	I <sub>O</sub> = 5mA		0.2	0.5	V
I <sub>DT1</sub>	VDT1 source current	V <sub>DT1</sub> = 0V		-0.1		μA
I <sub>DT2</sub>	VDT2 source current	V <sub>DT2</sub> = 0V		-0.5	-1.5	mA
I <sub>DT3</sub>	VDT3 source current	V <sub>DT3</sub> = 0V		-0.5	-1.5	mA
I <sub>AC</sub>	AC source current	V <sub>AC</sub> = 0V		-0.2		μA
I <sub>ACDL</sub>	ΣACDL leak current	V <sub>ACDL</sub> = 20V			10	μA
I <sub>REL</sub>	ΣREL leak current	V <sub>REL</sub> = 20V			10	μA
I <sub>LED</sub>	LED leak current	V <sub>LED</sub> = 20V			10	μA

**Symbol Explanation**

- ACDL : AC power supply Down Low
- REL : Reset Low
- T<sub>OFF2</sub> : Maximum time for detecting the AC power supply shut off.
- T<sub>D</sub> : The period from ΣACDL Low Reset output to ΣREL Low Reset output.
- T<sub>OFF3</sub> : The period which subtract previous T<sub>D</sub> from the following time, period from ΣACDL Low Reset output to ΣREL Low Reset output.
- T<sub>ON4</sub> : The period from detecting the AC power supply recover to ΣACDL High output.
- T<sub>ON5</sub> : The period from detecting the AC power supply recover to ΣREL High output.

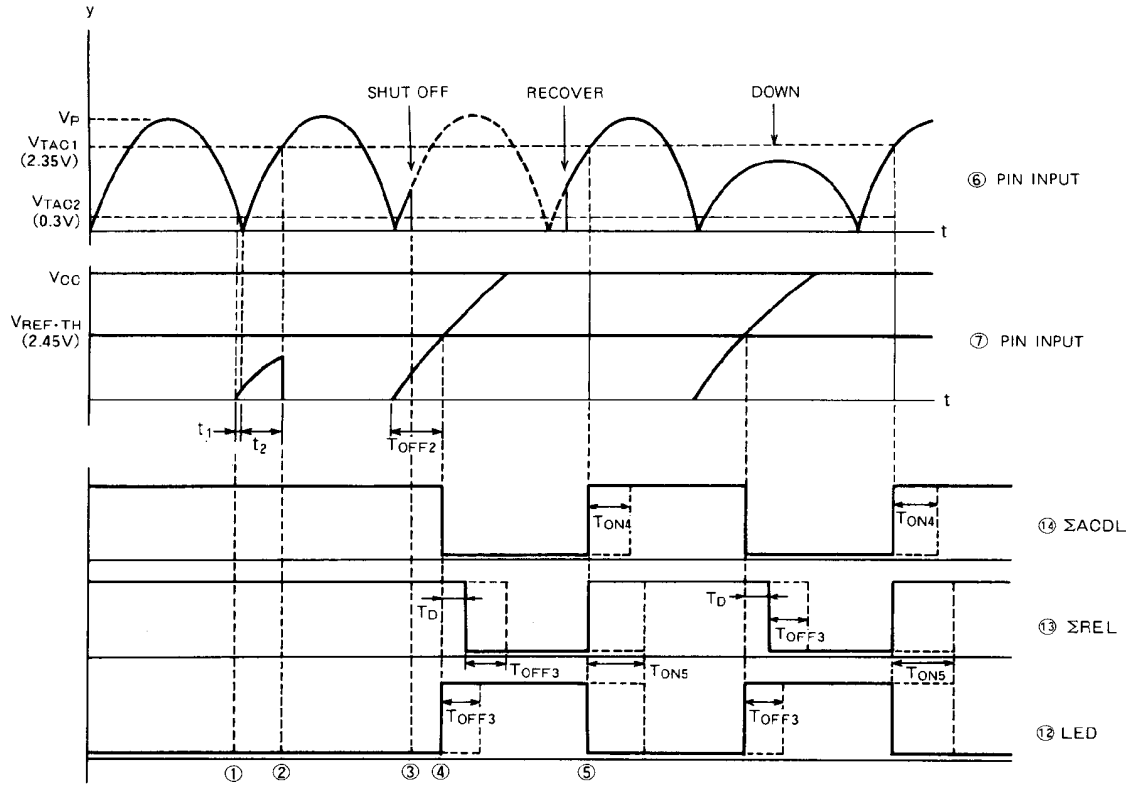
**AC-DC TOTAL SYSTEM RESET IC**

**Application for Power Supply System**



**AC-DC TOTAL SYSTEM RESET IC**

Timing Chart for detecting AC Power Supply ( $V_{DT1}$ ,  $V_{DT2}$ ,  $V_{DT3}$ , normal)



**Time calculation method**

$$t_1 = \frac{1}{360 \cdot f} \sin^{-1} \frac{0.3}{V_p}, \quad t_2 = \frac{1}{360 \cdot f} \sin^{-1} \frac{2.35}{V_p} \quad (f: \text{AC Power Supply frequency})$$

$$T_{OFF2} = C2 \cdot R2 \ln \frac{V_{CC}}{V_{CC} - 2.45}, \quad T_{OFF3} = C3 \cdot R3 \ln \frac{V_{CC}}{V_{CC} - 1.25}$$

$$T_{ON4} = C4 \cdot R4 \ln \frac{V_{CC}}{V_{CC} - 1.25}, \quad T_{ON5} = C5 \cdot R5 \ln \frac{V_{CC}}{V_{CC} - 1.25}$$