

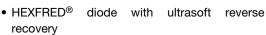
"Low Side Chopper" IGBT MTP (Ultrafast Speed IGBT), 100 A



PRODUCT SUMMARY					
V _{CES}	600 V				
I _C DC	100 A				
V _{CE(on)}	1.68 V				

FEATURES

• Generation 4 ultrafast speed IGBT technology





- · Very low conduction and switching losses
- Optional SMD thermistor (NTC)
- Al₂O₃ DBC
- Very low stray inductance design for high speed operation
- UL approved file E78996
- Speed 8 kHz to 60 kHz > 20 kHz hard switching, > 200 kHz resonant mode
- Compliant to RoHS Directive 2002/95/EC
- · Designed and qualified for industrial level

BENEFITS

- Optimized for welding, UPS and SMPS applications
- · Low EMI, requires less snubbing
- Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current	1	T _C = 25 °C	100		
Continuous collector current	I _C	T _C = 122 °C	50		
Pulsed collector current	I _{CM}		200	^	
Peak switching current	I _{LM}		200	Α	
Diode continuous forward current	I _F	T _C = 100 °C	48		
Peak diode forward current	I _{FM}		200		
Gate to emitter voltage	V_{GE}		± 20	V	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
IGBT		T _C = 25 °C	445		
Maximum power dissipation —	п	T _C = 100 °C	175	w	
	P_{D}	T _C = 25 °C	205	VV	
Diode		T _C = 100 °C	83		



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	600	-	-	
		$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}$	-	1.69	2.31	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A	-	1.96	2.55	v
		V _{GE} = 15 V, I _C = 100 A, T _J = 150 °C	-	1.88	2.24	ľ
Gate threshold voltage	V _{GE(th)}	I _C = 0.5 mA	3	-	6	
Diode reverse breakdown voltage	V_{BR}	I _R = 200 μA	600	-	-	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	$V_{CE} = V_{GE}, I_{C} = 500 \mu A$	-	- 13	-	mV/°C
Forward transconductance	9 _{fe}	V _{CE} = 50 V, I _C = 100 A	22	29	-	S
Collector to exitter leaking a correct		V _{GE} = 0 V, V _{CE} = 600 V	-	-	0.25	A
Collector to emitter leaking current	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V, T _J = 150 °C	-	-	6	mA
Die de fewered velkene dese		I _F = 100 A, V _{GE} = 0 V	-	1.64	1.82	V
Diode forward voltage drop	V _{FM}	I _F = 100 A, V _{GE} = 0 V, T _J = 150 °C	-	1.56	1.74]
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 250	nA

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I _C = 100 A	-	370	555	
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 480 V	-	64	96	nC
Gate to collector charge (turn-on)	Q _{gc}	V _{GE} = 15 V	-	163	245	
Turn-on switching loss	E _{on}	$V_{CC} = 480 \text{ V}, I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V},$	-	0.7	1.2	
Turn-off switching loss	E _{off}	$R_g = 5 \Omega$, $T_J = 25 °C$, energy losses include tail and diode	-	1.7	2.6	mJ
Total switching loss	E _{ts}	reverse recovery	-	2.4	3.8	
Turn-on switching loss	E _{on}	$V_{CC} = 480 \text{ V}, I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V},$	-	1.1	1.7	
Turn-off switching loss	E _{off}	$R_g = 5 \Omega$, $T_J = 125 ^{\circ}$ C, energy losses include tail and diode	-	2.5	3.8	
Total switching loss	E _{ts}	reverse recovery	-	3.6	5.5	
Input capacitance	C _{ies}	V _{GE} = 0 V	-	9800	14 700	
Output capacitance	C _{oes}	V _{CC} = 30 V	-	602	903	
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	121	182	pF
Diode junction capacitance	Ct	V _R = 600 V, f = 1.0 MHz	-	118	177	
Diode reverse recovery time	t _{rr}		-	99	150	ns
Diode peak reverse current	I _{rr}	$V_{CC} = 480 \text{ V}, I_{C} = 50 \text{ A}$	-	6.5	9.8	Α
Diode recovery charge	Q _{rr}	dl/dt = 200 A/μs $R_q = 5 \Omega$	-	320	735	nC
Diode peak rate of fall of recovery during t _b	dI _{(rec)M} /dt	9	-	236	-	A/µs



THERMISTOR SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Resistance	R ₀ ⁽¹⁾	T ₀ = 25 °C	-	30	-	kΩ
Sensitivity index of the	ß (1)(2)	T ₀ = 25 °C	_	4000	_	К
thermistor material	р	T ₁ = 85 °C	_	4000	_	

Notes

 $^{(1)}$ T_0 , T_1 are thermistor's temperatures

(2)
$$\frac{R_0}{R_1} = exp \left[\beta \left(\frac{1}{T_0} - \frac{1}{T_1} \right) \right]$$
, temperature in Kelvin

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		- 40	-	150	°C
Storage temperature range	T _{Stg}		- 40	-	125	
Junction to case	В		-	-	0.28	
Diode	R _{thJC}		-	-	0.6	°C/W
Case to sink per module	R _{thCS}	Heatsink compound thermal conductivity = 1 W/mK	-	0.06	-	
Mounting torque to heatsink ± 10 %		A mounting compound is recommended and the torque should be checked after 3 hours to allow for the spread of the compound. Lubricated threads.		3		Nm
Weight				66		g

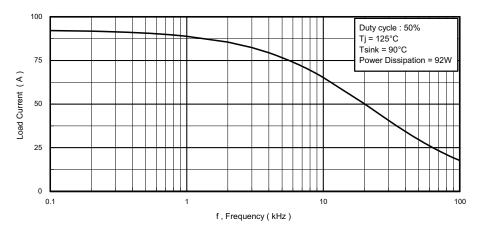


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of Fundamental)

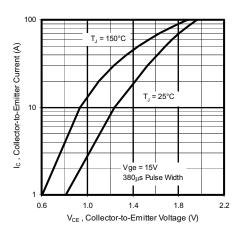


Fig. 2 - Typical Output Characteristics

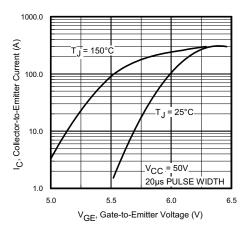


Fig. 3 - Typical Transfer Characteristics

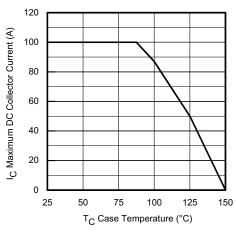


Fig. 4 - Maximum Collector Current vs. Case Temperature

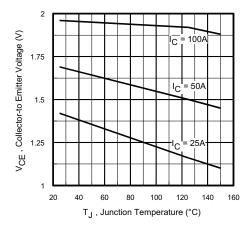


Fig. 5 - Typical Collector to Emitter Voltage vs. Junction Temperature

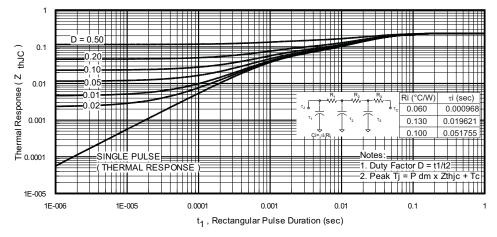


Fig. 6 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

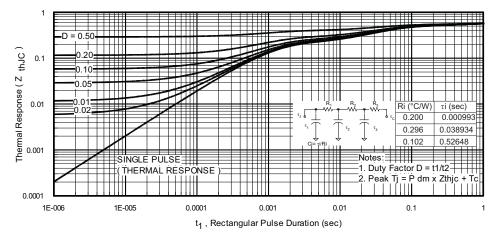


Fig. 7 - Maximum Transient Thermal Impedance, Junction to Case (Diode)

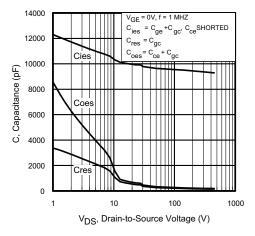


Fig. 8 - Typical Capacitance vs. Collector to Emitter Voltage

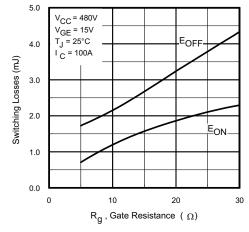


Fig. 10 - Typical Switching Losses vs. Gate Resistance

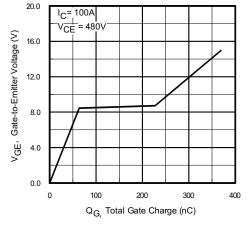


Fig. 9 - Typical Gate Charge vs. Gate to Emitter Voltage

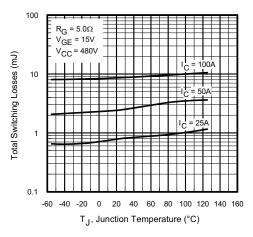


Fig. 11 - Typical Switching Losses vs. Junction Temperature

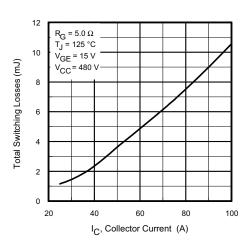


Fig. 12 - Typical Switching Losses vs. Collector to Emitter Current

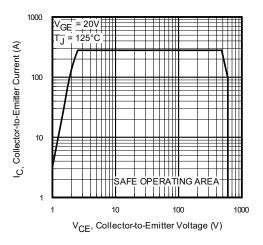


Fig. 13 - Turn-Off SOA

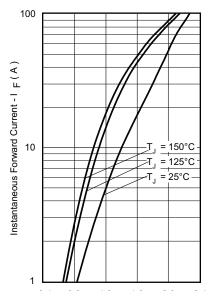


Fig. 14 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

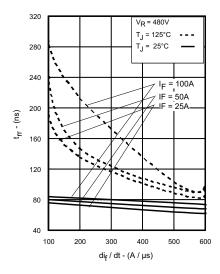


Fig. 15 - Typical Reverse Recovery Time vs. dl_F/dt

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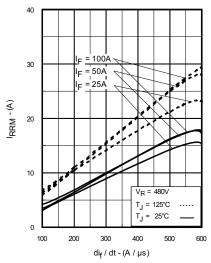


Fig. 16 - Typical Recovery Current vs. dl_F/dt

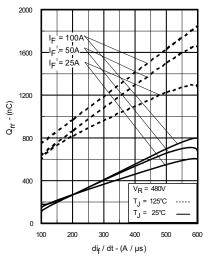


Fig. 17 - Typical Stored Charge vs.dl_F/dt

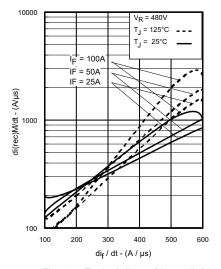


Fig. 18 - Typical dI_{(rec)M}/dt vs. dI_F/dt

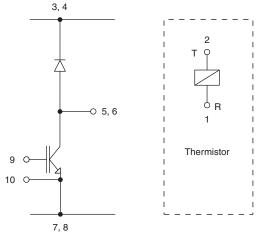
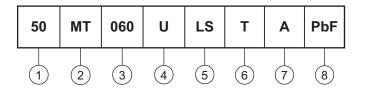


Fig. 19 - Electrical diagram

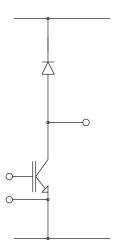
ORDERING INFORMATION TABLE

Device code



- 1 Current rating (50 = 50 A)
- 2 Essential part number
- Voltage rating (060 = 600 V)
- Speed/type (U = Ultrafast IGBT)
- Circuit configuration (LS = Low side chopper)
- 6 Special option:
 - None = No special option
 - T = Thermistor
- 7 A = Al₂O₃ DBC substrate
- PbF = Lead (Pb)-free

CIRCUIT CONFIGURATION

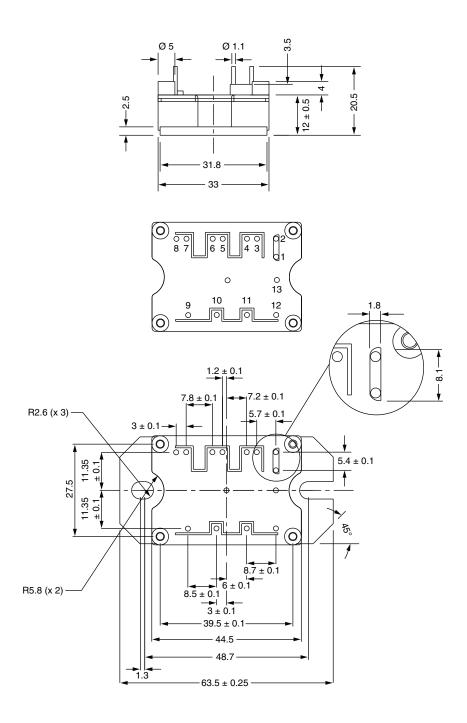


LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95175



MTP

DIMENSIONS in millimeters



Note

• Unused terminals are not assembled in the package

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Document Number: 91000 www.vishay.com
Revision: 11-Mar-11 1