# RENESAS

# HAT2080R

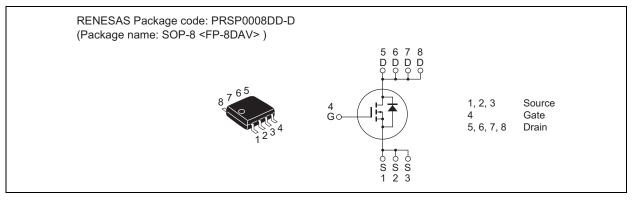
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1180-0200 (Previous: ADE-208-1229) Rev.2.00 Sep 07, 2005

# Features

- Low on-resistance
- Low drive current
- High density mounting

## Outline





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
ltem	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	250	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	ID	1.7	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	13.6	A
Body to drain diode reverse drain current	I <sub>DR</sub>	1.7	A
Channel dissipation	Pch Note 2	2.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	٦°

Notes: 1. PW  $\leq$  10  $\mu s,\,duty\,cycle \leq$  1%

2. When using the glass epoxy board (FR4 40  $\times$  40  $\times$  1.6 mm), PW  $\leq$  10 s

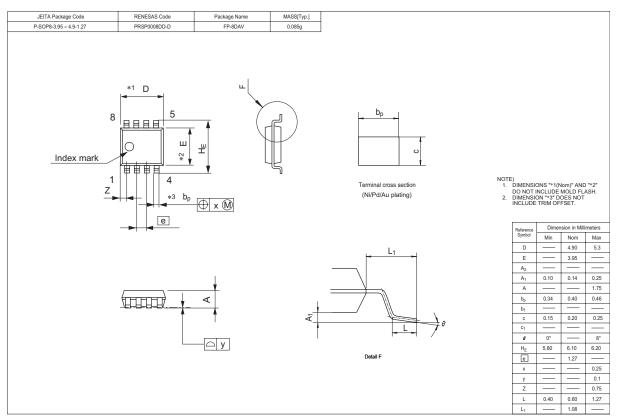
# **Electrical Characteristics**

						(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	250	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V},  V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	3.0	—	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	0.65	0.85	Ω	$I_D = 0.85 \text{ A}, V_{GS} = 10 \text{ V}^{Note 3}$
Forward transfer admittance	y <sub>fs</sub>	1.2	2.0	—	S	$I_D = 0.85 \text{ A}, V_{DS} = 10 \text{ V}^{Note 3}$
Input capacitance	Ciss	—	300	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	42	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	11	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	—	18	—	ns	$V_{DD} = 125 \text{ V}, \text{ I}_{D} = 0.85 \text{ A}$
Rise time	t <sub>r</sub>	—	10	—	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	t <sub>d (off)</sub>	—	47	—	ns	$R_L = 147 \Omega$
Fall time	t <sub>f</sub>	—	15	—	ns	Rg = 10 Ω
Total gate charge	Qg	—	11	—	nC	V <sub>DD</sub> = 200 V
Gate to source charge	Qgs	_	1.5	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	5	—	nC	I <sub>D</sub> = 1.7 A
Body to drain diode forward voltage	V <sub>DF</sub>	—	0.8	1.2	V	$I_F = 1.7 \text{ A}, V_{GS} = 0^{Note 3}$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	80		ns	I <sub>F</sub> = 1.7 A, V <sub>GS</sub> = 0
						di <sub>F</sub> /dt = 100 A/µs

Note: 3. Pulse test



# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2080R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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