

OBSERVE ELECTROSTATIC



DAC-011A

PARALLEL DIGITAL TO RGB AND YP_BP_R MODULE

- Small compact design for pcb mounting
- bit component outputs
- Inputs with single pin control
- Auto & forced 625/525 operation
- Simultaneous RGB or YP_BP_R
- Full video bandwidth filtered outputs

This interface card accepts CCIR656 parallel data and provides simultaneous component RGB and YP_BP_R analogue outputs, together with separate mixed sync's. The card accepts two inputs which can be selected via a single pin control. With other features such as the addition of mixed sync's to analogue outputs and automatic or forced 625/525 operation it is anticipated that this versatile module will provide more solutions than just interfacing to most broadcast monitors..

Input Requirements

<i>Input</i>	2 x digital inputs at TTL or CMOS levels. (CCIR656/SMPTE RP-125 coded)
<i>Line standard</i>	625/525 (Auto detection or forced operation)
<i>Set up time</i>	12nS min (data to clk rising edge)
<i>Hold time</i>	2nS min (clk rising edge to data)

Component Output Characteristics

<i>Output format</i>	Simultaneous YPbPr and RGB
<i>Output levels</i>	700 mV ±1%
<i>Resolution</i>	10 bit
<i>Freq. Response</i>	< 0.2 dB to 5.5 MHz (RGB or Y), < 0.2 to 2.2 MHz (P _B P _R)
<i>K-rating (2T)</i>	1% typical
<i>Noise</i>	>70 dB rms to 5.5 MHz

Sync Output Characteristics

<i>Sync output amplitude</i>	2V ±5% into 75 ohm
<i>Sync amp. on RGB or Y</i>	300 mV ±10mV into 75 ohm

Other parameters

<i>Power</i>	+5V ±0.25V (200mA max) - 5V ±0.25V (80mA max)
<i>Operating Temp.</i>	0°C to 40°C
<i>Aqueous Washable</i>	No
<i>Package</i>	DR00173A

DAC-011A Control Information		
	+5V or Open Circuit	0V
Control 0	See table below	See table below
Control 1	See table below	See table below
Control 2	SYNC's on RGB and Y	No SYNC's on RGB or Y
Control 3	Input 1 selected	Input 2 selected

DAC-011A Standards Selection		
	Control 0	Control 1
625	0V	+5V or floating
AUTO	+5V or floating	+5V or floating
525	+5V or floating	0V

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PACKAGE DETAIL

