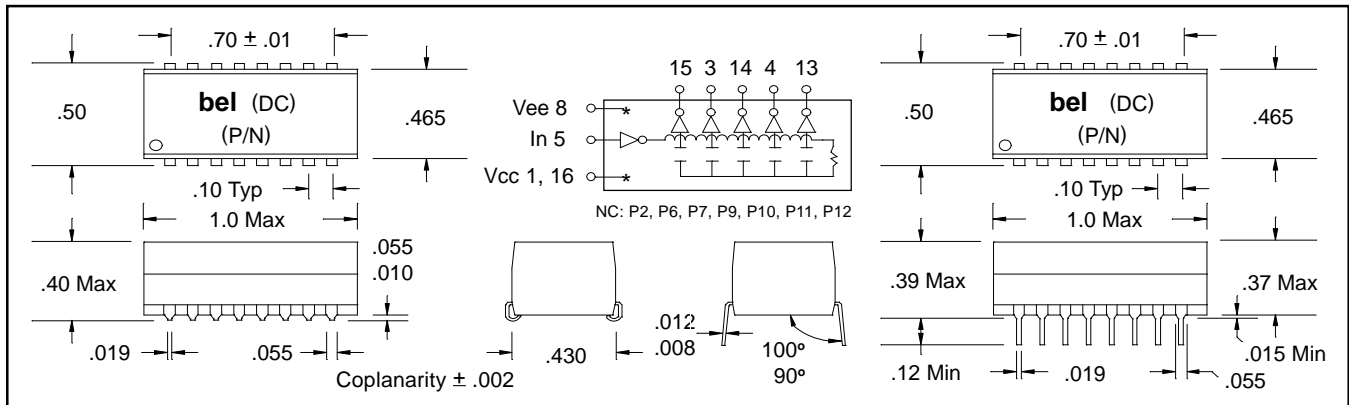


## 5 TAP LEADING EDGE CONTROL 10KH ECL DELAY MODULES

Cat 41-R0



### Part Numbers

SMD	Thru-Hole	Total Delay	Tap Delay Every	Rise Time
S459-0006-02	A459-0006-02	6 ns	* 1 ns	2 ns
S459-0010-02	A459-0010-02	10 ns	2 ns	2 ns
S459-0015-02	A459-0015-02	15 ns	3 ns	2 ns
S459-0020-02	A459-0020-02	20 ns	4 ns	2 ns
S459-0025-02	A459-0025-02	25 ns	5 ns	3 ns
S459-0030-02	A459-0030-02	30 ns	6 ns	3 ns
S459-0035-02	A459-0035-02	35 ns	7 ns	3 ns
S459-0040-02	A459-0040-02	40 ns	8 ns	3 ns
S459-0045-02	A459-0045-02	45 ns	9 ns	3 ns
S459-0050-02	A459-0050-02	50 ns	10 ns	3 ns
S459-0075-02	A459-0075-02	75 ns	15 ns	4 ns
S459-0100-02	A459-0100-02	100 ns	20 ns	6 ns
S459-0125-02	A459-0125-02	125 ns	25 ns	6 ns

### Test Conditions @ 25°C

Ein	Pulse Voltage, Logic 1	-0.9 Volts
	Pulse voltage, Logic 0	-1.8 Volts
Trin	Rise Time	1.0 ns (20%-80%)
PW	Pulse Width	Total Delay + 10 ns
PP	Pulse Period	4 x Pulse Width
Vee	Supply Voltage	-5.2 Volts

### Electrical Characteristics

	Min.	Max.	Units
Vee	-4.94	-5.46	V
Vih	-1.13		V
Vil		-1.48	V
Voh	-0.98		V
Vol		-1.63	V
lih		310	ua
lil	0.5		ua
Ta	0°	75°	C
PW	40		%
d		50	%
Tc	300 PPM/°C		

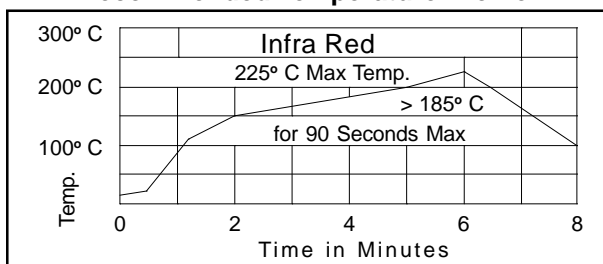
### Tolerances

Input to Taps  $\pm 1$  ns or 3% , Whichever is Greater  
Delays measured @ 50% levels on Leading Edge only  
with Outputs Terminated with 50 Ohms  $\pm 1\%$  to - 2 V  
Rise and Fall Times measured from 20% to 80% levels  
\* First Tap Delay is 2 ns from P5 to P15

### Notes

Transfer molded for better reliability  
Compatible with ECL 10K & 10KH circuits  
Terminals: Electro-Tin plate phosphor bronze  
Performance warranty is limited to specified parameters listed  
SMD - Tape & Reel available:  
44mm Wide x 24mm Pitch, 170 pieces per 13" reel

### Recommended Temperature Profile



### Other Delays and Tolerances Available Consult Sales

*Specifications subject to change without notice.*

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