TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS V)

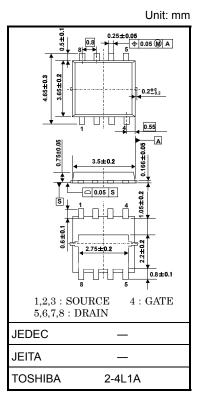
TPCM8102

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- Low drain-source ON-resistance: RDS (ON) = $6.0 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 44S$ (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_D = -1 \text{ mA})$

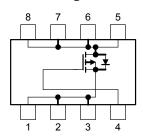
Absolute Maximum Ratings (Ta = 25°C)

| Characte | eristic | Symbol | Rating | Unit | |
|-------------------------|---------------------------------|------------------|------------|------|--|
| Drain-source voltage | | V_{DSS} | -30 | V | |
| Drain-gate voltage (R | $k_{GS} = 20 \text{ k}\Omega$) | V_{DGR} | -30 | V | |
| Gate-source voltage | | V_{GSS} | ±20 | V | |
| Drain current | DC (Note 1) | I _D | -25 | Α | |
| Diam current | Pulse (Note 1) | I _{DP} | -75 | , \ | |
| Drain power dissipation | on (Tc = 25°C) | P_{D} | 30 | W | |
| Drain power dissipation | on $(t = 10 s)$ (Note 2a) | P_{D} | 2.3 | W | |
| Drain power dissipation | on (t = 10 s) (Note 2b) | P _D | 1.0 | W | |
| Single-pulse avalance | ne energy (Note 3) | E _{AS} | 81 | mJ | |
| Avalanche current | | I _{AR} | -25 | Α | |
| Repetitive avalanche | energy = 25°C) (Note 4) | E _{AR} | 1.8 | mJ | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature | range | T _{stg} | -55 to 150 | °C | |



Weight: 0.028 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

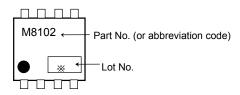
This transistor is an electrostatic-sensitive device. Handle with care.



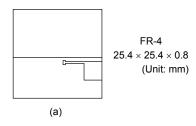
Thermal Characteristics

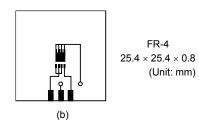
| Characteristic | Symbol | Max | Unit |
|---|------------------------|------|------|
| Thermal resistance, channel to case (Tc = 25°C) | R _{th (ch-c)} | 4.17 | °C/W |
| Thermal resistance, channel to ambient (t = 10 s) (Note 2a) | R _{th (ch-a)} | 54.3 | °C/W |
| Thermal resistance, channel to ambient (t = 10 s) (Note 2b) | R _{th (ch-a)} | 125 | °C/W |

Marking (Note 5)

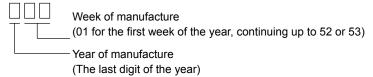


- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a)
- (b) Device mounted on a glass-epoxy board (b)





- Note 3: $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$ (initial), $L = 100 \mu H,~R_G = 25~\Omega,~I_{AR} = -25~A$
- Note 4: Repetitive rating: pulse width limited by max channel temperature
- Note 5: on lower left of the marking indicates Pin 1.
 - * Weekly code: (Three digits)



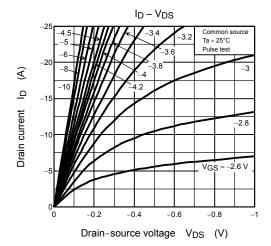


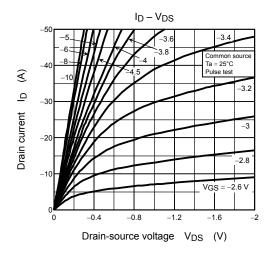
Electrical Characteristics (Ta = 25°C)

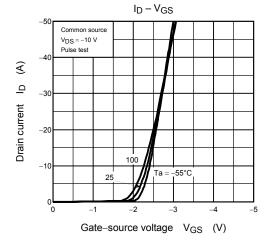
| Ch | aracteristic | Symbol | Test Condition | Min | Тур. Мах | | Unit |
|---|----------------|-----------------------|---|------|----------|--------------------|--------|
| Gate leakage cur | rent | I _{GSS} | $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±100 | nA |
| Drain cutoff curre | ent | I _{DSS} | $V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$ | _ | _ | -10 | μА |
| Drain agurag bro | akdowa voltago | V _{(BR) DSS} | $I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ | -30 | _ | _ | V |
| Drain-source breakdown voltage | | V _{(BR) DSX} | $I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$ | -13 | _ | _ | V |
| Gate threshold ve | oltage | V _{th} | $V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$ | -0.8 | _ | -2.0 | V |
| Drain course ON | rocietanoo | Pro (ON) | $V_{GS} = -4 \text{ V}, I_D = -12 \text{ A}$ | 1116 | | mΩ | |
| Drain-source ON-resistance | | R _{DS} (ON) | $V_{GS} = -10 \text{ V}, I_D = -12 \text{ A}$ | | 6.0 | 7.7 | 1117.5 |
| Forward transfer | admittance | Y _{fs} | $V_{DS} = -10 \text{ V}, I_D = -12 \text{ A}$ | 22 | 2 44 — | | S |
| Input capacitance | e | C _{iss} | | 2450 | | | |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{V}, f = 1 \text{MHz}$ | _ | 530 | _ | pF |
| · | | Coss | | _ | 740 | _ | |
| | Rise time | t _r | V_{GS} 0 V Γ $I_D = -12 \text{ A}$ | _ | 13 | ±100 -102.0 16 7.7 | |
| Switching time | Turn-on time | t _{on} | -10 V G G S S S | _ | 22 | | |
| Input capacitance C_{iss} Reverse transfer capacitance C_{rss} Output capacitance C_{oss} Rise time C_{oss} | _ | ns | | | | | |
| | Turn-off time | t _{off} | | | 340 | | |
| Total gate charge (gate-source plus | | Qg | V _{DD} ≈ -24 V, V _{GS} = -10 V, | _ | 60 | _ | |
| Gate-source charge 1 | | Q _{gs1} | $I_D = -25 \text{ A}$ | | 11 | | nC |
| Gate-drain ("Mille | er") charge | Q _{gd} | | _ | 19 | _ | |

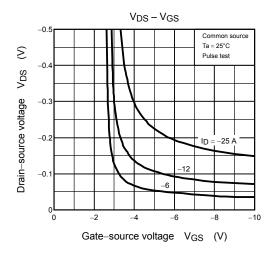
Source-Drain Ratings and Characteristics (Ta = 25°C)

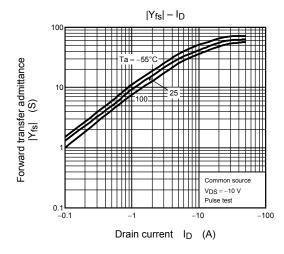
| Characteristic | | Symbol | Test Condition | Min | Тур. | Max | Unit | |
|-------------------------|-------|----------|------------------|--|------|-----|------|---|
| Drain reverse current | Pulse | (Note 1) | I _{DRP} | _ | _ | _ | -75 | Α |
| Forward voltage (diode) | | | V_{DSF} | $I_{DR} = -25 \text{ A}, V_{GS} = 0 \text{ V}$ | | _ | 1.2 | V |

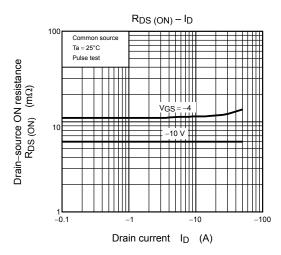


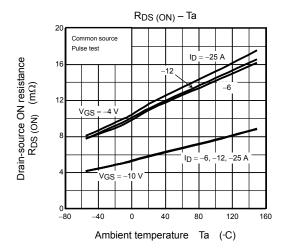


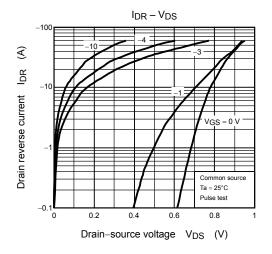


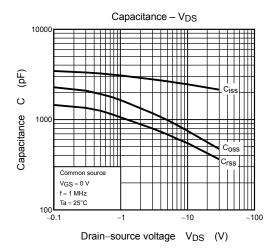


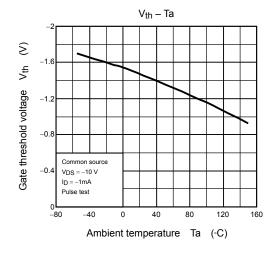


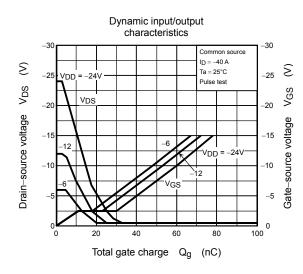


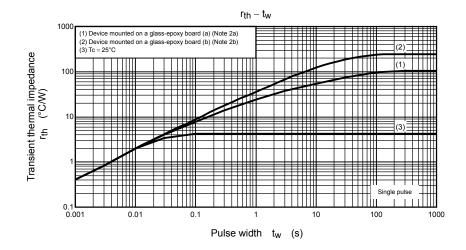


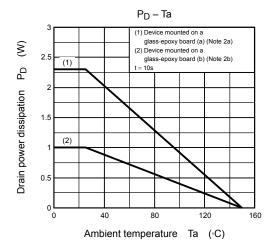


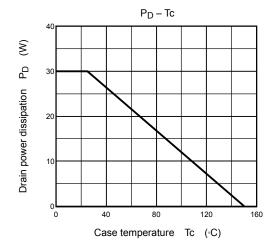


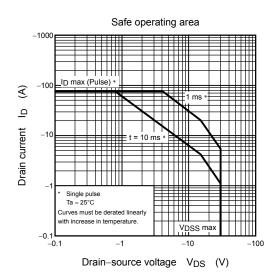












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20070701-EN GENERAL

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