



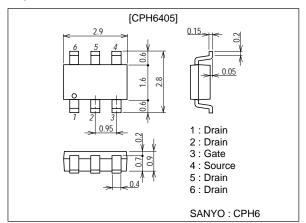
Ultrahigh-Speed Switching Applications

Features

- · Low ON-state resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2151A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ΙD		6	Α
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	24	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1.6	W
Channel Temperature	Tch		150	ပ္စ
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _G S=0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3A	6.3	9		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =3A, V _{GS} =4V		33	43	mΩ
	RDS(on)2	ID=1A, VGS=2.5V		43	61	mΩ

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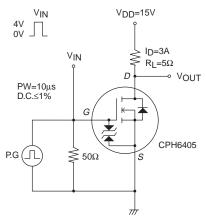
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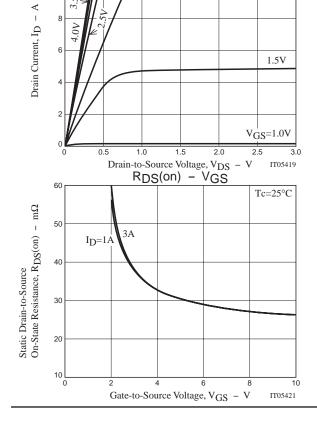
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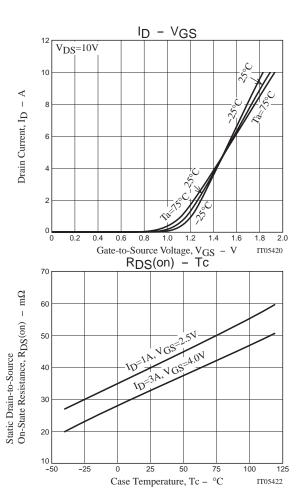
Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		560		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		120		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		70		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		12		ns
Rise Time	tr	See specified Test Circuit.		135		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		50		ns
Fall Time	tf	See specified Test Circuit.		110		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =6A		24		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =6A		1.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =6A		3.2		nC
Diode Forward Voltage	V _{SD}	I _S =6A, V _{GS} =0			1.2	V

Switching Time Test Circuit

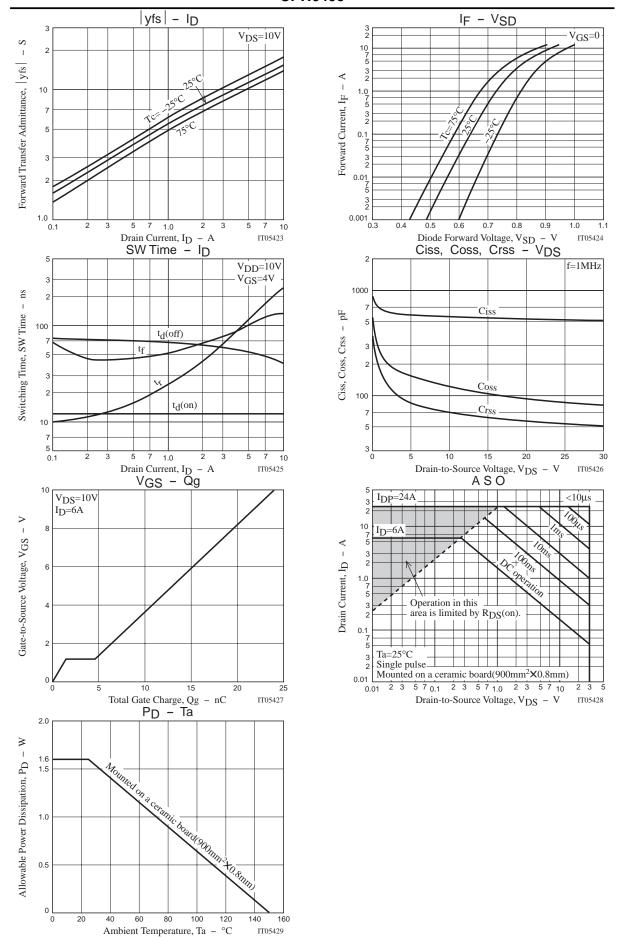


ID - VDS





No.7369-2/4



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