

PRELIMINARY PRODUCT DATA

FEATURES

- 8-Bit flash A/D converter
- 20MHz sampling rate
- · Complete support circuitry
- Low power, 900mW
- Sample-hold not required
- Three-state outputs
- MIL-STD-883 versions

GENERAL DESCRIPTION

The ADC-228A combines analog front-end circuitry and a flash A/D converter to digitize high-speed analog signals at a rate of 20 million samples per second. The ADC-228A contains an 8bit, 20MHz, flash A/D, a wideband analog input buffer, a precision voltage reference, temperature compensation circuitry, reference trims, and a three-state output buffer in a 24-pin package.

The ADC-228A offers significant savings by combining all of the circuitry in a single package. Valuable board real estate is saved, and design time and manufacturing costs are reduced.

The ADC-228A is housed in a 24-pin ceramic DDIP package and is available in the commercial, 0 to +70°C, or military, -55 to +125°C, temperature ranges. A MIL-STD-883 version is also available. Operation is from ±15V and +5V power supplies.



INPUT/OUTPUT CONNECTIONS

PIN	FUNCTION	PIN	FUNCTION
1	+5V SUPPLY	24	BIT 8 (LSB)
2	GROUND	23	BIT 7
3	+5V REFERENCE OUT	22	BIT 6
4	GROUND	21	BIT 5
5	ANALOG INPUT	20	NO CONNECTION
6	GROUND	19	+15V SUPPLY
7	GROUND	18	CLOCK INPUT
8	NO CONNECTION	17	BIT 4
9	NO CONNECTION	16	BIT 3
10	-15V SUPPLY	15	BIT 2
11	CS1	14	BIT 1 (MSB)
12	CS2	13	NO CONNECTION





ADC-228A

8-Bit, 20MHz, Complete Flash A/D Converter



ABSOLUTE MAXIMUM RATINGS

PARAMETER	LIMITS
Power Supply Voltage, Pin 1 Pin 19 Pin 10	-0.3 to +7V -0.3 to +18V +0.3 to -18V
Digital Inputs, Pins 8,9,11,12,18 Analog Input, Pin 5 Digital Outputs	-0.5 to +5.5V -3.8 to +6.6V -0.5 to +5.5V (short circuit protected to ground)
Lead Temp. (10 seconds)+300°C	

FUNCTIONAL SPECIFICATIONS

(Apply over the operating temperature range with 20MHz clock and \pm 15V and \pm 5V power supply voltages, unless otherwise specified.)

ANALOG INPUTS	MIN.	TYP.	MAX.	UNITS
Single-Ended, Non-Isolated Input Range, dc-20MHz Input Resistance Input Capacitance	0 1.95 —	 2 5	+5.0 — 10	Volts kΩ pF
DIGITAL INPUTS				
Logic Levels Logic 1 Logic 0 Logic Logding	+2.0		+0.8	Volts Volts
Logic 0 Logic Rules Widths	_	_	+160 -0.5	μA mA
"High" "Low"	20 20	_ _	_ _	ns ns
DIGITAL OUTPUTS				
Coding Resolution	Straight Binary 8 Bits			
Logic 1 Logic 0	+2.4	_	+0.4	Volts Volts
Logic 1 Logic 1	_	_	-1 +1	mA mA
From Rising Edge Output Hold Time	6	_	40 —	ns ns
PERFORMANCE		I	I	
Sampling Rate ① Differential Linearity	20	_	_	MHz
+25°C 0 to +70°C -55 to +125°C		±0.5 ±0.5 ±0.5	±0.75 ±0.75 ±0.85	LSB LSB LSB
Integral Linearity, +25°C End-point Best-fit Line Over Temperature End-point		±0.5 ±0.35 —	±1 ±0.75 ±1.75	LSB LSB LSB
Best-fit Line Zero-Scale Offset Code "0" to "1" Transition	_	-	±1	LSB
+25°C -55 to +125°C		±0.5 ±0.5	±1 ±1.5	LSB LSB

PERFORMANCE	MIN.	TYP.	MAX.	UNITS	
Gain error Full Scale Absolute Accuracy Differential Gain Differential Phase Aperture Delay Aperture Jitter		±0.5 ±0.5 2 1 8 50	±1.5 ±1.5 — — —	LSB LSB % deg. ns ps	
No Missing Codes Power Supply Rejection	Over the operating temperature range				
DYNAMIC PERFORMANCE					
Total Harm. Distortion, –0.5dB					
DC to 2.5 MHz 2.5 MHz to 5 MHz 5 MHz to 10 MHz Signal-to-Noise Ratio	 	-55 -49 -39	-53 -44 -36	dB dB dB	
and Distortion, -0.5dB DC to 2.5 MHz 2.5 MHz to 5 MHz 5 MHz to 10 MHz	44 41 35	49 46 39		dB dB	
Signal-to-Noise Ratio w/o Distortion, -0.5 dB DC to 2.5 MHz	45	48	_	dB	
5 MHz to 10 MHz Effective Bits, -0.5dB	44 42	49 45 7 75	_	dB Dite	
2.5 MHz to 5 MHz 5 MHz to 10 MHz	7.1 6.8 5.6	7.75 7.4 6.1		Bits Bits Bits	
Large Signal (-3dB) Small Signal (-20dB)	15 40			MHz MHz	
POWER SUPPLY			1		
Power Supply Range +15V Supply -15V Supply +5V Supply Baues Sumply Current	+11 –11 +4.75	+15 -15 +5	+15.75 -15.75 +5.25	Volts Volts Volts	
+15V Supply -15V Supply +5V Supply Power Dissipation	 _	+12 -13 +70	+20 -20 +80	mA mA mA	
±15V, +5V Nominal	-	0.7	0.9	Watts	
PHYSICAL/ENVIRONMENTA					
Operating Temp. Range, Case ADC-228AMC ADC-228AMM, ADC-228A/883 Storage Temp. Range Package Type Weight	0 to +70°C -55 to +125°C -65 to +150°C 24-pin, ceramic DDIP 0.3 ounces (8.5 grams)				

① At full power input and chip selects enabled.

② For 10-step, 40 IRE NTSC ramp test.



TECHNICAL NOTES

- Rated performance requires using good high-frequency techniques. The analog and digital ground pins are connected to each other internally. Avoid ground related problems by connecting the grounds to one point, the ground plane beneath the converter. Due to the inductance and resistance of the power supply return paths, return the analog and digital ground separately to the power supplies.
- Bypass all the analog and digital supplies and the +5V REFERENCE (pin 3) to ground with a 4.7µF, 25V tantalum electrolytic capacitor in parallel with a 0.1µF ceramic capacitor.

Table 1. ADC-228A Unipolar Output Coding

ANALOG INPUT	CODE	STRAIGHT BIN.
+4.96V	+FS – 1 LSB	1111 1110
+3.75V	+ 3/4 FS	1100 0000
+2.50V	+ 1/2 FS	1000 0000
+1.25V	+ 1/4 FS	0100 0000
+0.02V	+ 1 LSB	0000 0001
0.00V	ZERO	0000 0000

Table 2. Chip Select Truth Table

CS2 Pin 12	CS1 Pin 11	Bits 1-8
0	0	Three State Mode
0	1	Three State Mode
1	0	Data Outputted
1	1	Three State Mode



Figure 2. ADC-228A Timing Diagram



Figure 3. ADC-228A Typical Connections

MECHANICAL DIMENSIONS INCHES (mm)



ORDERING INFORMATION

MODEL	TEMPERATURE RANGE	SAMPLING RATE
ADC-228AMC	0 to +70°C	20MSPS
ADC-228AMM	–55 to +125°C	20MSPS
ADC-228A/883 *	–55 to +105°C	15MSPS

Receptacle for PC board mounting can be ordered through AMP Inc., part # 3-331272-8 (component lead socket), 24 required. Contact DATEL for 883 product specifications

* DATEL's initial qualification was done at 15MSPS and as a Mil-STD-883 Class G product per customer request. Mil-STD-883 Class G allows for a reduced temperature range (-55°C to +105°C) that applies to this device. Contact DATEL if a 20MSPS rate or a Mil-STD-883 Class H (-55°C to +125°C) temperature range is desired.





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DATEL, Inc. 11 Cabot Boulevard, Mansfield, MA 02048-1151 Tel: (508) 339-3000 (800) 233-2765 Fax: (508) 339-6356 Internet: www.datel.com E-mail:sales@datel.com DATEL (UK) LTD. Tadley, England Tel: (01256)-880444 DATEL S.A.R.L. Montigny Le Bretonneux, France Tel: 1-34-60-01-01 DATEL GmbH München, Germany Tel: 89-544334-0 DATEL KK Tokyo, Japan Tel: 3-3779-1031, Osaka Tel: 6-6354-2025

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