

SANYO Semiconductors DATA SHEET

LA6517 LA6517M LA6518M

Monolithic Linear IC

2-Output Power Operational Amplifier

Overview

The LA6517, LA6517M, and LA6518M are 2-output power operational amplifiers developed for use in consumer and industrial equipment.

Features

- High output current ($I_O \max = 0.5A$).
- High gain.
- Includes a current limiter.
- Wide operating voltage range (± 2 to $\pm 18V$).
- Single-supply operation possible (4 to 36V).
- Thermal shutdown built in.

Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} /V _{EE}		±18	V
Differential input voltage	V _{ID}		30	V
Common-mode input voltage	VIN		±15	V
Allowable power dissipation	Pd max	LA6517	1000	mW
		LA6517M	350	mW
		LA6518M	700	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

- Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.
- Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

LA6517, 6517M, 6518M

Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	VCC/VEE		±2 to ±16	V

Electrical Characteristics at Ta = 25°C, $V_{CC}/V_{EE} = \pm 15V$

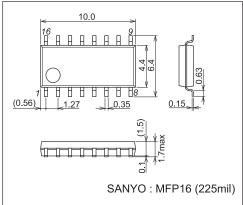
Parameter	Symbol	Conditions	min	typ	max	Unit
No-load current drain	Icc			8	20	mA
Input offset voltage	V _{IO}	$R_S \le 10k\Omega$		2	7	mV
Input offset current	lο			10	100	nA
Input bias current	IB			100	300	nA
Common-mode input voltage range	VICM	LA6517, 6517M	-15		+13	V
		LA6518M	-14		+13	V
Common-mode signal rejection ratio	CMRR		65	80		dB
Maximum output voltage	VO	$R_L = 33\Omega$	±11	±12		V
Voltage gain	VGO			85		dB
Slew rate	SR	$G_V = 0$, $R_L = 33\Omega$, $R = 10\Omega$, $L = 0.1\mu F$		0.15		V/μs
Supply voltage rejection ratio	SVR			30	300	μV/V
Limiting current (built in)	I _{SC}			0.5		Α

Package Dimensions

unit : mm (typ)

3001D [LA6517]

unit : mm (typ) 3035B [LA6517M]



unit : mm (typ) 3097B

3097B [LA6518M]

12.5

16.6

17.5

18.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

19.6

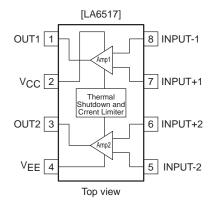
19.6

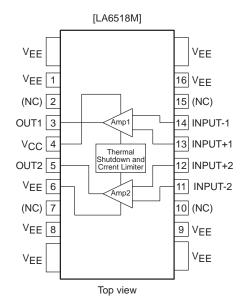
19.6

19.6

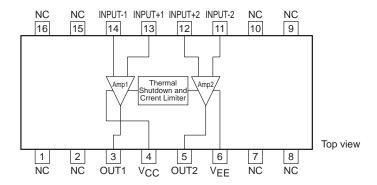
1

Block Diagram and Pin Assignments

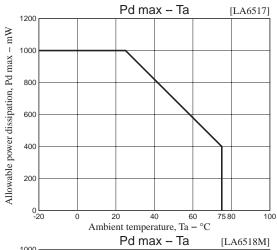


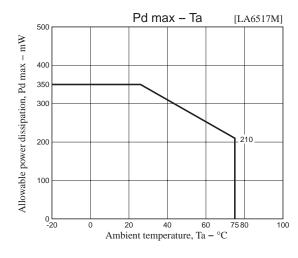


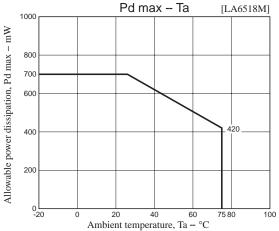
[LA6517M]



LA6517, 6517M, 6518M

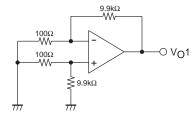


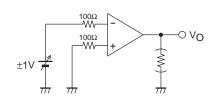




Test Circuits

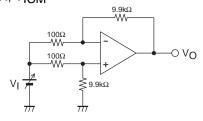
1. V_{IO}, SVRR





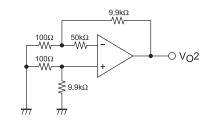
$$\begin{aligned} V_{IO} : V_{CC} / V_{EE} = & \pm 15V & V_{IO} = V_{O}1 / 100 \\ SVRR \begin{bmatrix} V_{CC} = 15V, 5V & SVR (+) \\ V_{EE} = -5V, -15V & SVR (-) \end{bmatrix} = \begin{vmatrix} \Delta V_{O}1 \\ 100 \times 10V \end{vmatrix} \end{aligned}$$

3. CMRR, VICM



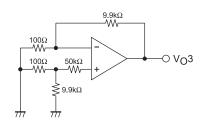
$$\begin{split} &CMRR: V_I = \pm 7.5V \\ &CMR = 20log \left. \frac{15 \times 100}{\left| \Delta V_O \right|} \right. \end{split}$$

4. IB (-)



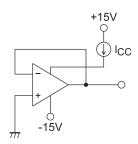
$$I_{B}(-) = \frac{|V_{O}2 - V_{O}1|}{50k\Omega \times 100}$$

5. IB (+)

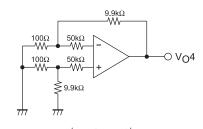


$$I_B(+) = \frac{|V_O 3 - V_O 1|}{50k\Omega \times 100}$$

7. ICC

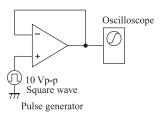


6. I_{IO}

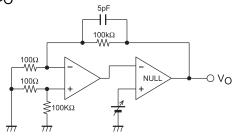


$$I_{IO} = \frac{|V_O4 - V_O1|}{50k\Omega \times 100}$$

8. SR



9. VGO



$$VG_{O} = 20log \frac{1000 \times 20}{\Delta V_{O}}$$

- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned

This catalog provides information as of May, 2008. Specifications and information herein are subject to change without notice.