

SANYO Semiconductors **DATA SHEET**

LA6581M—Fan Motor Driver BTL Driver Single-Phase Full-Wave

Overview

The LA6581M is single-phase bipolar fan motor is driven, through BTL output linear drive, at high efficiency, low power, and low sound by suppressing the reactive power. Lock protection, rotary signal (FG, RD) circuits are incorporated, which is optimum for the notebook PC, consumer equipment power supply, car audio system, CPU cooler, etc. that require high reliability and low noise.

Features

• Single-phase full-wave linear drive for fan motor

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------|----------------------|---------------------------------|-------------|------|
| Supply voltage | V _{CC} max | | 18 | V |
| OUT output current | I _{OUT} max | T<200ms | 0.50 | Α |
| OUT output withstand voltage | V _{OUT} max | | 18 | V |
| FG output withstand | VFG max | | 18 | V |
| FG output current | IFG max | | 5 | mA |
| Allowable dissipation | Pd max | Mounted on a specified board *1 | 800 | mW |
| Operating temperature | Topr | *2 | -30 to +100 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |

^{*1:} Mounted on a specified board: 114.3mm×76.1mm×1.6mm glass epoxy

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SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

^{*2:} Tj = 150°C max must not be exceeded.

Recommended Operating Conditions at Ta = 25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|--|--------|------------|-----------------------------|------|
| Supply voltage | Vcc | | 2.2 to 16 | V |
| Common-phase input voltage range of Hall input | VICM | | 0.3 to V _{CC} -1.5 | V |

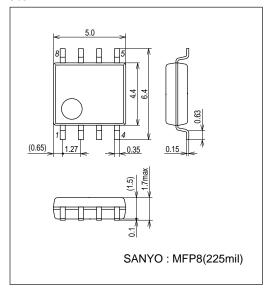
Electrical Characteristics at Ta = 25°C, $V_{CC} = 12.0$ V, unless especially specified.

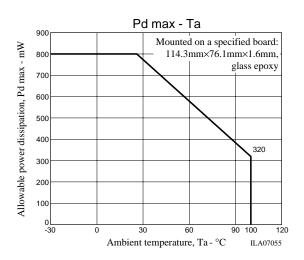
| Parameter | Symbol Conditions | 0 10 | Ratings | | | 1.1-2 |
|------------------------------|-------------------|---------------------------------------|---------|------|------|-------|
| | | Conditions | min | Тур | Max | Unit |
| Circuit Current | lcc | IN-=5.8V, IN+=6.0V, R _L =∞ | | 14 | 19 | mA |
| OUT output low voltage | V _{OL} | I _O =100mA | | 0.1 | 0.2 | V |
| OUT output high voltage | VOH | I _O =100mA | | 0.1 | 0.2 | V |
| Hall bias voltage | VHB | RH=360Ω +91Ω | 1.85 | 1.95 | 2.05 | V |
| Hall amplifier gain | Vg | | 52 | 55 | 58 | dB |
| Hall amplifier input current | VINR | | -10 | -2 | 10 | μΑ |
| FG output low voltage | VFG | IFG=3mA | | 0.2 | 0.3 | V |
| FG output leakage current | IFGL | VFG=7V | | | 30 | μΑ |
| Thermal protection circuit | Th | * Design guarantee | 150 | 180 | 200 | °C |

^{*:} These values are design guarantee values, and are not tested.

Package Dimensions

unit : mm (typ) 3032D

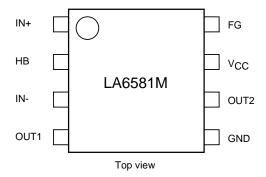




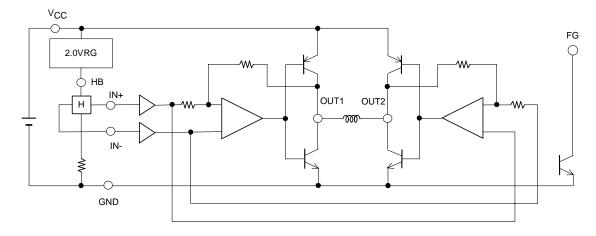
Truth Table

| IN- | IN+ | OUT1 | OUT2 | FG | mode | |
|-----|-----|------|------|-----|----------------------------|--|
| Н | L | Н | L | L | During sateting | |
| L | Н | L | Н | Off | During rotation | |
| - | - | Off | Off | - | During overheat protection | |

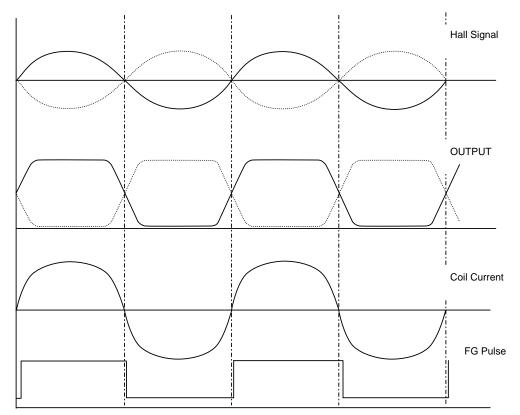
Pin Assignment



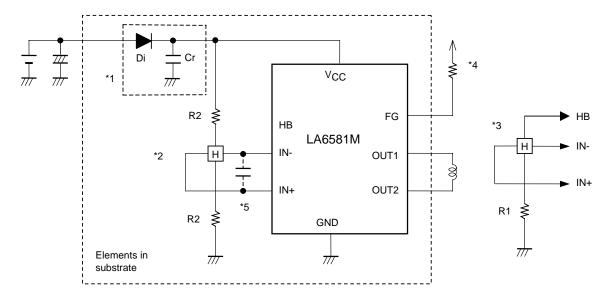
Block Diagram



Timing Chart



Sample Application Circuit



- *1: When Di to prevent breakdown in case of reverse connection is used, it is necessary to insert a capacitor Cr to secure the regenerative current route. Similarly, Cr is necessary to enhance the reliability when there is no capacitor near the fan power line.
- *2: To obtain Hall bias from V_{CC}, carry out 1/2× V_{CC} bias as shown in the figure. Linear driving is made through voltage control of the coil by amplifying the Hall output. When the Hall element output is large, the startup performance and efficiency are improved. Adjustment of the Hall element can reduce the noise further.
- *3: When the Hall bias is taken from the HB pin, constant-voltage bias is made with about 2.0V. Therefore, the Hall element can provide the output satisfactory in temperature characteristics. Adjustment of the Hall output amplitude is made with R1. (When V_{CC} = 12V, the step *2 above proves advantageous for IC heat generation.)
- *4: Keep this open when not used.
- *5: When the wiring from the Hall output to IC Hall input is long, noise may be carried through the wiring. In this case, insert the capacitor as shown in the figure.
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