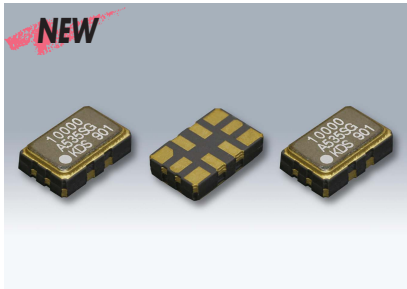


Ultra High-precision SMD VC-TCXO/TCXO

DSA535SG/DSB535SG for Stratum3/ Femtocell



Actual size

Features

- 5032 size. 1.35mm high miniature TCXO (0.024cc · 0.08g)
- Wide voltage range. Supply voltage up to +2.7~+5.5V
- Clipped-sine wave or CMOS level output
- Low phase noise
- Single packaged structure
- Moisture Sensitivity Level: LEVEL 1 (IPC/JEDEC J-STD-033)



Applications

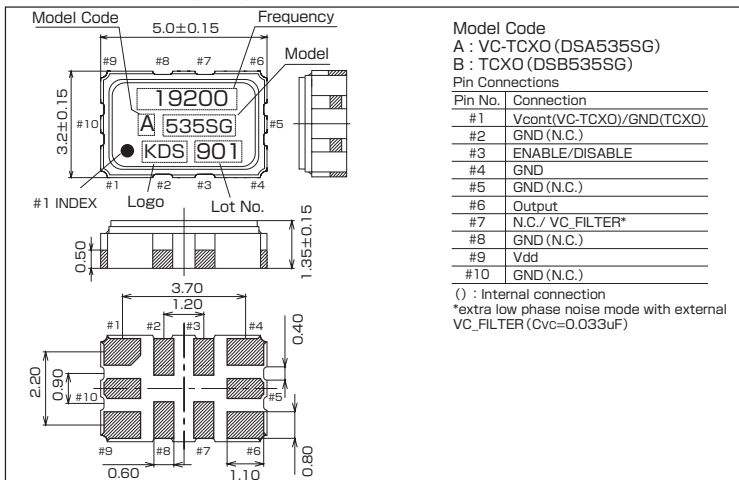
- Stratum3/ Femtocell/ Industrial radio communications

Standard Specification

Item	DSA535SG (VC-TCXO)		DSB535SG (TCXO)	
Output Frequency Range	10MHz~52MHz			
Standard Frequency	10/ 12.8/ 19.2/ 19.44/ 20/ 38.88MHz			
Supply Voltage (Vdd)	+3.0V/ +3.3V/ +5.0V			
Output Level	Clipped sine wave 0.8Vp-p min. (DC-coupled)	CMOS '0'level0.1×Vdd V max. '1'level0.9×Vdd V max.	Clipped sine wave 0.8Vp-p min. (DC-coupled)	CMOS '0'level0.1×Vdd V max. '1'level0.9×Vdd V max.
Current Consumption				
10≤F≤20MHz	+2.0mA max.	+3.5mA max.	+2.0mA max.	+3.5mA max.
20<F≤40MHz	+2.5mA max.	+5.0mA max.	+2.5mA max.	+5.0mA max.
F>40MHz	+3.5mA max.	+6.0mA max.	+3.5mA max.	+6.0mA max.
Output Load	10kΩ//10pF	15pF	10kΩ//10pF	15pF
Frequency Stability Tolerance	±1.5×10 ⁻⁶ max. (After 2 reflows)			
vs. Temperature	±0.28×10 ⁻⁶ max./ -40 ~+85°C			
vs. Hysteresis	±0.10×10 ⁻⁶ max./ -10~+70°C (Option)			
vs. Supply Voltage	±0.10×10 ⁻⁶ max.			
vs. Load Variation	±0.20×10 ⁻⁶ max. (Vdd±5%)			
vs. Aging	±0.20×10 ⁻⁶ max. (10kΩ//10pF±10%)			
vs. Aging	±1.0×10 ⁻⁶ max. /year			
Frequency Control Control Sensitivity	±3.0×10 ⁻⁶ ~±5.0×10 ⁻⁶ / Vcont=+1.5V±1V		—	
Response Slope	±3.0×10 ⁻⁶ ~±5.0×10 ⁻⁶ / Vcont=+2.5V±2V (O.P.Vdd=5V)		—	
Phase Noise	Positive			
Offset 100Hz	Offset 100Hz	Offset 1kHz	Offset 10kHz	Offset 100kHz
10≤F≤20MHz	-118dBc/Hz	-138dBc/Hz	-150dBc/Hz	-152dBc/Hz
20<F≤40MHz	-110dBc/Hz	-132dBc/Hz	-148dBc/Hz	-150dBc/Hz
F>40MHz	-100dBc/Hz	-128dBc/Hz	-145dBc/Hz	-150dBc/Hz
Packing Unit	1000pcs./reel (φ180)			

Consult our sales representative for other specifications.

Dimensions [mm]



Recommended Land Pattern [mm]

