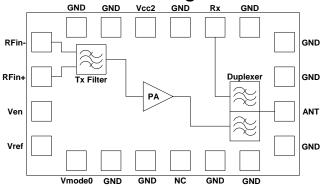






Functional Block Diagram



#### Product Description

TriQuint's TQM613028 is a fully matched CDMA cellular band PA/Duplexer (PAD) module for use in mobile phones. The 7.0 x 4.0 x 1.2 mm, 20-pin module includes a SAW Duplexer, Power Amplifier, Differential input transmit filter, and Logic Controller. With an RF Power Output up to 25.5dBm the TQM613028 PAD meets the strict ACPR and ALTR requirements for products designed to the CDMA IS-95/98/2000 standards. It's thin form factor and compact size, coupled with the low quiescent current, makes the TQM613028 ideal for today's compact feature rich multi-media handsets requiring longer battery life.

TriQuint's multilayer laminate technology provides low loss interconnect and optimized match between the duplexer, PA and filter enabling the TQM613028 to achieve only 40 mA of typical current consumption at maximum output power in low power mode (+13.5dBm). The small  $7.0\times4.0$  mm module replaces four separate components and matching requiring less board space. TQM613028 provides handset designers with a simple to use surface mount module requiring minimal external circuitry for faster time to market and reduced BOM count.

# Electrical Specifications

Test Conditions V<sub>CC</sub>=3.4 V, V<sub>REF</sub> =2.85V, T=+25°C

Parameter	Min	Тур	Max	Units
Frequency	824.7	836.5	848.3	MHz
Pout		+24.5		dBm
ACPR (+/- 885kHz offset)		-50		dВс
ALTR (+/- 1.98 MHz offset)		-60		dВс
Current Consumption @ +24.5 dBm		395		mΑ
Current Consumption @ +13.5 dBm		40		mA
Quiescient Current		18		mΑ
Leakage at Rx Port		-31		dBm
ANT-to-Rx Insertion Loss		2.6		dВ

#### Features

- InGaP GaAs HBT PA with high efficiency at low power architecture
- Low Quiescent Current in LPM

Typical: 18 mA

Low Current Consumption
 Typical: 395 mA @ +24.5dBm

Typical: 395 mA @ +24.5dBm Typical: 40 mA @ +13.5dBm

Excellent ACPR

Typical: -50 dBc @ +/- 885kHz offset

Excellent ALTR

Typical: -60 dBc @ +/- 1.98 MHz offset

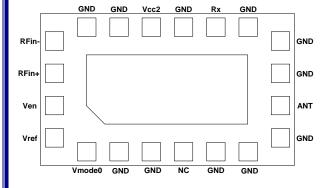
- Lead-free 260°C RoHS Compliant
- Small 20-pin, 7.0x4.0mm module with matching replacing ~10 discrete components
- Height of 1.17mm for thin phones
- Integrated duplexer, differential input transmit filter and matching
- Optimized for use with Qualcomm's QSC6010/20/30<sup>TM</sup> single chip devices

# **Applications**

- IS-95/98/CDMA2000
- Single-band CDMA Cellular radios

# Package Style

TOP VIEW PIN NAMES







#### Absolute Maximum Ratings 1

Parameter	Symbol	Min Rating	Max Rating	Unit	
Supply voltage	Vcc	-	+6.0	V	
Reference Voltage	Vref	-0.5	+3.0	V	
Control voltages	Ven, Vmode	-0.5	+3.0	V	
Input RF power	Tx port	-	+10	dBm	
	Antenna port	-	+ 30	dBm	
	Rx port	-	+10	dBm	
Case Operating Temperature	Tcase	-30	+85	°C	
Storage temperature	Tstore	-30	+150	°C	
MSL		MSL-3, +260°C			

Note 1: Stresses above those listed under absolute maximum ratings may cause permanent and functional damage to the device. Exposure exceeding absolute maximum rating conditions for extended periods may affect device reliability.

#### DC Electrical Characteristics

Parameter	Condition	Symbol	Min	Тур	Max	Unit
Supply Voltage	High / low power	Vcc	3.20		4.25	V
Reference Voltage	-	Vreg	2.79		2.91	V
Frankla Dirikal Cartael Valtage	Low	Ven	0.00		0.50	V
Enable Digital Control Voltage	High	Ven	2.15		2.75	V
Enable Digital Control Current	-	len			1	mA
417 5 0 4 10	Low	Vmode	0.00		0.50	V
1 bit Bias Control Current	High	Vmode	2.15		2.75	V
1 bit Bias Control Current	Current	Imode			1	mA
Operating Current High Power Mode	Pout=+24.5dBm, T=+25°C, Vcc=3.4V	lcc_high		395		mA
Operating Current Low Power Mode	Pout=+13.5dBm, T=+25°C, Vcc=3.4V	lcc_low		40		mA
Quiescent Current	Low Power Mode	Icq		18		mA
l(0	Ven=high	Iref			5	mA
Iref Current	Ven=low	Iref			1	mA



#### DC Electrical Characteristics (Continued)

Parameter	Condition	Symbol	Min	Тур	Max	Unit
Leakage Current	Vcc=high, enable=low, Vmode0=X	lleak			10	μA
Turn on/off time <sup>1</sup>	DC:lcc	Ton-DC, Toff-DC			20	μS
	RF: Pout	Ton-RF,Toff-RF			6	μS
Gain Switching Time	High-low	tmode			10	μS
Ambient Operating Temperature		TEMPop	-30		+85	°C
Case Temperature	Ambient operating temp not exceeded	TEMPcase	-20		TBD	°C

#### 1. Definition of switching time:

 $T_{ON}$ -DC = The time required to obtain the idle bias condition  $\pm 10\%$  from a zero bias condition with RF signal applied.

 $T_{ON}$ -RF = The time required to go from zero output power to reach the final output power  $\pm$  1 dB.

 $T_{OFF}$ -RF = The time required to for the output power to decrease 30 dB when input signal is removed.

 $T_{OFF}$ -DC = The time required for the bias current (idle current) to decrease to < 100  $\mu$ A.

#### Power Range Truth Table

Mode of operation	Ven	V <sub>MODE</sub>	Vcc
Power down	LOW	LOW	ON
Stand-by mode	LOW	Х	ON
High power mode (13.5 dBm $\leq$ P <sub>OUT</sub> $\leq$ 24.5 dBm)	HI	LOW	ON
Low power mode (-50 dBm $\leq$ P <sub>OUT</sub> $\leq$ 13.5 dBm)	HI	HIGH	ON

Note 1: Logic Low is 0 V to +0.5V, Logic High is +2.35 V to +2.85V



#### RF Electrical Characteristics

Tx to Antenna Port Parameters						
Parameter	Condition	Min	Тур	Max	Unit	
Frequency	-30°C <t<+85°c< td=""><td>824.7</td><td></td><td>848.3</td><td>MHz</td></t<+85°c<>	824.7		848.3	MHz	
Maximum Output Power	Vcc=3.2-4.25V; -30C <t<85c; vref="2.85V&lt;/td"><td>24.5</td><td></td><td></td><td>dBm</td></t<85c;>	24.5			dBm	
October Today	Pout=24.5dBm, Vcc=3.4V;836.5MHz ;T=25C ; Vref=2.85V	19	22	25	dB	
Gain in Tx band	Pout <u>&lt;</u> 13.5dBm, Vcc=3.4V ; T=25C ; Vref=2.85V	10	12.5	15	dB	
Gain Flatness Tx band	13.5dBm <pout<24.5dbm Vcc=3.4V; -30C<t<85c; vref="2.85V&lt;/td"><td>-1.5</td><td></td><td>1.5</td><td>dB</td></t<85c;></pout<24.5dbm 	-1.5		1.5	dB	
Gain Flattiess 1x band	-50dBm <pout<13.5dbm Vcc=3.4V; -30C<t<85c; vref="2.85V&lt;/td"><td>-1.5</td><td></td><td>1.5</td><td>dB</td></t<85c;></pout<13.5dbm 	-1.5		1.5	dB	
Cain Sanaitivity	Vcc=3.2V to 4.25V; T=25C; Vref=2.85V	-1.25		1.25	dB	
Gain Sensitivity	Vcc=3.4V; -30C <t<85c; vref="2.85V&lt;/td"><td>-2.5</td><td>-</td><td>2</td><td>dB</td></t<85c;>	-2.5	-	2	dB	
	Tx Port		200		Ohms	
Impedance	Antenna Port		50	2:1	Ohms	
	Rx Port		50	2:1	Ohms	
Amplitude imbalance	Tx Port		0	±1	dB	
Phase imbalance	Tx Port		2	±8	Deg	
Adjacent Channel Power (ACP)  Offset = +/- 885kHz	Vcc=3.8V; -30C <t<85c; 24.5="" dbm<br="" pout="" ≤="">Vref=2.85V</t<85c;>	m -52 -45		dBc		
Alternate Channel Power (ALT)  Offset = +/- 1.98MHz	Vcc=3.8V; -30C <t<85c; 24.5="" dbm<br="" pout="" ≤="">Vref=2.85V</t<85c;>		-62	-56	dBc	
Stability, Spurious	VSWR ≤8:1 @ all phases in Tx Band Pout=24.5dBm; Pin=10dBm			-65	dBc	
Ruggedness	VSWR ≤20:1 @ all phases Pout=24.5dBm; Pin=10dBm	No damage No degradation		-		



#### RF Electrical Characteristics (Continued)

Tx to Antenna Port Parameters					
Parameter	Condition	Min	Тур	Max	Unit
Attenuation	0 - 800 MHz	20	30		dB
Rx	869 - 894 MHz	45	50		dB
GPS	1570 - 1580 MHz	50	60		dB
2fo	1638 - 1708 MHz	50	60		dB
PCS Rx	1930 - 1990 MHz	45	55		dB
3fo	2462 - 2557 MHz	50	60		dB
Harmonics, 2fo, 3fo	Pout<24.5dBm		-45	-38	dBc
GPS Noise Power in GPS Band at 824.7MHz @ ANT terminal <sup>1</sup>	T=25C		-182		dBm/Hz

<sup>1.</sup> Assumes = 130dBm/Hz @ 1575.42 MHz injected at input to the Transmit port.

Antenna to Rx Port Parameters					
Parameter	Condition	Min	Тур	Max	Units
Frequency	-30C <t<85c< td=""><td>869</td><td></td><td>894</td><td>MHz</td></t<85c<>	869		894	MHz
Insertion Loss	-30C <t<85c< td=""><td></td><td>2.6</td><td>3.5</td><td>dB</td></t<85c<>		2.6	3.5	dB
Return Loss	-30C <t<85c< td=""><td>8</td><td>12</td><td></td><td>dB</td></t<85c<>	8	12		dB
Attenuation	0 - 800 MHz	20	27		dB
Attenuation: Transmit <sup>1</sup>	824 - 849 MHz	47	52		dB
Attenuation: GPS	1570 - 1580 MHz	50	55		dB
Attenuation: PCS & IMT Bands	1850 - 2170 MHz	35			dB
Attenuation: 3LO & ISM Band	2400 - 2600 MHz	35	47		dB

<sup>1.</sup> Typical/minimum specified at center band



#### RF Electrical Characteristics (Continued)

Tx to Rx Port Parameters					
Parameter	Condition	Min	Тур	Max	Units
Noise Power in Rx Band at Rx terminal <sup>1,2</sup>	Pout=24.5dBm; Vcc=3.4V, 824.7MHz T=25C, 44dB noise input		-182		dBm/Hz
Tx Leakage at Rx terminal <sup>2</sup>	Pout=24.5dBm; Vcc=3.4V -30C <t<85c, vswr="2:1&lt;/td"><td></td><td>-31</td><td>-28.5</td><td>dBm</td></t<85c,>		-31	-28.5	dBm

Assumes -130 dBm/Hz Rx band noise injected at input to the Tx port. Noise power is computed from a differential NF measurement of the Rx path while under CDMA Tx Input RF drive with an added noise of 44dB above thermal noise floor.

<sup>2.</sup> Antenna port terminated into a 2:1 VSWR.



#### **Evaluation Board**

TriQuint offers our customers the below evaluation board as a means for testing and analysis of the TQM613028. The evaluation board schematic and picture are provided for preliminary analysis and design. Figure 1 shows the TriQuint application board while Figure 2 shows the schematic of the board followed by the power-up/power-down sequence instructions.

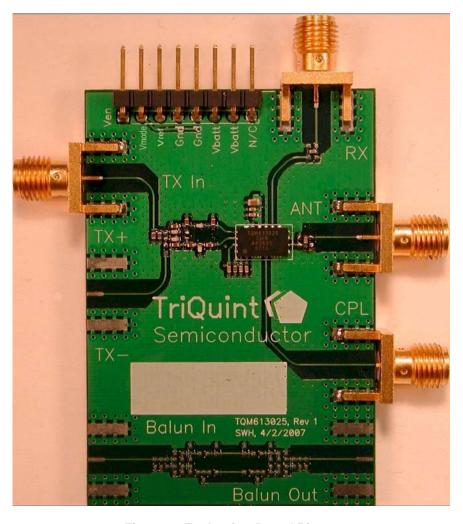


Figure 1: Evaluation Board Picture





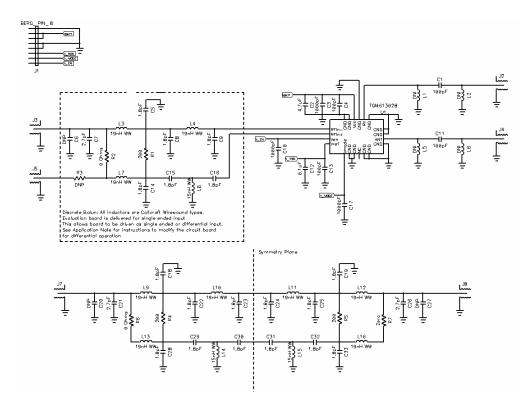


Figure 2: Evaluation Board Schematic

#### Applications Information: Power Up/Down Sequences

Power-Up Sequence				Power-Down	Sequence
Sequence	PIN	Description	Sequence	PIN	Description
1	VCC	Apply Battery Voltage	1	RF	Remove RF
2	VREF	Apply Reference Voltage	2	VEN	Disable PA
3	Vmode	Set Bias Mode	3	Vmode	Set Bias Mode to 0V
4	VEN	Enable PA	4	VREF	Remove Reference Voltage
5	RF	Apply RF	5	VCC	Remove Battery Voltage



# **Packaging Characteristics**

Package Pin Assignments

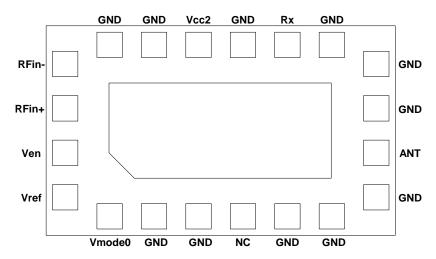


Figure 3: Package Pin-Out Identification Drawing
Top View (X-ray)

Note: DC Block included inside the module

Pin #	Description	Function
1	VREF	Reference Voltage
2	Vmode	High/Medium Power Mode selection
3	GND	Ground
4	GND	Ground
5	NC	No Connect
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	ANT	Antenna Port
10	GND	Ground
11	GND	Ground
12	GND	Ground
13	RX	Receiver output port from Duplexer
14	GND	Ground
15	VCC	Power Supply
16	GND	Ground
17	GND	Ground
18	RFin-	Tx Differential Input (-)
19	RFin+	Tx Differential Input (+)
20	VEN	Enable Voltage input



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#### PC Board Layout Recommendations

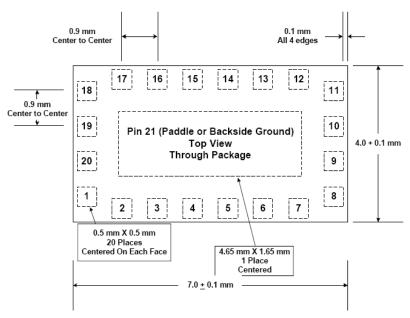
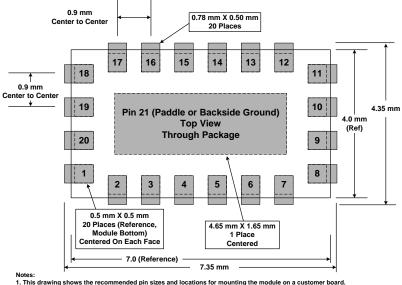


Figure 4: 7x4mm Module Package Drawing for Footprint Design



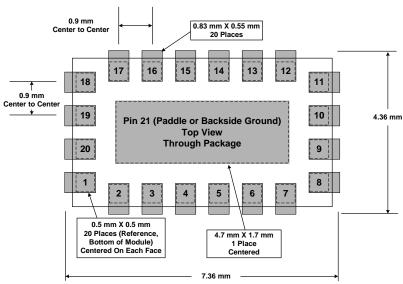
1. This drawing shows the recommended pin sizes and locations for mounting the module on a customer board.

2. Pins that are "No Connect" or that net to ground may be buried in any surface copper pour used for ground plane. TriQuint does not recommend any thermal relief for surface ground pins. These pins should be buried in the surface copper.

Figure 5: Recommended Copper Footprint for 7x4mm Module



#### PC Board Layout Recommendations (continued)



- Notes:

  1. This drawing is a negative image. Colored areas indicate the <u>absence</u> of solder mask material.

  2. Dimensions reflect a solder mask swell of 0.05 mm. Depending on the board vendor's tolerances, this dimension might need to grow to a solder mask swell of 0.1 mm. Check with your vendor and quality departments.

Figure 6: Solder Mask Layout for 7x4mm Module Package

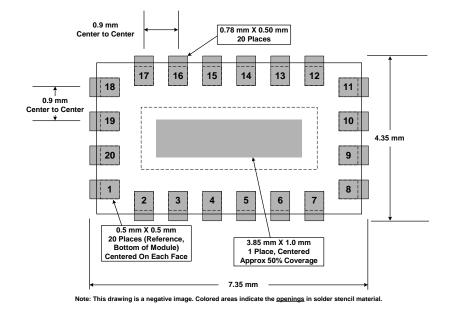


Figure 7: Solder Stencil Option for 7x4mm Module

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Preliminary Data Sheet: Subject to change without notice

For additional information and latest specifications, see our website: www.triquint.com Revision D, May 2008

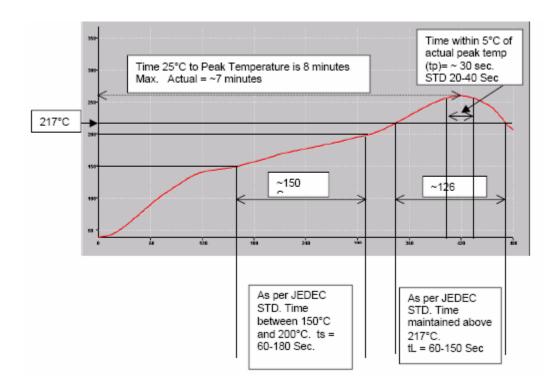


# **Preliminary Data Sheet**

# **CDMA Cellular Band PA/Duplexer Module**

#### Recommended Reflow Profile

The TQM613028 is rated for 260°C reflow profile. Below is a general recommendation for 260°C reflow. The specific profiled used will need to take into account the requirements of the board used, other components used, and the specific layout of the components. The following recommendation should be used as a guideline only.



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# **Preliminary Data Sheet**

### **CDMA Cellular Band PA/Duplexer Module**

#### Package Dimensions

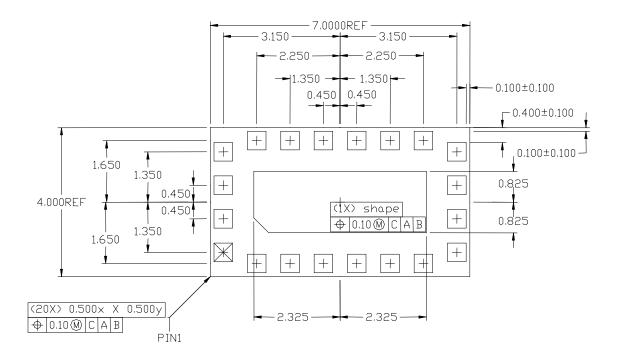


Figure 8: Package Drawing Top View (X-ray)

Note: Height is 1.12mm typical / 1.20mm maximum



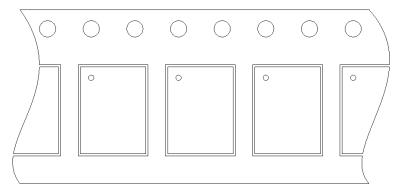
#### Package Marking

#### **TOP MARK**



- 1) Line 1: Product code = TQM613028
- 2) Line 2: Country Code = CCCC (USA = United States, PHIL = Philippines)
- 3) Line 3: AaXXXX-Z = Aa = Vendor code + XXXX = TriQuint Lot Number + Z = Sub lot # (1, 2, 3, ...)
- 4) Line 4: YYWW = Year and Work Week

#### Tape and Reel Specification:



# MODULE 4x7 and 5x8 User Direction of Feed →

Carrier tape - 3M part # 3M053091, Ao = 4.55mm, Bo = 7.60mm, Ko = 1.73mm, width = 16mm, pitch = 8mm Cover tape - 3M part # 2678 13.3mm wide

Note: Package Quantity is 2,500 pcs per reel



# Shipment Box & Label Description:

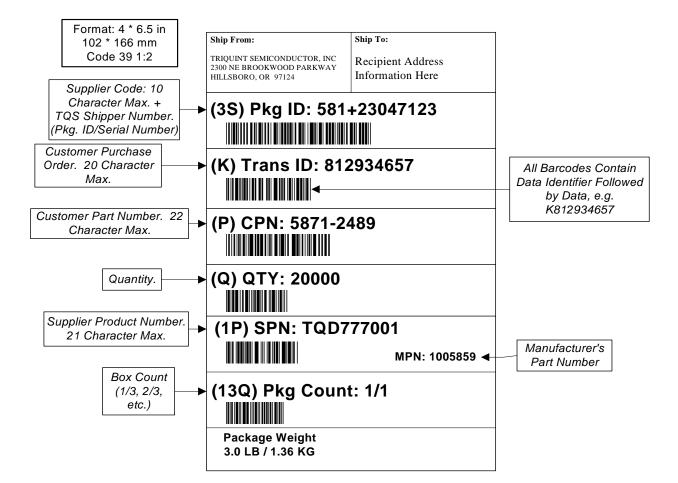
Tape & Reels will be packaged in a dry-pack bag and then in a shipment box. The box dimensions will depend on the number of reels shipped in each box and are noted in the table below. The box label and a description of each item on the label are also shown below.

CDMA Cellular Band PA/Duplexer Module

13 Inch x16mmDrypack					
Box Size	Reel Qty/Box	Empty Box Wt w/ Packing			
15x15x7	3	2			
18x15x11	5	2.36			
17x16x17	9	2.76			







Preliminary Data Sheet: Subject to change without notice

For additional information and latest specifications, see our website: <a href="https://www.triquint.com">www.triquint.com</a>

Revision D, May 2008





# TQM613028

**Preliminary Data Sheet** 

### **CDMA Cellular Band PA/Duplexer Module**

#### Additional Information 1 T

This part is compliant with RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

The part is rated Moisture Sensitivity Level 3 at 260°C per JEDEC standard IPC/JEDEC J-STD-020.

TQM613028 7.0 x 4.0 x 1.2 mm package has gold (Au) plated contacts.

1 For latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

 Web: <u>www.triquint.com</u>
 Tel: (503) 615-9000

 Email: info\_wireless@tqs.com
 Fax: (503) 615-8902

For technical questions and additional information on specific applications:

Email: info\_wireless@tqs.com

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