TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

# **TPCA8048-H**

Switching Regulator Applications

Motor Drive Applications

DC-DC Converter Applications

- · Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: Q<sub>SW</sub> = 19 nC (typ.)
- Low drain-source ON-resistance:  $R_{DS}$  (ON) = 4.3 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 118 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- Enhancement mode:  $V_{th}$  = 1.3 to 2.3 V ( $V_{DS}$  = 10 V,  $I_D$  = 1.0 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

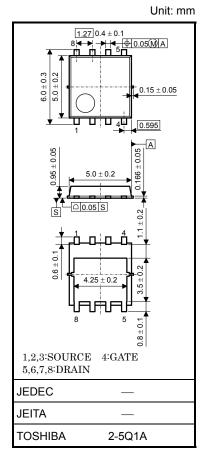
Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	60	V	
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	60	V	
Gate-source voltage		$V_{GSS}$	±20	٧	
Drain current	DC (Note 1)	I <sub>D</sub>	35	Α	
Diam current	Pulsed (Note 1)	I <sub>DP</sub>	105		
Drain power dissipati	on (Tc = 25°C)	$P_{D}$	45	W	
Drain power dissipati	on (t = 10 s) (Note 2a)	$P_{D}$	2.8	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.6	W	
Single-pulse avalanc	he energy (Note 3)	EAS	88	mJ	
Avalanche current		I <sub>AR</sub>	35	Α	
Repetitive avalanche	energy c = 25°C) (Note 4)	E <sub>AR</sub>	2.46	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

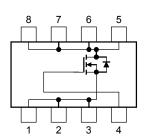
operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.069 g (typ.)

#### **Circuit Configuration**

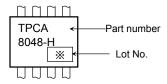




#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	78.1	°C/W

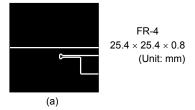
## Marking (Note 5)

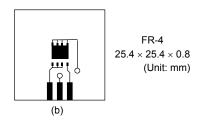


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

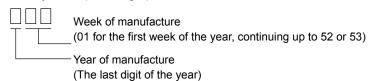




Note 3:  $V_{DD} = 24~V$ ,  $T_{ch} = 25^{\circ}C$  (initial),  $L = 100~\mu H$ ,  $R_G = 25~\Omega$ ,  $I_{AR} = 35~A$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)



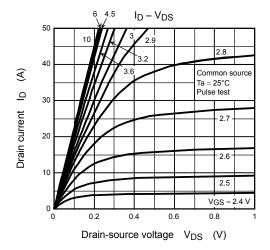


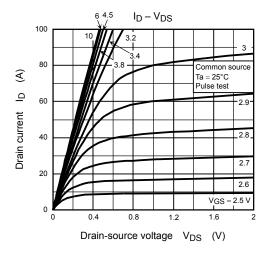
# **Electrical Characteristics (Ta = 25°C)**

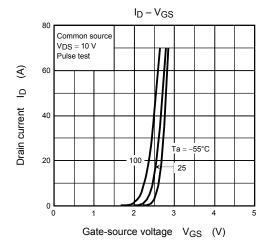
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$ 60	_	_	V	
Diam-source bre	akuowii voitage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	45	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 1.0 \text{ mA}$	1.3	_	2.3	V
Drain source ON	ragistance	RDS (ON)	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 18 A	_	4.8	7.1	0
Diain-source ON	ain-source ON-resistance		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 18 A	_	4.3	6.6	mΩ
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 18 A	59	118	_	S
Input capacitance	9	C <sub>iss</sub>		_	5800	7540	pF
Reverse transfer	capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	210	315	
Output capacitance		Coss		_	650	_	
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	1.0	1.5	Ω
	Rise time	t <sub>r</sub>	VGS 10 V	_	3.6	_	ns
	Turn-on time	t <sub>on</sub>		_	13	_	
Switching time	Fall time	t <sub>f</sub>		_	7.3	_	
	Turn-off time	t <sub>off</sub>	V <sub>DD</sub> ≈ 30 V Duty ≤ 1%, t <sub>W</sub> = 10 μs	_	66	_	
Total gate charge		Qg	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$		90	_	
(gate-source plus	e-source plus gate-drain)		$V_{DD} \approx 48 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 35 \text{ A}$	_	46	_	
Gate-source charge 1		Q <sub>gs1</sub>		_	16	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$		12	_	
Gate switch charge		Q <sub>SW</sub>			19	_	

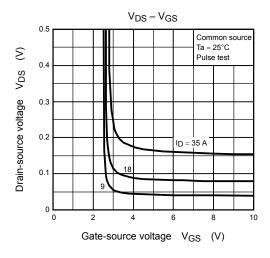
# Source-Drain Ratings and Characteristics (Ta = 25°C)

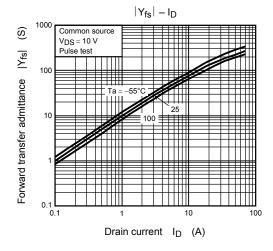
Character	istic		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I <sub>DRP</sub>	_	_	_	105	Α
Forward voltage (diode)			V <sub>DSF</sub>	I <sub>DR</sub> = 35 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V

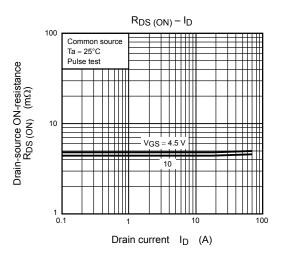




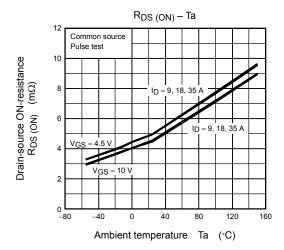


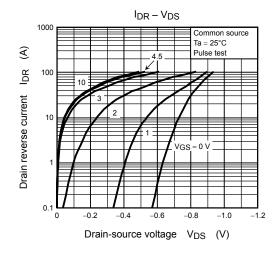


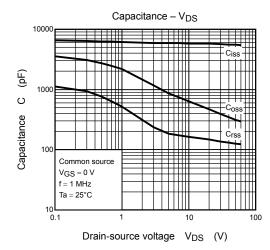


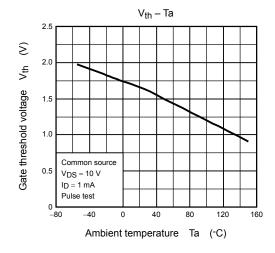


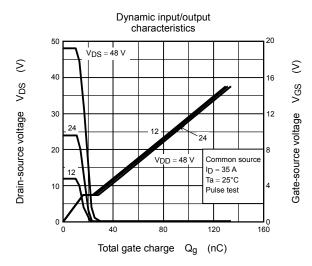
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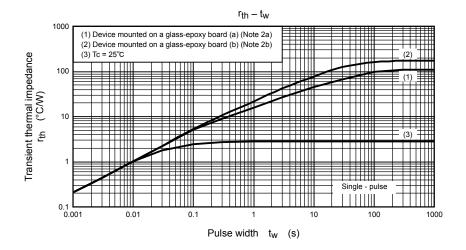


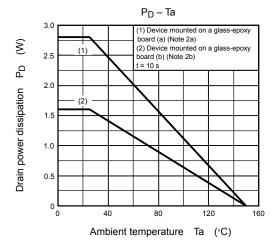


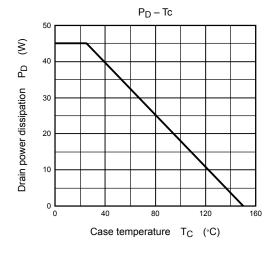


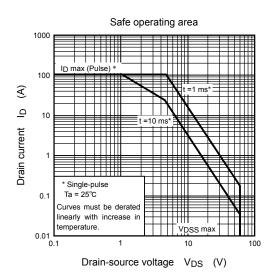


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