

## SOLID STATE DEVICES, INC.

14005 Stage Road \* Santa Fe Springs, Ca 90670 Phone: (562) 404-4474 \* Fax: (562) 404-1773

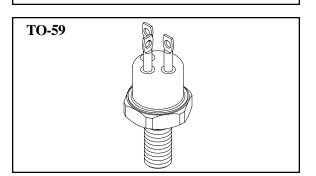
#### **DESIGNER'S DATA SHEET**

#### **FEATURES:**

- Designed for Pulse Modulators in Radar Applications.
- High Surge Current, 100A.
- High Blocking Voltage, 600V min.
- High dv/dt, 200V/us min.
- di/dt = 100A/us.
- Fast Switching Time.
- · Isolated Stud.
- · Hermetically sealed.

## SFS2004/59

### 10 AMP 800 VOLTS HIGH VOLTAGE THYRISTOR



| MAXIMUM RATINGS   |                            |             |       |
|---|----------------------------|-------------|-------|
| CHARACTERISTIC  | SYMBOL                     | VALUE       | UNIT  |
| Peak Repetitive Forward Blocking Voltage  | $V_{DRM}$                  | 800         | Volts |
| Peak Repetitive Reverse Blocking Voltage  | $V_{RRM}$                  | 600         | Volts |
| <b>RMS On-State Current</b> (All Condition Angles, T <sub>C</sub> = 85°C max)   | $I_{T(RMS)}$               | 10          | Amps  |
| <b>Peak Repetitive Surge Current</b> (One Cycle, 60Hz, Pulse width 2µsec, Duty Cycle 0.6%, T <sub>C</sub> = 85°C max) | I <sub>TFM (REP)</sub>     | 100         | Amps  |
| Peak Gate Power   | $P_{GM}$                   | 20          | Watts |
| Average Gate Power (Pulse width 2µsec)  | P <sub>G (AV)</sub>        | 1.0         | Watts |
| Peak Gate Current   | $\mathbf{I}_{\mathbf{GM}}$ | 5.0         | Amps  |
| Peak Gate Voltage   | $V_{GM}$                   | 10          | Volts |
| Operating Junction Temperature Range  | $T_{ m J}$                 | -65 TO +105 | °C    |
| Storage Temperature Range   | $T_{STG}$                  | -65 TO +200 | ∘C    |
| Thermal Resistance Junction to Case   | $\Theta_{	extbf{JC}}$      | 3.0         | °C/W  |

**NOTE:** All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SCR0002A

## SFS2004/59

# ARY SSDI

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| ELECTRICAL CHARACTERISTICS @ $T_J = 25$ °C (Unless Otherwise Specified)  |  |  |     |                 |       |  |  |
|--|--|--|-----|-----------------|-------|--|--|
| RATING   |  | SYMBOL   | MIN | MAX             | UNIT  |  |  |
| Peak Reverse Blocking Current (Rated V <sub>RRM</sub> )  | $\begin{array}{cc} T_J = & 25^{o}C \\ T_J = & 105^{o}C \end{array}$  | $I_{RRM}$  | -   | 0.5<br>2.0      | mA    |  |  |
| Peak Forward Blocking Current (Rated V <sub>DRM</sub> )  | $T_{J} = 25^{\circ}C$<br>$T_{J} = 105^{\circ}C$  | $I_{ m DRM}$   | -   | 0.5<br>2.0      | mA    |  |  |
| Forward On-State Voltage $(I_F = 2.0 A \text{ Peak}, t = 1 \text{ms}, \text{Duty Cycle} \le 1\%)$  |  | $\mathbf{V_F}$   | -   | 1.50            | Volts |  |  |
| Gate Trigger Current $(V_A = 7Vdc, R_L = 100\Omega)$   | $\begin{array}{ccc} T_{J} = & 25^{o}C \\ T_{J} = & 105^{o}C \end{array}$   | $I_{GT}$   | -   | 50<br>100       | mA    |  |  |
| Gate Trigger Voltage $(V_A = 7Vdc, R_L = 100\Omega)$   | $\begin{array}{ccc} T_J = & 25^{\rm o}{\rm C} \\ T_J = & -65^{\rm o}{\rm C} \\ T_J = & 105^{\rm o}{\rm C} \end{array}$ | $ m V_{GT}$  | 0.2 | 1.5<br>2.5      | Volts |  |  |
| Holding Current<br>(V <sub>A</sub> = 7Vdc, R <sub>KG</sub> - Open, T <sub>J</sub> = 105°C)   |  | I <sub>H</sub>   | 0.2 | -               | mA    |  |  |
| $\label{eq:Switch Time} \begin{aligned} \textbf{Switch Time} \\ (I_F = 30\text{A min Pulse}; I_R = 5\text{A}; \\ T_C = 85^{\circ}\text{C};  dV/dt = 250\text{V/}\mu\text{s to }600\text{V}; \\ V_{RA(OFF)} = 0\text{V};  V_{RG(OFF)} = 6\text{V}) \end{aligned}$ | Delay Time<br>Rise Time<br>Turn Off Time   | $egin{aligned} t_{ m d} \ t_{ m r} \ t_{ m off} \end{aligned}$ | 0.2 | 1.5<br>2.5<br>- | µsec  |  |  |

# **PACKAGE OUTLINE: TO-59** PIN OUT: 1. CATHODE 2. GATE 3. ANODE .455 .400 .110 .090 .468 .320 .078 -MAX .110 .090 .380 .318 .215 .185 .437 .380 -3× ø.065 .437 .424 410-32 UNF-2A