TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS IV)

TPC8041

Lithium Ion Battery Applications Portable Equipment Applications Notebook PC Applications

- Small footprint due to small and thin package
- Low drain-source ON-resistance: RDS (ON) = $5.5 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 26 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage	Drain-source voltage		30	V	
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	13	Α	
Drain current	Pulse (Note 1)	I_{DP}	52	A	
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	P_{D}	1.9	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	P_{D}	1.0	W	
Single pulse avalanche energy (Note 3)		E _{AS}	44	mJ	
Avalanche current		I _{AR}	13	Α	
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.066	mJ	
Channel temperature	!	T _{ch}	150	°C	
Storage temperature	range	T _{stg}	–55 to 150	°C	

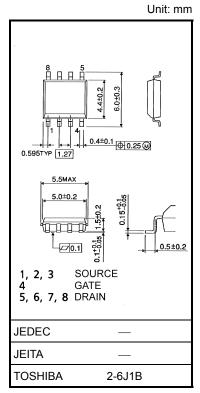
Note 1, Note 2, Note 3 and Note 4: See the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly

even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

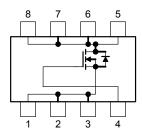
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 0.08 g (typ.)

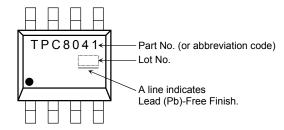
Circuit Configuration



Thermal Characteristics

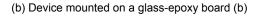
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

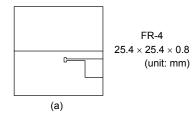
Marking (Note 5)

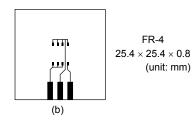


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)



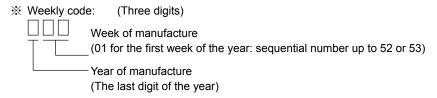




Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $I_{AR} = 13 \text{ A}$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: • on lower left of the marking indicates Pin 1.



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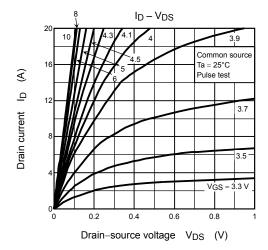


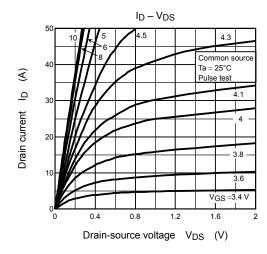
Electrical Characteristics (Ta = 25°C)

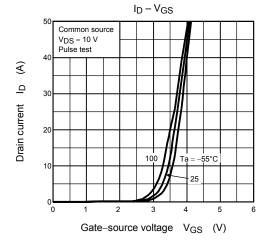
Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cut-OFF cu	ırrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source bre	akdown voltago	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	30	_	_	V
Dialii-Source bre	akuowii voitage	V (BR) DSX	$I_D = 10$ mA, $V_{GS} = -20$ V	10	_	_	V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.3	_	2.5	V
Drain-source ON	rosistanco	Pro (OLI)	$V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$	9 13.5		13.5	mΩ
Diain-source ON	-resistance	R _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 6.5 \text{ A}$	- + ±100 - 10 30 10 1.3 - 2.5	1115.2		
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, I_D = 6.5 \text{ A}$	13	26	_	S
Input capacitance	е	C _{iss}		_	1270	_	
Forward transfer admittance Input capacitance Reverse transfer capacitance Output capacitance Rise time		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	240	_	pF
Output capacitan	ice	C _{oss}		_	380	_	
Output capacitance	Rise time	t _r	Voc 10 V	_	11	_	
Cuitabina tina	Turn-ON time	t _{on}	V _{GS} 10 V I _D = 6.5 A V _{OUT} V _{GS} 10 V V _{OUT} V _{OUT}	_	20	_	
Switching time	Fall time	t _f	4.7 Ω 3. When the second sec	_	15	_	- ns
	Turn-OFF time	t _{off}	$V_{DD}\approx 15~V$ Duty \leq 1%, $t_W=10~\mu s$	_	39	_	
Total gate charge (gate-source plus		Qg			_ 27 _		
Gate-source charge 1		Q _{gs1}	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	4.2	_	nC
Gate-drain ("mille	er") charge	Q _{gd}		_	8.2	_	

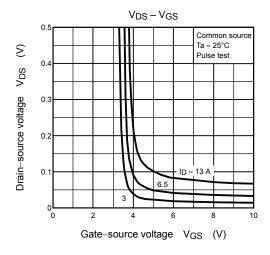
Source-Drain Ratings and Characteristics (Ta = 25°C)

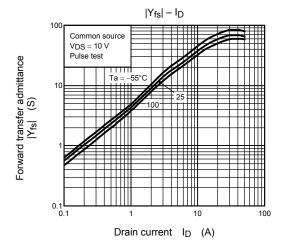
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	52	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

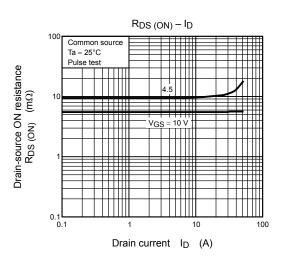




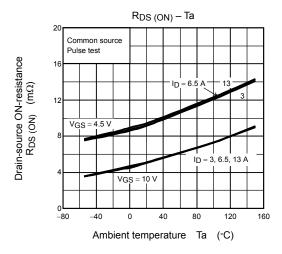


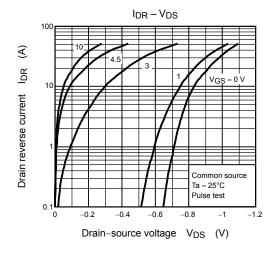


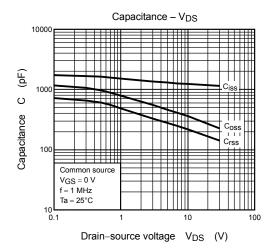


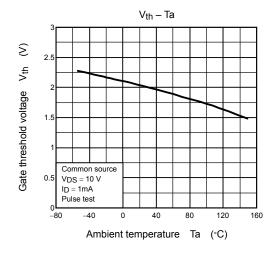


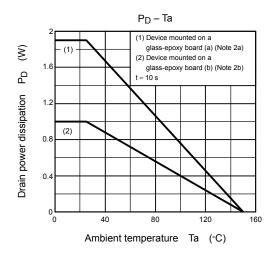
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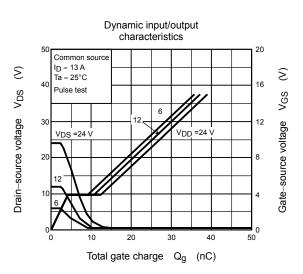




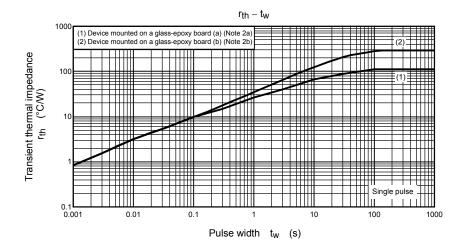


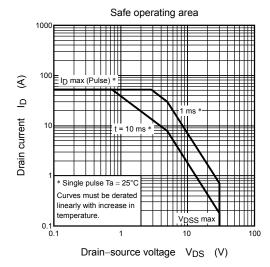






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20070701-EN GENERAL

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