TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

TPCP8601

High-Speed Switching Applications DC-DC Converter Applications Strobo Flash Applications

- High DC current gain: $h_{FE} = 200 \text{ to } 500 \text{ (I}_{C} = -0.6 \text{ A)}$
- Low collector-emitter saturation: $V_{CE (sat)} = -0.19 \text{ V (max)}$
- High-speed switching: tf = 35 ns (typ.)

Maximum Ratings (Ta = 25°C)

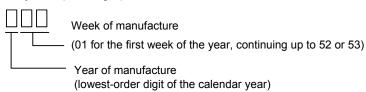
Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V_{CBO}	-20	٧	
Collector-emitter voltage		V _{CEO}	-20	V	
Emitter-base voltage		V _{EBO}	-7	V	
Collector current	DC (Note 1)	IC	-4.0	А	
	Pulse (Note 1)	I _{CP}	-7.0		
Base current		Ι _Β	-0.5	Α	
Collector power dissipation (t = 10s)	t = 10s	Pc (Note 2)	3.3	W	
	DC	FC (NOIE 2)	1.3		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

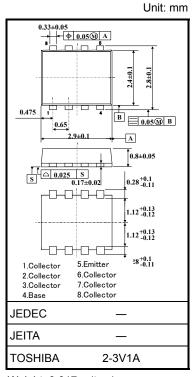
Note 1: Ensure that the junction temperature does not exceed 150°C during use of this device.

Note 2: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Note 3: ● on the lower left of the marking indicates Pin 1.

* Weekly code (three digits):





Weight: 0.017 g (typ.)

Figure 1.
Circuit Configuration
(Top View)

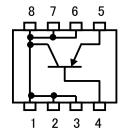
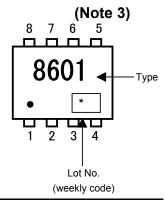


Figure 2. Marking

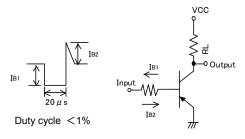


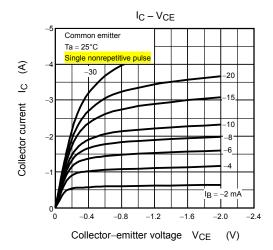
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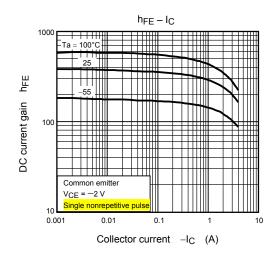
Electrical Characteristics (Ta = 25°C)

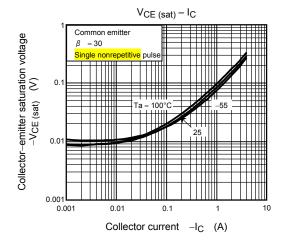
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -7 \text{ V}, I_{C} = 0$	_	_	-100	nA
Collector-base break	kdown voltage	V (BR) CBO	$I_C = -1 \text{ mA}, I_B = 0$	-20	_	_	V
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-20	_	_	V
DC current gain		h _{FE} (1)	$V_{CE} = -2 \text{ V}, I_{C} = -0.6 \text{ A}$	200	_	500	
		h _{FE} (2)	V _{CE} = -2 V, I _C = -2.0 A	100	_	_	
Collector-emitter sat	uration voltage	V _{CE} (sat)	$I_C = -2 \text{ A}, I_B = -67 \text{ mA}$	_	_	-0.19	V
Base-emitter saturat	ion voltage	V _{BE (sat)}	$I_C = -2 \text{ A}, I_B = -67 \text{ mA}$	_	_	-1.1	V
Switching time	Rise time	t _r	See Figure 3 circuit diagram $V_{CC} \simeq 12 \text{ V}, \text{ R}_L = 6 \Omega$ $I_{B1} = -I_{B2} = -67 \text{ mA}$	_	72	_	
	Storage time	t _{stg}		_	170	_	ns
	Fall time	t _f		_	35	_	

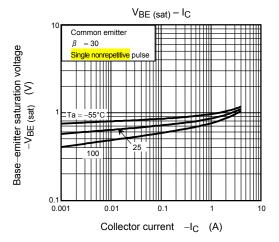
Figure 3. Switching Time Test Circuit & Timing Chart

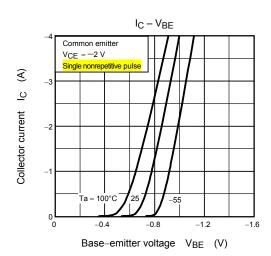




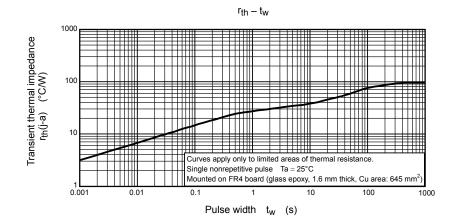


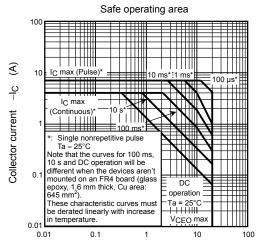






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Collector-emitter voltage -V_{CE} (V)

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