

Power Transistor (−60V, −3A)

2SB1184 / 2SB1243

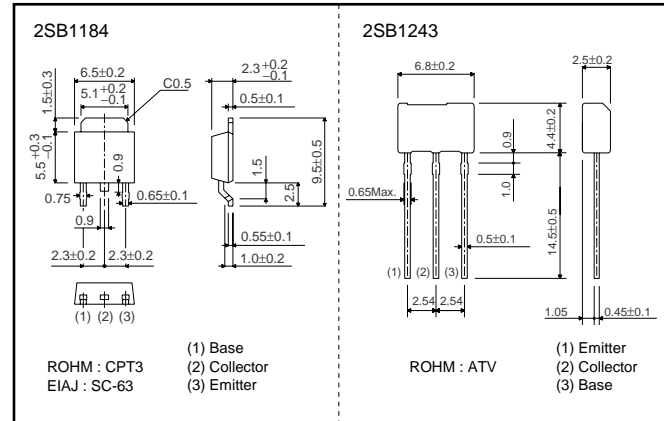
●Features

- Low $V_{CE(sat)}$.
 $V_{CE(sat)} = -0.5V$ (Typ.)
 $(I_c/I_B = -2A / -0.2A)$
- Complements the 2SD1760 / 2SD1864.

●Structure

Epitaxial planar type
 PNP silicon transistor

●External dimensions (Unit : mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	V_{CBO}	−60	V	
Collector-emitter voltage	V_{CEO}	−50	V	
Emitter-base voltage	V_{EBO}	−5	V	
Collector current	I_c	−3	A (DC)	
Collector power dissipation	2SB1184	P_c	1	W
			15	W (Tc=25°C)
			1	W *1
2SB1243				
Junction temperature	T_j	150	°C	
Storage temperature	T_{stg}	−55 to 150	°C	

*1 Printed circuit board, 1.7mm thick, collector copper plating 100mm² or larger.

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●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{CB0}	-60	-	-	V	I _c = -50μA
Collector-emitter breakdown voltage	BV _{CE0}	-50	-	-	V	I _c = -1mA
Emitter-base breakdown voltage	BV _{EB0}	-5	-	-	V	I _E = -50μA
Collector cutoff current	I _{cBO}	-	-	-1	μA	V _{CB} = -40V
Emitter cutoff current	I _{EBO}	-	-	-1	μA	V _{EB} = -4V
Collector-emitter saturation voltage	V _{CE(sat)}	-	-	-1	V	I _c /I _B = -2A/ -0.2A
DC current transfer ratio	h _{FE}	82	-	390	-	V _{CE} = -3V, I _c = -0.5A
Transition frequency	f _T	-	70	-	MHz	V _{CE} = -5V, I _E =0.5A, f=30MHz
Output capacitance	C _{ob}	-	50	-	pF	V _{CB} = -10V, I _E =0A, f=1MHz

* Measured using pulse current.

●Packaging specifications and h_{FE}

Type	h _{FE}	Package	Taping	
		Code	TL	TV2
		Basic ordering unit (pieces)	2500	2500
2SB1184	PQR		○	-
2SB1243	PQR		-	○

h_{FE} values are classified as follows :

Item	P	Q	R
h _{FE}	82 to 180	120 to 270	180 to 390

●Electrical characteristic curves

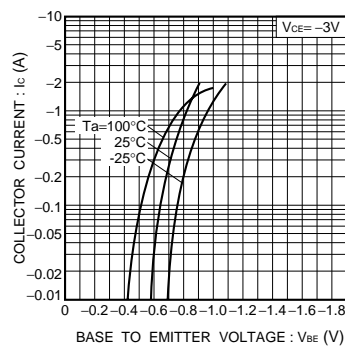


Fig.1 Grounded emitter propagation characteristics

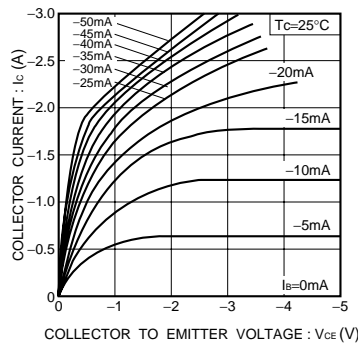


Fig.2 Grounded emitter output characteristics (I)

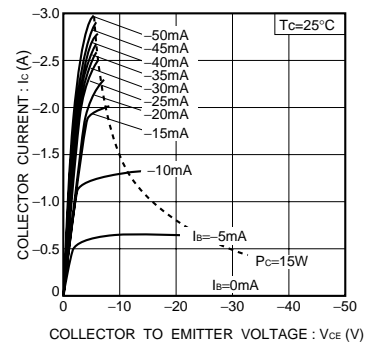


Fig.3 Grounded emitter output characteristics (II)

Transistors

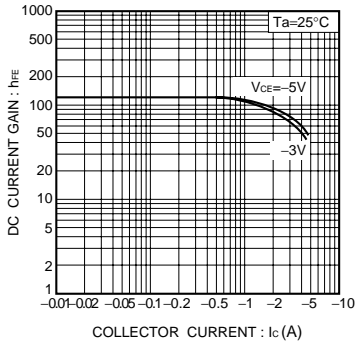


Fig.4 DC current gain vs. collector current (I)

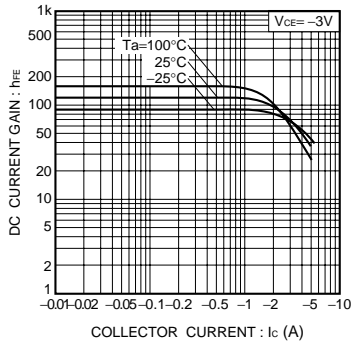


Fig.5 DC current gain vs. collector current (II)

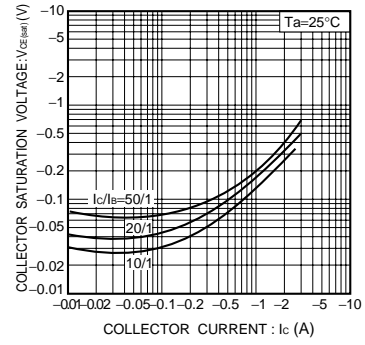


Fig.6 Collector-emitter saturation voltage vs. collector current

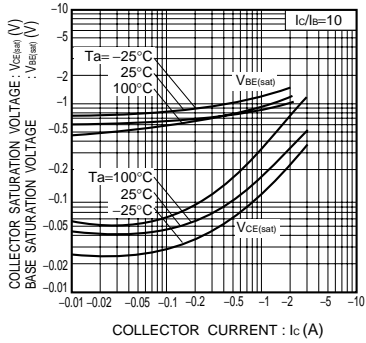


Fig.7 Collector-emitter saturation voltage vs. collector current
Base-emitter saturation voltage vs. collector current

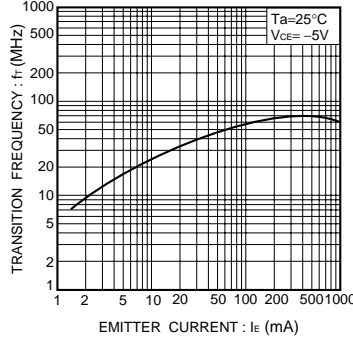


Fig.8 Gain bandwidth product vs. emitter current

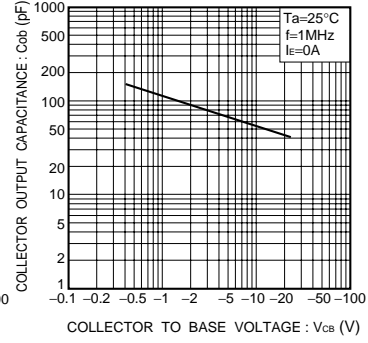


Fig.9 Collector output capacitance vs. collector base voltage

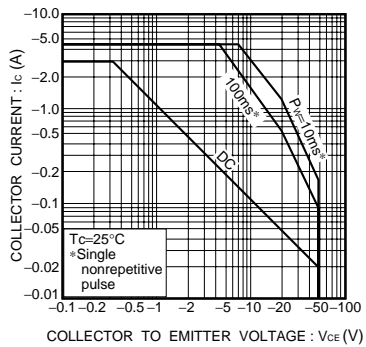


Fig.10 Safe operation area (2SB1184)

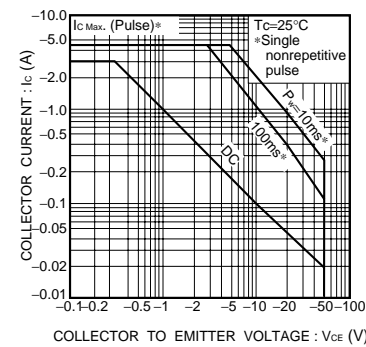


Fig.11 Safe operation area (2SB1243)

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