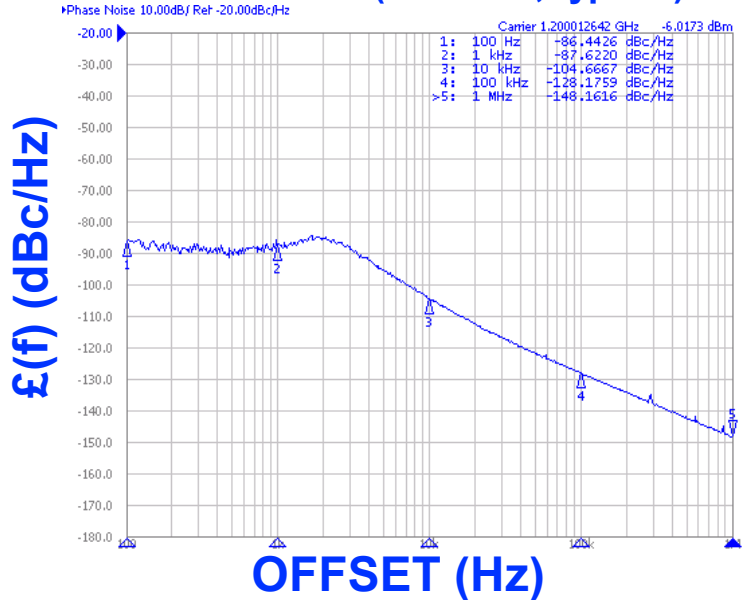

PHASE NOISE (1 Hz BW, typical)


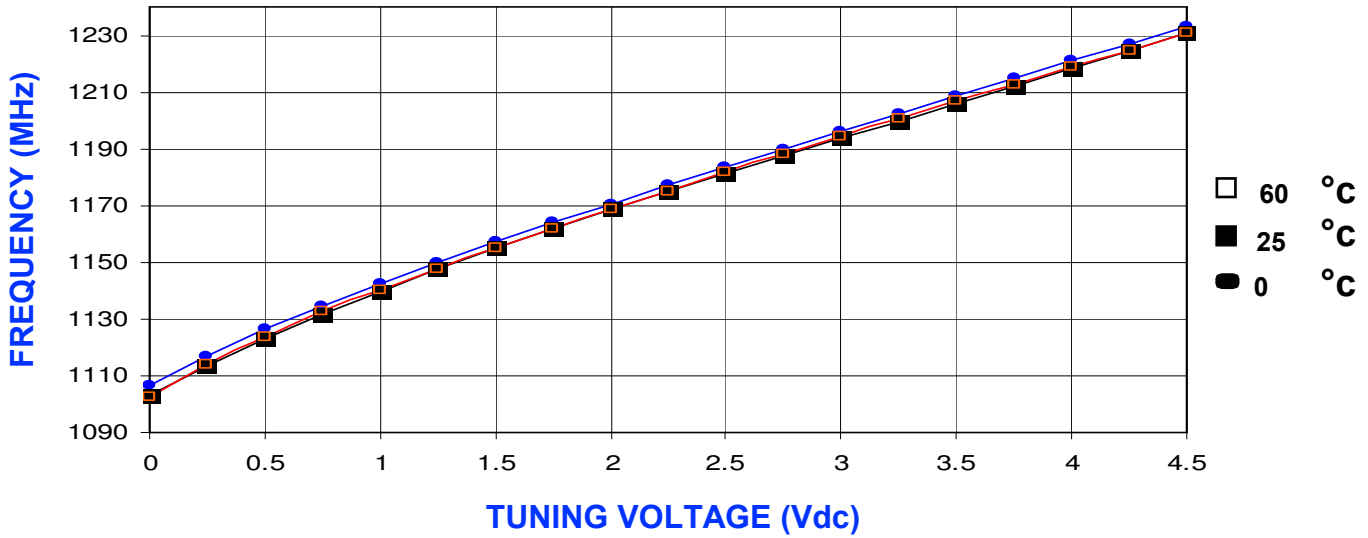
FEATURES
<ul style="list-style-type: none"> • Frequency Range: 1200 - 1200 MHz • Tuning Voltage: Vdc • PLL-24 - Style Package
APPLICATIONS
<ul style="list-style-type: none"> • Basestations • Satellite Communications •

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Oscillation Frequency Range	1200 - 1200	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-103	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Tuning Voltage		Vdc
Tuning Sensitivity (avg.)	5000	MHz/V
Power Output	0±3	dBm
Load Impedance	50	Ω
Input Capacitance (max.)	1000	pF
Pushing	2	MHz/V
Pulling (14dB Return Loss, Any Phase)	2	MHz
Operating Temperature Range	0 to 60	°C
Package Style	PLL-24	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	33	mA

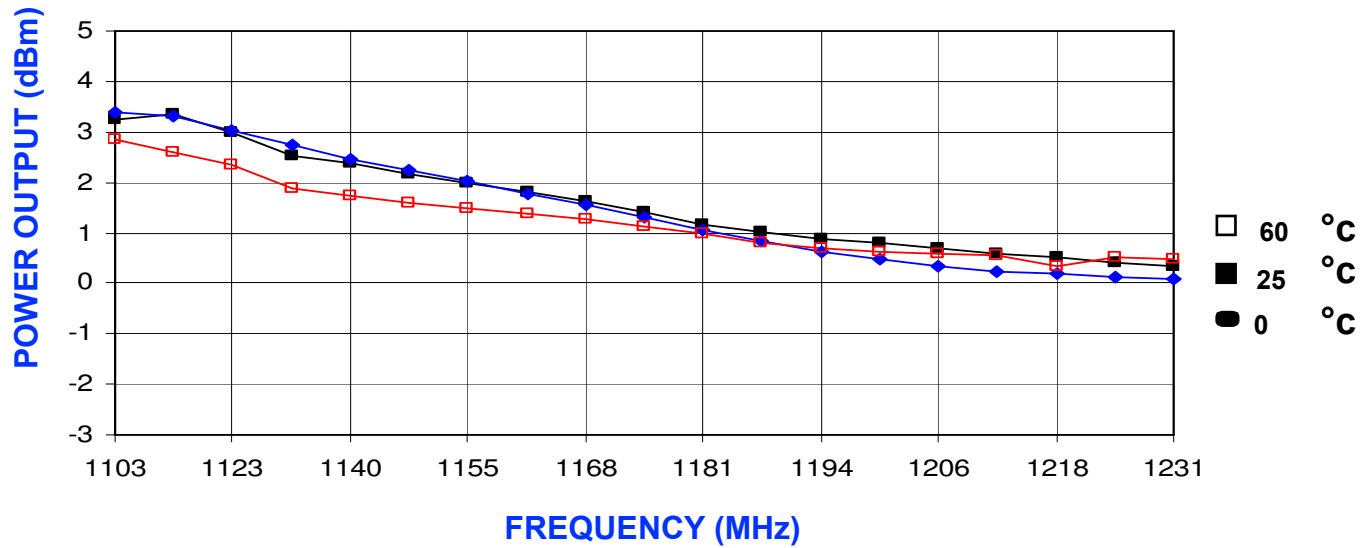
All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES
<ul style="list-style-type: none"> • AN-100/1 : Mounting and Grounding of VCOs • AN-102 : Proper Output Loading of VCOs • AN-107 : How to Solder Z-COMM VCOs
NOTES:
Reference Oscillator Signal: $5\text{ MHz} < f_{\text{osc}} < 100\text{ MHz}$
Frequency Synthesizer: Analog Devices - ADF4106

TUNING CURVE, typ.



POWER CURVE, typ.



PHYSICAL DIMENSIONS

