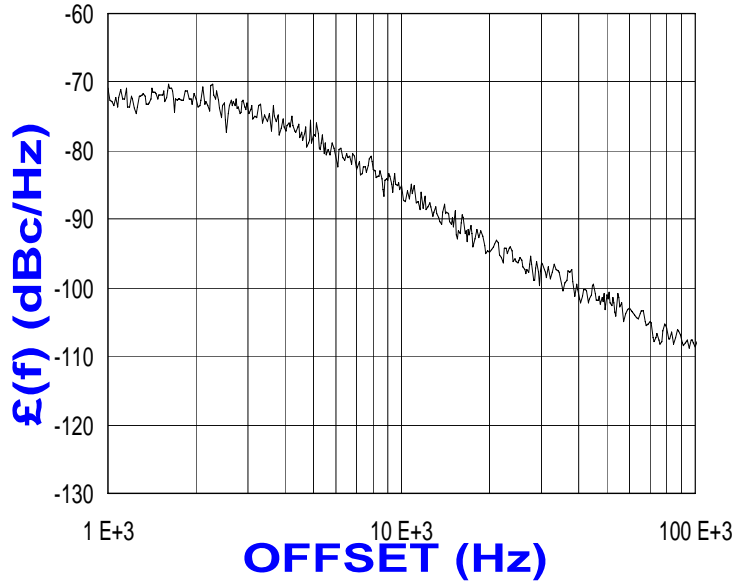




**PHASE NOISE (1 Hz BW, typical)**



**FEATURES**

- Frequency Range: 2344 - 2544 MHz
- Step Size: 500 KHz
- PLL-24 - Style Package

**APPLICATIONS**

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	2344 - 2544	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-86	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	2±3	dBm
Load Impedance	50	$\Omega$
Step Size	500	KHz
Charge Pump Output Current	2500	$\mu$ A
Switching Speed (typ., adjacent channel)	1.5	mSec
Startup Lock Time (typ.)	2.5	mSec
Operating Temperature Range	-15 to 70	$^{\circ}$ C
Package Style	PLL-24	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	27	mA

All specifications are typical unless otherwise noted and subject to change without notice.

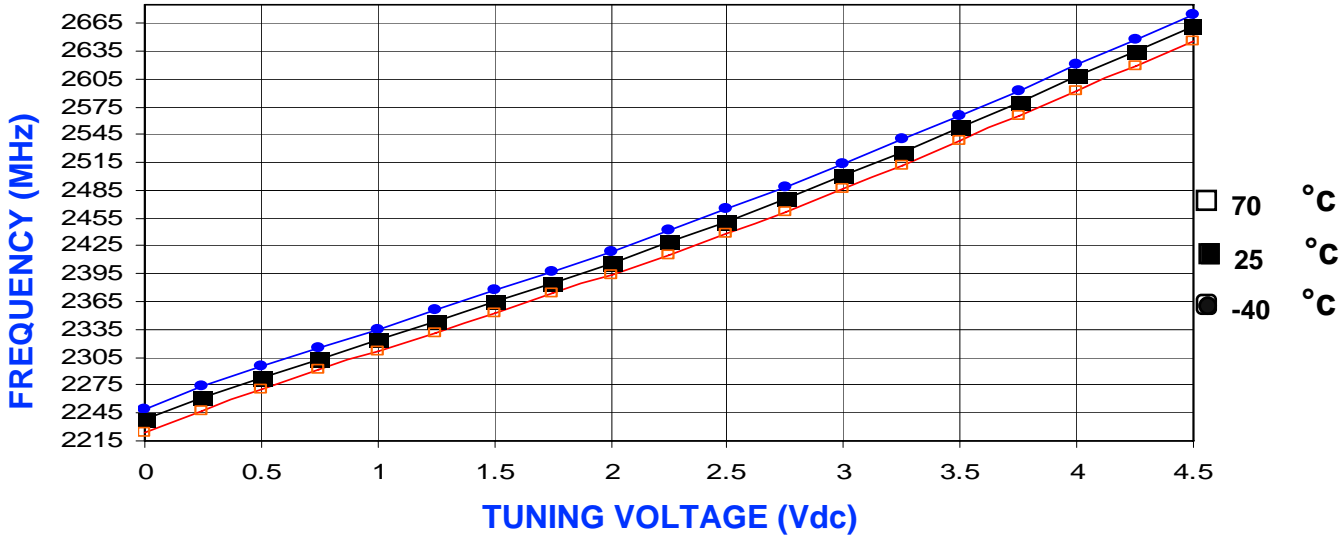
**APPLICATION NOTES**

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals      AN-202 : PLL Functional Description

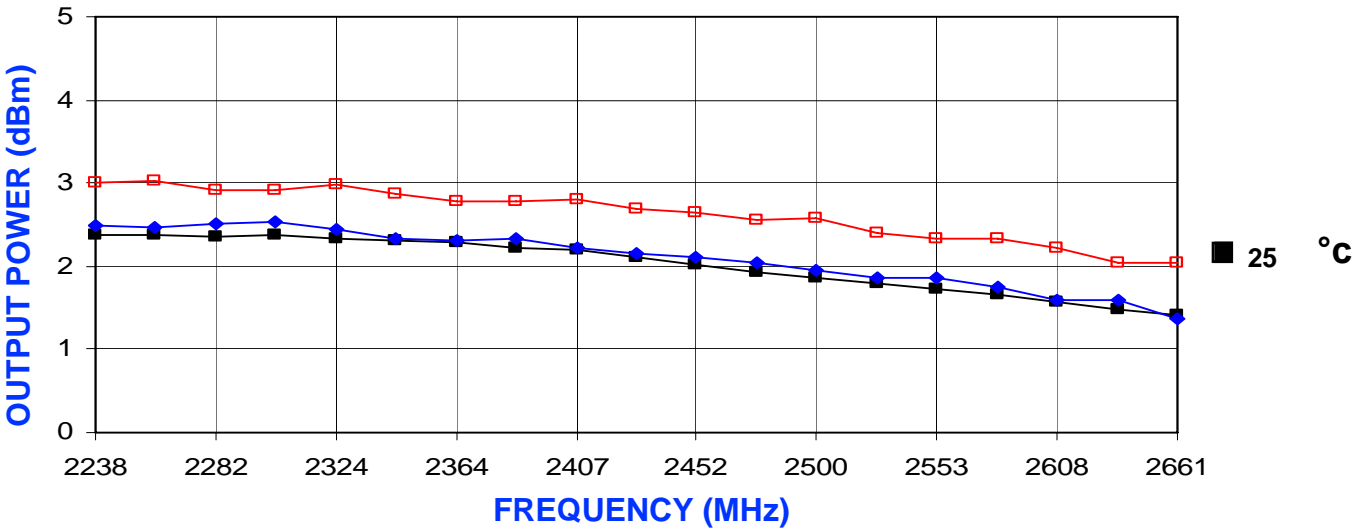
**NOTES:**

Reference Oscillator Signal: 5 MHz <math>f\_{osc}</math> <math>< 100</math> MHz      Prescaler: 32  
 Frequency Synthesizer: Analog Devices - ADF4106

**VCO TUNING CURVE, typ.**



**VCO POWER CURVE, typ.**



**PHYSICAL DIMENSIONS**

