## 1A, 1.5MHz Synchronous Step-Down Converter

## General Description

The AME5250 is a high efficiency monolithic synchronous buck regulator using a constant frequency, current mode architecture. Capable of delivering 1A output current over a wide input voltage range from 2.5 V to 5.5 V , the AME5250 is ideally suited for single Li-lon battery powered applications. $100 \%$ duty cycle provides low dropout operation, extending battery life in portable systems. Under light load conditions, the AME5250 operates in a power saving mode that consumes just around $20 \mu \mathrm{~A}$ of supply current, maximizing battery life in portable applications.

The internal synchronous switch increases efficiency and eliminates the need for an external Schottky diode. Low output voltages are easily supported with the 0.6 V feedback reference voltage. The AME5250 is available in small DFN-6D package.

Other features include soft start, lower internal reference voltage with $2 \%$ accuracy, over temperature protection, and over current protection.

## ■ Features

- High Efficiency: Up to 95\%
- Very Low $20 \mu \mathrm{~A}$ Quiescent Current
- High efficiency in light load condition
- 2.5 V to 5.5 V Input Range
- Adjustable Output From 0.6 V to $\mathrm{V}_{\mathrm{IN}}$
- $1.0 \mathrm{~V}, 1.2 \mathrm{~V}, 1.5 \mathrm{~V}, 1.6 \mathrm{~V}, 1.8 \mathrm{~V}, 2.5 \mathrm{~V}$ and 3.3V Fixed/Adjustable Output Voltage
- 1A Output Current
- Low Dropout Operation: 100\% Duty Cycle
- No Schottky Diode Required
- 1.5 MHz Constant Frequency PWM Operation
- Small DFN-6D Package
- All AME' s Lead Free Product Meet RoHS Standard


## Applications

- Cellular Telephones
- Personal Information Appliances
- Wireless and DSL Modems
- MP3 Players
- Portable Instruments


## Typical Application



Figure 1: High Efficiency Step-Down Conventer

Adjustable Output Voltage


Figure 2: 1.8 V at 1000 mA Step-Down Requlator

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## Function Block Diagram



Figure 3: Founction Block Diagram

Pin Configuration
DFN-6D
( 2 mmx 2 mmx 0.75 mm )

Top View


AME5250-AVYxxx

1. NC
2. EN
3. IN
4. SW
5. GND
6. FB/OUT

Die Attach:
Conductive Epoxy

Note:
The area enclosed by dashed line represents Exposed Pad and connect to GND.

## ■ Pin Description

| Pin Number | Pin Name | Pin Description |
| :---: | :---: | :--- |
| 1 | NC | No connection. Not internally connected. Can left floating or connected <br> to GND. |
| 2 | EN | Enable Control Input, active high. |
| 3 | IN | Input Supply Voltage Pin. <br> Bypass this pin with a capacitor as close to the device as possible. |
| 4 | SW | Switch Node Connection to Inductor. |
| 5 | GND | Ground. Tie directly to ground plane. |
| 6 | FB/OUT | Output voltage Feedback input. |

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## Ordering Information



| Pin Configuration \& Special Feature | Package Type | Number of Pins | Output Voltage |
| :---: | :---: | :---: | :---: |
| A 1. NC <br> (DFN-6D) 2. EN <br>  3. IN <br>  4. SW <br>  5. GND <br>  6. FB/OUT | V: DFN | Y: 6 | $\begin{aligned} & 100: 1.0 \mathrm{~V} \\ & 120: 1.2 \mathrm{~V} \\ & \text { 150: } 1.5 \mathrm{~V} \\ & \text { 160: } 1.6 \mathrm{~V} \\ & \text { 180: } 1.8 \mathrm{~V} \\ & \text { 250: } 2.5 \mathrm{~V} \\ & 330: 3.3 \mathrm{~V} \end{aligned}$ <br> ADJ: Adjustable |

Available Opetions

| Part Number | Marking* | Output Voltage | Package | Operating Ambient <br> Temperature Range |
| :---: | :---: | :---: | :---: | :---: |
| AME5250-AVYADJ | 5250 <br> AMXX | ADJ | DFN-6D | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| AME5250-AVY120 | 5250 <br> BMXX | 1.2 V | DFN-6D | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| AME5250-AVY180 | 5250 <br> CMXX | 1.8 V | DFN-6D | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |

Note:

1. The first places represent product code. It is assigned by AME such as A.
2. A bar on top of first letter represents Green Part such as 5250 .
3. The last 3 places MXX represent Marking Code. It contains $M$ as date code in "month", XX as LN code and that is for AME internal use only. Please refer to date code rule section for detail information.
4. Please consult AME sales office or authorized Rep./Distributor for the availability of output voltage and package type.

## ■ Absolute Maximum Ratings

| Parameter | Symbol | Maximum | Unit |
| :--- | :---: | :---: | :---: |
| Input Supply Voltage | $\mathrm{V}_{\text {IN }}$ | -0.3 to 6.5 | V |
| EN, $\mathrm{V}_{\text {OUT }}$ Voltage | $\mathrm{V}_{\text {EN }}, \mathrm{V}_{\text {OUT }}$ | -0.3 to $\mathrm{V}_{\mathrm{IN}}$ |  |
| SW Voltage | $\mathrm{V}_{\text {SW }}$ | -0.3 to $\mathrm{V}_{\mathrm{IN}}$ |  |
| ESD Classification | $\mathrm{B}^{*}$ |  |  |

Caution: Stress above the listed absolute maximum rating may cause permanent damage to the device.

* HBM B: 2000V~3999V


## ■ Recommended Operating Conditions

| Parameter | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Supply Voltage Voltage | $\mathrm{V}_{\mathrm{IN}}$ | 2.5 to 5.5 | V |
| Ambient Temperature Range | $\mathrm{T}_{\mathrm{A}}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Junction Temperature Range | $\mathrm{T}_{\mathrm{J}}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

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## - Thermal Information

| Parameter | Package | Die Attach | Symbol | Maximum | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Thermal Resistance* (Junction to Case) | DFN-6D | Conductive Epoxy | $\theta_{\mathrm{Jc}}$ | 85 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance (Junction to Ambient) |  |  | $\theta_{\text {JA }}$ | 160 |  |
| Internal Power Dissipation |  |  | $\mathrm{P}_{\mathrm{D}}$ | 625 | mW |
| Solder Iron (10Sec)** |  |  |  | 350 | ${ }^{\circ} \mathrm{C}$ |

[^0]
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Electrical Specifications
$\mathrm{V}_{I N}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{OUT}}=2.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{FB}}=0.6 \mathrm{~V}, \mathrm{~L}=2.2 \mu \mathrm{H}, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{OUT}}=10 \mu \mathrm{~F}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{MAX}}=1 \mathrm{~A}$ unless otherwise specified.

| Parameter | Symbol | Test Condition | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage | $V_{\text {IN }}$ |  | 2.5 |  | 5.5 | V |
| Output Voltage Accuracy | $\Delta \mathrm{V}_{\text {OUT }}$ | $\mathrm{V}_{\text {IN }}=2.5$ to 5.5 V , in PWM mode For Fixed Output Voltage | -3 |  | 3 | \% |
| Adjustable Output Range | $V_{\text {out }}$ |  | $\mathrm{V}_{\mathrm{FB}}$ |  | $\mathrm{V}_{\mathbb{N}-0.2}$ | V |
| Feedback Voltage | $V_{\text {FB }}$ | For Adjustable OutputVoltage | 0.588 | 0.6 | 0.612 | V |
| Feedback Pin Bias Current | $\mathrm{IfB}^{\text {f }}$ | $\mathrm{V}_{\mathrm{FB}}=\mathrm{V}_{\text {IN }}$ | -50 |  | 50 | nA |
| Quiescent Current | $\mathrm{l}_{\mathrm{Q}}$ | $\mathrm{l}_{\text {OUT }}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{FB}}=1 \mathrm{~V}$ |  | 20 | 35 | $\mu \mathrm{A}$ |
| Shutdown Current | ISHDN | $\mathrm{V}_{\mathrm{EN}}=\mathrm{GND}$ |  | 0.1 | 1 | $\mu \mathrm{A}$ |
| Switch Frequency | fosc |  | 1.2 | 1.5 | 1.8 | MHz |
| High-side Switch On-Resistance | R ${ }_{\text {DS, ON, LHI }}$ | $\mathrm{l}_{\mathrm{SW}}=200 \mathrm{~mA}, \mathrm{~V}_{\text {IN }}=3.6 \mathrm{~V}$ |  | 0.28 |  | $\Omega$ |
| Low-side Switch On-Resistance | $\mathrm{R}_{\mathrm{DS}, \mathrm{ON}, \mathrm{LO}}$ | $\mathrm{I}_{\mathrm{SW}}=200 \mathrm{~mA}, \mathrm{~V}_{\text {IN }}=3.6 \mathrm{~V}$ |  | 0.25 |  | $\Omega$ |
| Switch Current Limit | $\mathrm{I}_{\text {SW,CL }}$ | $\mathrm{V}_{\text {IN }}=2.5$ to 5.5 V | 1.4 | 1.6 |  | A |
| EN High (Enabled the Device) | $\mathrm{V}_{\text {EN,HI }}$ | $\mathrm{V}_{\text {IN }}=2.5$ to 5.5 V | 1.5 |  |  | V |
| EN Low (Shutdown the Device) | $\mathrm{V}_{\text {EN,LO }}$ | $\mathrm{V}_{\text {IN }}=2.5$ to 5.5 V |  |  | 0.4 | V |
| Input Undervoltage Lockout | V UVLO | rising edge |  | 1.8 |  | V |
| Input Undervoltage Lockout Hysteresis | Vuvlo,hyst |  |  | 0.1 |  | V |
| Thermal Shutdown Temperature | OTP | Shutdown, temperature increasing |  | 160 |  | ${ }^{\circ} \mathrm{C}$ |
| Maximum Duty Cycle | $\mathrm{D}_{\text {MAX }}$ |  | 100 |  |  | \% |
| SW Leakage Current |  | $\begin{gathered} \mathrm{EN}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=5.0 \mathrm{~V} \\ \mathrm{~V}_{\mathrm{SW}}=0 \mathrm{~V} \text { or } 5.0 \mathrm{~V} \end{gathered}$ | -1 |  | 1 | $\mu \mathrm{A}$ |

## 1A, 1.5MHz Synchronous Step-Down Converter

## ■ Detailed Description

## Main Control Loop

AME5250 uses a constant frequency, current mode step-down architecture. Both the main (P-channel MOSFET) and synchronous (N-channel MOSFET) switches are intermal. During normal operation, the internal top power MOSFET is turned on each cycle when the oscillator sets the RS latch, and turned off when the current comparator resets the RS latch. While the top MOSFET is off, the bottom MOSFET is turned on until either the inductor current starts to reverse as indicated by the current reversal comparator IRCMP.

## Short-Circuit Protection

When the output is shorted to ground, the frequency of the oscillator is reduced to about 180 KHz . This frequency foldback ensures that the inductor current hsa more time do decay, thereby preventing runaway. The oscillator' s frequency will progressively increase to 1.5 MHz when $\mathrm{V}_{\text {в }}$ or $\mathrm{V}_{\text {OUT }}$ rises above 0 V .

## Dropout Operation

As the input supply voltage decreases to a value approaching the output voltage, the duty cycle increases toward the maximum on-time. Further reduction of the supply voltage forces the main switch to remain on for more than one cycle until it reaches $100 \%$ duty cycle. The output voltage will then be determined by the input voltage minus the voltage drop across the P -channel MOSFET and the inductor.

## Application Information

The basic AME5250 application circuit is shown in Typical Application Circuit. External component selection is determined by the maximum load current and begins with the selection of the inductor value and followed by $\mathrm{C}_{\mathrm{IN}}$ and $\mathrm{C}_{\text {out }}$

## Inductor Selecton

For a given input and output voltage, the inductor value and operating frequency determine the ripple current. The ripple current DIL increases with higher $\mathrm{V}_{\mathbb{N}}$ and decreases with higher inductance.

$$
\Delta I_{L}=\frac{1}{f \cdot L} \cdot V_{\text {OUT }}\left(1-\frac{V_{\text {OUT }}}{V_{I N}}\right)
$$

A reasonable starting point for setting ripple current is DIL=0.4(Imax). The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation. For better efficiency, choose a low DC-resistance inductor.

## $\mathrm{C}_{\mathrm{IN}}$ and $\mathrm{C}_{\mathrm{OUT}}$ Selection

The input capacitance, $\mathrm{G}_{\mathrm{N}}$ is needed to filter the trapezoidal current at the source of the top MOSFET. To prevent large voltage transients, a low ESR input capacitorsized for the maximum RMS current must be used. The maximum RMS capacitor current is given by:

$$
I_{\text {RMS }}=I_{\text {OUT }}(\max ) \cdot \frac{V_{\text {OUT }}}{V_{I N}} \cdot \sqrt{\frac{V_{I N}}{V_{\text {OUT }}}-1}
$$

This formula has a maximum at $\mathrm{V}_{\mathbb{I N}}=2 \mathrm{~V}_{\text {out }}$, where IRMS=I $\mathrm{I}_{\mathrm{OUT}} / 2$. This simple worst-case condition is commonly used for design because even significant deviations do not offer much relief. Note that the capacitor manufacturer ripple current ratings are often based on 2000 hours of life. This makes it advisable to further derate the capacitor, or choose a capacitor rated at a higher temperature than required.

The selection of $C_{\text {But }}$ is determined by the effective series resistance(ESR) that is required to minimize voltage ripple and load step transients. The output ripple, $\mathrm{V}_{\text {out }}$, is determined by:

$$
\Delta V_{\text {OUT }} \leq \Delta I_{L}\left[E S R+\frac{1}{8 f \cdot C_{\text {OUT }}}\right]
$$

## Using Ceramic Input and Output Capacitors

Higher values, lower cost ceramic capacitors are now becoming available in smaller case sizes. Their high ripple current, high voltage rating and low ESR make them ideal for switching regulator applications. However, care must be taken when these capacitors are used at the input and output. When a ceramic capacitor is used at the input and the power is supplied by a wall adapter through long wires, a load step at the output can induce ringing at the input, $\mathrm{V}_{\mathbb{I N}}$. At best, this ringing can couple to the output and be mistaken as loop instability. At worst, a sudden inrush of current through the long wires can potentially cause a voltage spike at $\mathrm{V}_{\text {IN }}$ large enough to damage the part.

## Output Voltage Programming

The output voltage is set by an external resistive divider according to the following equation :

$$
V_{\text {OUT }}=V_{\text {REF }} \cdot\left(1+\frac{R_{2}}{R_{1}}\right)
$$

Where VREF equals to 0.6 V typical. The resistive divider allows the FB pin to sense a fraction of the output voltage as shown in Figure 4.


Figure 4: Setting the AME5250 Output Voltage

## Thermal Considerations

In most applications the AME5250 does not dissipate much heat due to its high efficiency. But, in applications where the AME5250 is running at high ambient temperature with low supply voltage and high duty cycles, such as in dropout, the heat dissipated may exceed the maximum junction temperature of the part. If the junction temperature reaches approximately $160^{\circ} \mathrm{C}$, both power switches will be turned off and the SW node will become high impedance. To avoid the AME5250 from exceeding the maximum junction temperature, the user will need to do some thermal analysis. The goal of the thermal analysis is to determine whether the power dissipated exceeds the maximum junction temperature of the part. The temperature rise is given by:

$$
\boldsymbol{T}_{R}=(P D)\left(\theta_{J A}\right)
$$

Where PD is the power dissipated by the regulator and $\theta_{J A}$ is the thermal resistance from the junction of the die to the ambient temperature.


Figure 5: 1.2V Step-Down Regulator


Figure 6: 1.5V Step-Down Regulator


Figure 7: 2.5V Step-Down Regulator


Figure 8: 1.6V Step-Down Regulator


Figure 9: 3.3V Step-Down Regulator

## PC Board Layout Checklist

When laying out the printed circuit board, the following checklist should be used to ensure proper operation of the AME5250. These items are also illustrated graphically in Figures 10 and Figures 11 . Check the following in your layout:

1. The power traces, consisting of the GND trace, the SW trace and the $\mathrm{V}_{\mathrm{IN}}$ trace should be kept short, direct and wide.
2. Does the $\mathrm{V}_{\mathrm{FB}}$ pin connect directly to the feedback resistors? The resistive divider R2/R1 must be connected between the (+) plate of $\mathrm{C}_{\text {out }}$ and ground.
3. Does the (+) plate of CIN connect to $\mathrm{V}_{\text {IN }}$ as closely as possible? This capacitor provides the AC current to the internal power MOSFETs.
4. Keep the switching node, SW, away from the sensitive $V_{F B}$ node.
5. Keep the $(-)$ plates of $\mathrm{C}_{\mathbb{I N}}$ and $\mathrm{C}_{\text {out }}$ as close as possible.


Figure 10: AME5250 Adjustable Voltage Regulator Layout Diagram


Figure 11: AME5250 Fixed Voltage Regulator Layout Diagram 1A, 1.5 MHz Synchronous
Step-Down Converter

Application Information
External components selection

| Supplier | Inductance <br> $(\mu \mathrm{H})$ | Current Rating <br> $(\mathrm{mA})$ | DCR <br> $(\mathrm{m} \Omega)$ | Dimensions <br> $(\mathrm{mm})$ | Series |
| :---: | :---: | :---: | :---: | :---: | :---: |
| TAIYO YUDEN | 2.2 | 1480 | 60 | $3.00 \times 3.00 \times 1.50$ | NR 3015 |
| GOTREND | 2.2 | 1500 | 58 | $3.85 \times 3.85 \times 1.80$ | GTSD32 |
| Sumida | 2.2 | 1500 | 75 | $4.50 \times 3.20 \times 1.55$ | CDRH2D14 |
| Sumida | 4.7 | 1000 | 135 | $4.50 \times 3.20 \times 1.55$ | CDRH2D14 |
| TAIYO YUDEN | 4.7 | 1020 | 120 | $3.00 \times 3.00 \times 1.50$ | NR 3015 |
| GOTREND | 4.7 | 1100 | 146 | $3.85 \times 3.85 \times 1.80$ | GTSD32 |

Table 1. Recommended Inductors

| Supplier | Capacitance <br> $(\mu \mathrm{H})$ | Package | Part Number |
| :---: | :---: | :---: | :---: |
| TDK | 4.7 | 603 | C1608JB0J475M |
| MURATA | 4.7 | 603 | GRM188R60J475KE19 |
| TAIYO YUDEN | 4.7 | 603 | JMK107BJ475RA |
| TAIYO YUDEN | 10 | 603 | JMK107BJ106MA |
| TDK | 10 | 805 | C2012JB0J106M |
| MURATA | 10 | 805 | GRM219R60J106ME19 |
| MURATA | 10 | 805 | GRM219R60J106KE19 |
| TAIYO YUDEN | 10 | 805 | JMK212BJ106RD |

Table 2. Recommended Capacitors for $\mathrm{C}_{\mathbb{N}}$ and $\mathrm{C}_{\text {out }}$

## 1A, 1.5MHz Synchronous Step-Down Converter

## Characterization Curve(For reference only)



Efficiency vs. Output Current


Reference Voltage vs. Temperature


Efficiency vs. Output Current


Efficiency vs. Output Current


Frequency vs. Temperature


## 1A, 1.5MHz Synchronous Step-Down Converter

Characterization Curve(For reference only)


## 1A, 1.5MHz Synchronous Step-Down Converter

## Characterization Curve(For reference only)


$\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$
$V_{\text {OUT }}=1.8 \mathrm{~V}$
$\mathrm{I}_{\text {OUt }}=0 \mathrm{~A} \sim 1 \mathrm{~A} \sim 0 \mathrm{~A}$

1) $V_{\text {out }}=100 \mathrm{mV} / \mathrm{div}$
2) $\mathrm{I}_{\text {out }}=500 \mathrm{~mA} / \mathrm{div}$

Load Step

$\mathrm{V}_{\mathrm{IN}}=3.6 \mathrm{~V}$
$V_{\text {OUT }}=1.8 \mathrm{~V}$
$\mathrm{I}_{\text {Out }}=100 \mathrm{~mA} \sim 1 \mathrm{~A} \sim 100 \mathrm{~mA}$

1) $V_{\text {out }}=100 \mathrm{mV} /$ div
2) $I_{\text {OUT }}=500 \mathrm{~mA} / \mathrm{div}$

$\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$
$\mathrm{V}_{\text {OUT }}=1.8 \mathrm{~V}$
$\mathrm{I}_{\text {out }}=50 \mathrm{~mA} \sim 1 \mathrm{~A} \sim 50 \mathrm{~mA}$
3) $V_{\text {OUT }}=100 \mathrm{mV} / \mathrm{div}$
4) $\mathrm{I}_{\text {out }}=500 \mathrm{~mA} / \mathrm{div}$

Load Step

$\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$
$\mathrm{V}_{\text {OUT }}=1.8 \mathrm{~V}$
$\mathrm{I}_{\text {OUT }}=200 \mathrm{~mA} \sim 1 \mathrm{~A} \sim 200 \mathrm{~mA}$

1) $V_{\text {out }}=100 \mathrm{mV} /$ div
2) $\mathrm{I}_{\text {OUT }}=500 \mathrm{~mA} / \mathrm{div}$

## 1A, 1.5MHz Synchronous Step-Down Converter

## Characterization Curve(For reference only)



## 1A, 1.5MHz Synchronous Step-Down Converter

## Date Code Rule

| Month Code |  |
| :--- | :--- |
| 1: January | 7: July |
| 2: February | 8: August |
| 3: March | 9: September |
| 4: April | A: October |
| 5: May | B: November |
| 6: June | C: December |


| Marking |  |  |  |  |  | Year |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | A | A | M | X | X | xxx0 |
| A | A | A | M | X | $\underline{X}$ | xxx1 |
| A | A | A | M | $\underline{X}$ | X | xxx2 |
| A | A | A | M | $\underline{X}$ | $\underline{x}$ | xxx3 |
| A | A | A | M | X | X | xxx4 |
| A | A | A | M | X | $\underline{X}$ | xxx5 |
| A | A | A | M | $\underline{X}$ | X | xxx6 |
| A | A | A | M | $\underline{x}$ | $\underline{X}$ | xxx7 |
| A | A | A | M | X | X | xxx8 |
| A | A | A | M | X | $\underline{X}$ | xxx9 |

## ■ Tape and Reel Dimension

DFN-6D
(2mmx2mmx0.75mm)


Carrier Tape, Number of Components Per Reel and Reel Size

| Package | Carrier Width (W) | Pitch (P) | Part Per Full Reel | Reel Size |
| :---: | :---: | :---: | :---: | :---: |
| DFN-6D <br> $(2 \times 2 \times 0.75 \mathrm{~mm})$ | $8.0 \pm 0.1 \mathrm{~mm}$ | $4.0 \pm 0.1 \mathrm{~mm}$ | 3000 pcs | $180 \pm 1 \mathrm{~mm}$ |

■ Package Dimension

DFN-6D
(2mmx2mmx0.75mm)


REAR VIEW

| SYMBOLS | MILLIMETERS |  | INCHES |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |
| A | 0.700 | 0.800 | 0.028 | 0.031 |
| D | 1.900 | 2.100 | 0.075 | 0.083 |
| E | 1.900 | 2.100 | 0.075 | 0.083 |
| e | 0.600 | 0.700 | 0.024 | 0.028 |
| D1 | 1.100 | 1.300 | 0.043 | 0.051 |
| E1 | 0.600 | 0.800 | 0.024 | 0.031 |
| b | 0.180 | 0.300 | 0.007 | 0.012 |
| L | 0.250 | 0.450 | 0.010 | 0.018 |
| G | 0.178 | 0.228 | 0.007 | 0.009 |
| G1 | 0.000 | 0.050 | 0.000 | 0.002 |

## Life Support Policy:

These products of AME, Inc. are not authorized for use as critical components in life-support devices or systems, without the express written approval of the president
of AME, Inc.
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[^0]:    * Measure $\theta_{\mathrm{Jc}}$ on backside center of Exposed Pad.
    ** MIL-STD-202G 210F

