

## SANYO Semiconductors **DATA SHEET**

### **LA6220PM**—

# For Automotive Applications Rail-to-Rail Dual Operational Amplifier

#### Overview

The LA6220PM dual operational amplifier is optimal for both consumer and industrial applications, including all types of transducer amplifier and DC amplifier circuit. It supports from ground to  $V_{CC}$  (rail to rail) as the voltage range for both inputs and outputs and is a high-performance dual operational amplifier that features the wide operating temperature range of -40 to +85°C. It is optimal for the amplification of signals from all types of sensors.

#### **Functions**

- Does not require phase compensation
- Supports from ground to VCC (rail to rail) as the voltage range for both inputs and outputs
- Low current dissipation : I<sub>CC</sub> = 1.2mA typ/ $V_{CC}$  = +5V, R<sub>L</sub> =  $\infty$

#### **Specifications**

**Maximum Ratings** at Ta = 25°C

| Parameter                   | Symbol              | Conditions                              | Ratings     | Unit |
|-----------------------------|---------------------|---|-------------|------|
| Maximum supply voltage      | V <sub>CC</sub> max |   | 18          | V    |
| Differential input voltage  | V <sub>ID</sub>     |   | ±1          | V    |
| Maximum input voltage       | V <sub>IN</sub> max |   | -0.3 to +18 | V    |
| Allowable power dissipation | Pd max              | Ta ≤ 25°C Mounted on specified board. * | 0.8         | W    |
| Operating temperature       | Topr                |   | -40 to +85  | °C   |
| Storage temperature         | Tstg                |   | -55 to +150 | °C   |

<sup>\*</sup> Specified board size: 114.3×76.1×1.6mm³, glass epoxy.

#### **Recommended Operating Conditions** at Ta = 25°C

| Parameter      | Symbol          | Conditions | Ratings | Unit |
|----------------|-----------------|------------|---------|------|
| Supply voltage | V <sub>CC</sub> |            | 2 to 17 | ٧    |

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#### SANYO Semiconductor Co., Ltd.

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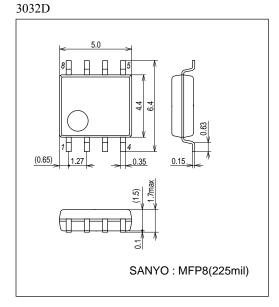
#### **LA6220PM**

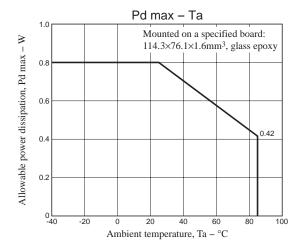
#### **Electrical Characteristics** at $Ta = 25^{\circ}C$ , $V_{CC} = 5V$ , Otherwise unless specified.

| Parameter                       | Symbol                | Conditions                                     | Test    | Ratings |      |      | 11.7 |
|---------------------------------|-----------------------|--|---------|---------|------|------|------|
|                                 |                       |  | circuit | min     | typ  | max  | Unit |
| Input offset voltage            | V <sub>IO</sub>       |  | 1       |         | ±2   | ±7   | mV   |
| Input offset current            | IIO                   | I <sub>IN</sub> (+)/I <sub>IN</sub> (-)        | 2       |         | ±5   | ±50  | nA   |
| Input bias current              | ΙΒ                    | I <sub>IN</sub> (+)/I <sub>IN</sub> (-)        | 3, 4    |         | 45   | 250  | nA   |
| Common-mode input voltage range | VICM                  |  | 5       | 0       |      | Vcc  | V    |
| Common-mode rejection ration    | CMR                   |  | 5       |         | 80   |      | dB   |
| Large amplitude voltage         | VG                    |  | 6       |         | 100  |      | V/mV |
| Output voltage range            | V <sub>OH</sub> 1A    | $R_L$ = 20kΩ : Ta = 25°C                       | 12      | 4.9     |      |      | V    |
|                                 | V <sub>OH</sub> 1B    | $R_L$ = 20kΩ : Ta = -40 to 85°C                | 12      | 4.85    |      |      | V    |
|                                 | V <sub>OL</sub> 1     | $R_L = 20k\Omega$                              | 12      |         |      | 0.1  | V    |
| Output voltage range            | V <sub>OH</sub> 2     | $R_L = 2k\Omega$                               | 12      | 4.75    |      |      | V    |
|                                 | V <sub>OL</sub> 2     | $R_L = 2k\Omega$                               | 12      |         |      | 0.25 | V    |
| Supply voltage rejection ratio  | SVR                   |  | 11      |         | 80   |      | dB   |
| Channel separation              | CS                    | f = 1kHz to 20kHz                              | 7       |         | 80   |      | dB   |
| Current drain                   | lcc                   |  | 8       |         | 1.2  | 2.5  | mA   |
| Output current (source)         | I <sub>O</sub> source | $V_{IN}$ + = 1V, $V_{IN}$ - = 0V               | 9       | 6       | 10   |      | mA   |
| Output current (sink)           | I <sub>O</sub> sink   | V <sub>IN</sub> + = 0V, V <sub>IN</sub> - = 1V | 10      | 3       | 5    |      | mA   |
| Slew rate                       | SR                    | $R_L = 2k\Omega$                               |         |         | 0.35 |      | V/μs |
| Gain-bandwidth product          | Ft                    | $R_L = 2k\Omega$                               |         |         | 1    |      | MHz  |
| Phase margin                    | ΦМ                    | $R_L = 2k\Omega$                               |         |         | 80   |      | Deg  |

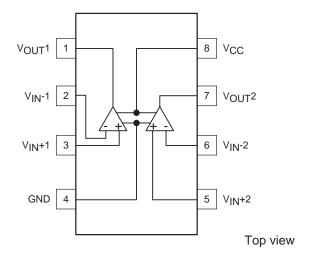
#### **Package Dimensions**

unit: mm (typ)

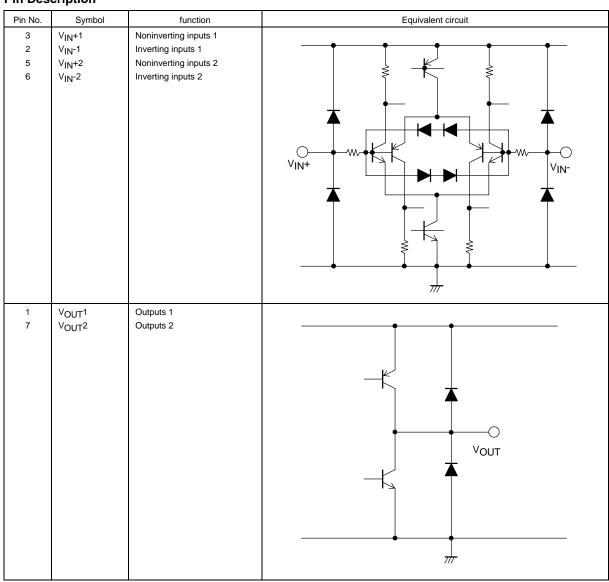




#### **Pin Assignment**

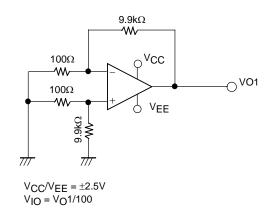


#### **Pin Description**

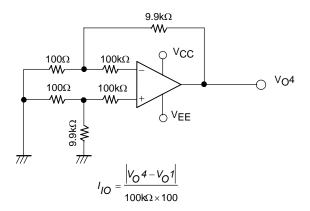


#### **Test Circuits**

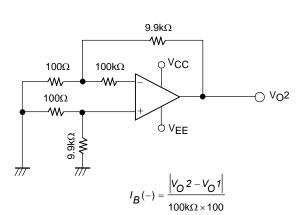
#### 1. Input offset voltage V<sub>IO</sub>



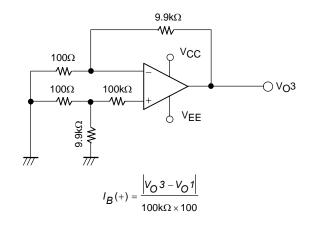
#### 2. Input offset current I<sub>IO</sub>



#### 3. Input bias current I<sub>B</sub> (-)

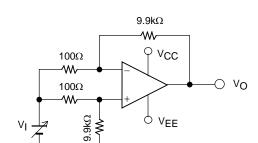


4. Input bias current I<sub>B</sub> (+)



5. Common-mode rejection ratio (CMR) Common-mode input voltage range (VICM)

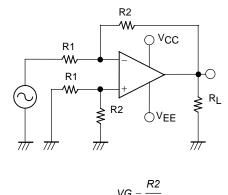
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CMR =  $20\log (5 \times 100 / |\Delta V_{O}|)$ 

CMR  $V_I = \pm 2.5V$ 

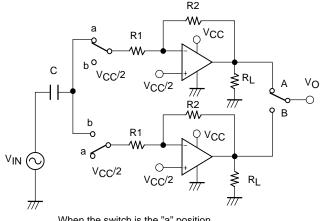
6. Voltage gain (VG)

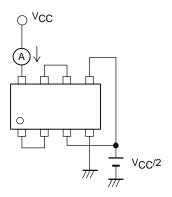


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#### 7. Channel separation (CS)

#### 8. Current drain (ICC)





When the switch is the "a" position. R2VoA

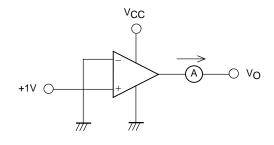
$$CS(A \to B) = 20log \frac{R2VOA}{R1VoB}$$

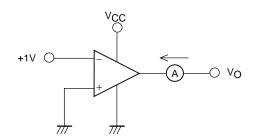
When the switch is the "b" position.

$$CS(B \to A) = 20log \frac{R2VOB}{R1VOA}$$

#### 9. Output current (IOsource)

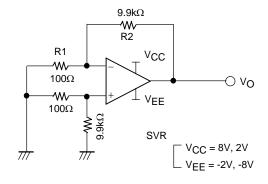
#### 10. Output current (Iosink)

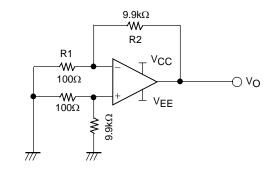




#### 11. Supply voltage rejection ratio SVR (+)

12. Supply voltage rejection ratio SVR (-)





$$SVR(+) = 20\log \left| \frac{\Delta V_{CC} \times 100}{\Delta V_{CC}} \right|$$

$$SVR(-) = 20\log \frac{\Delta V_{EE} \times 100}{\Delta V_{C}}$$

#### 13. Output voltage range (Isink)

#### 14. Output voltage range (Isource)

$$\begin{array}{c|c} & V_{CC} \\ & 2k\Omega \\ & 20k\Omega \\ \hline & & W \\ \hline & & V_{CC/2} \\ \hline & & & \\ \end{array}$$

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