TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA7358APG

FM Front-End

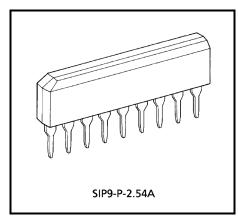
The TA7358APG is designed for a FM front-end application, which is suitable to a portable radio or a radio cassette.

Comparing with conventional types, supply voltage dependence, overload characteristics and spurious radiation characteristics are improved.

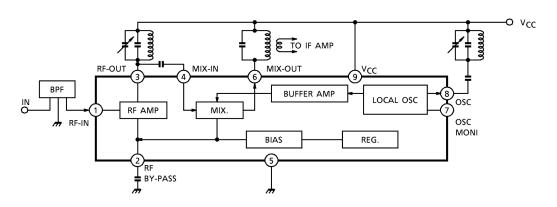
Features

- Wide supply voltage range : $V_{CC} = 1.6 \sim 6.0 V$
- Excellent supply voltage dependence of local oscillator : Oscillation stop
 - $V_{CC} = 0.9 V (typ.)$
- Improved inter-modulation characteristics by double balanced type mixer circuit.
- Low spurious radiation.
- Built-in clampping diode for the local oscillator output.

Block Diagram



Weight: 0.92g (typ.)



Explanation Of Terminals (terminal voltage is DC voltage at Ta = 25° C, V_{CC} = 5V, and no signal)

Pin No.	Symbol	Internal	Terminal Voltage (V)	
1	FM-RF IN	3	0.8	
2	BY PASS		1.5	
3	FM-RF OUT		5.0	
4	MIX IN	GND (S)	1.5	
5	GND	—	0	
6	MIX OUT	cf. pin(4)	5.0	
7	OSC MONITOR	V _{CC} (9)	4.3	
8	OSC		5.0	
9	V _{CC}	_	5.0	

Absolute Maximum Ratings (Ta = 25°C)

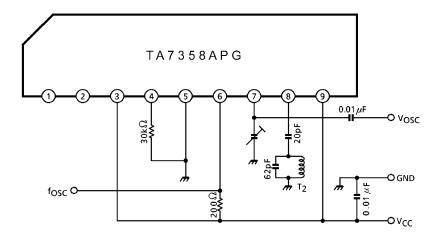
Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	8	V
Power dissipation	P _D (Note)	500	mW
Operating temperature	T _{opr}	-25~75	°C
Storage temperature	T _{stg}	-55~150	°C

(Note) Derated above 25°C in the proportion of 4mW / °C.

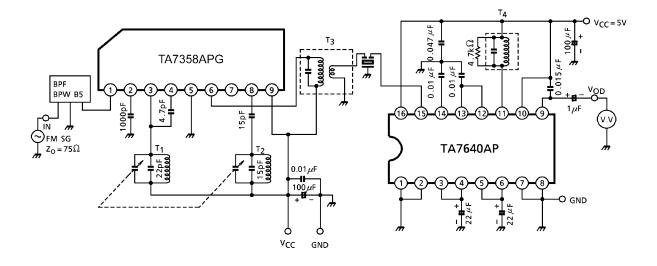
Electrical Characteristics (V_{CC} = 3V, f = 83MHz, f_m = 1kHz, Δf = ±22.5kHz, Ta = 25°C)

Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Supply current		Icc	2	V _{in} = 0	_	5.2	8.0	mA
-3dB limiting sensitivity		V _{in(lim)}	2	_	_	3.0	7.0	dBµV EMF
Quiescent sensitivity		QS	2	_	_	11.0	_	dBµV EMF
Conversion gain		G _C	_	_	_	31	—	dB
Local OSC vo	Itage	V _{OSC}	1	f _{OSC} = 60MHz	90	165	220	mV _{rms}
Pin (1) impedance	Parallel input resistance	r _{ip} 1	3		_	57	_	Ω
Pin (3) impedance	Parallel output resistance	r _{op} 3		f = 83MHz	_	25	_	kΩ
	Parallel output capacitance	c _{op} 3	- 3		_	2.0	_	pF
Pin (4) impedance	Parallel input resistance	r _{ip} 4			_	2.7	_	kΩ
	Parallel input capacitance	c _{ip} 4	- 3		_	3.3	_	pF
Pin (6) impedance	Parallel output resistance	r _{op} 6		6 - 40 7001-	_	100	_	kΩ
	Parallel output capacitance	c _{op} 6	- 3	f = 10.7MHz	_	4.8	_	pF
Local OSC stop voltage		V _{stop}	1	-	—	0.9	1.3	V

Test Circuit 1



Test Circuit 2



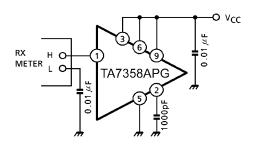
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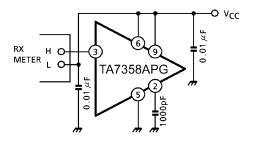
Test Circuit 3

Input output impedance

(1) r_{ip1}, c_{ip1}

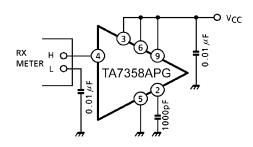
(2) r_{op3}, c_{op3}

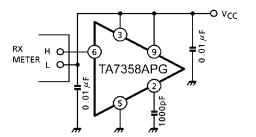




(3) r_{ip4}, c_{ip4}

(4) r_{op6}, c_{op6}





Test Circuit Coil Data (Japan band for 76.0MHz to 108.0MHz)

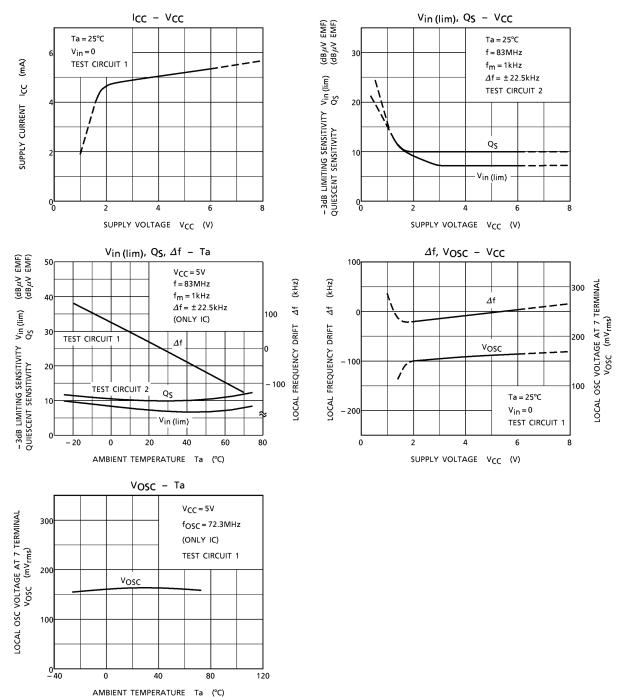
Coil	f _o	Qo	Turns	Capacitance	
T ₁ RF coil	100MHz	100	0.5mm ϕ 2 $\frac{1}{4}$ T Center tap (Japan band)	15pF (external)	
T ₂ OSC coil	100MHz	100	0.5mm ϕ 2 $\frac{1}{2}$ T (Japan band)	15pF (external)	
T ₃ IFT coil	10.7MHz	115	(1)–(3) 12T (4)–(6) 1T Wire 0.12mm∳ UEW SUMIDA ELECTRIC Co., LTD 5764 or equivalent	75pF	V _{CC} Pin ® (BOTTOM VIEW)
T ₄ Quad coil	10.7MHz	150	(4)–(6) 14T Wire 0.12mmø UEW SUMIDA ELECTRIC Co., LTD 44M–933A or equivalent	47pF	(BOTTOM VIEW)

Band pass filter (BPF) SOSHIN ELECTRIC Co., LTD. BPWB5

Tuning cpacitor

ALPS ELECTRIC Co., LTD. CB41EL933

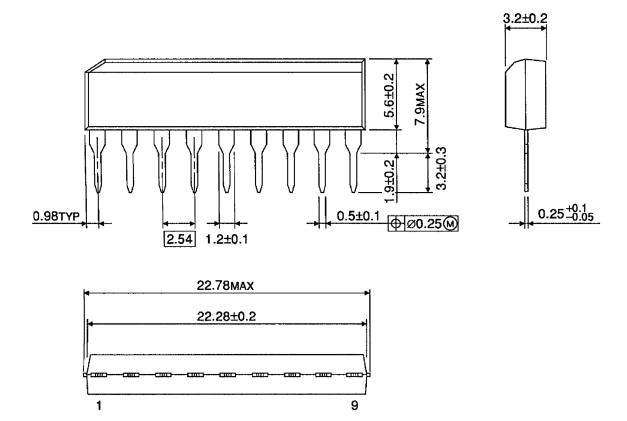
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Package Dimensions

SIP9-P-2.54A

Unit : mm



Weight: 0.92g (typ.)

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RESTRICTIONS ON PRODUCT USE

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• The information contained herein is subject to change without notice. 021023_D

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About solderability, following conditions were confirmed

document shall be made at the customer's own risk. 021023_B

- Solderability
 - (1) Use of Sn-37Pb solder Bath
 - solder bath temperature = 230°C
 - · dipping time = 5 seconds
 - \cdot the number of times = once
 - use of R-type flux
 - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - solder bath temperature = 245°C
 - · dipping time = 5 seconds
 - $\cdot \,$ the number of times = once
 - · use of R-type flux