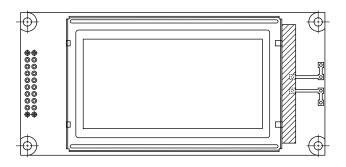


Vishay

RoHS

COMPLIANT

128 x 64 Graphic LCD



MECHANICAL DATA					
ITEM	STANDARD VALUE	UNIT			
Module Dimension	113.0 x 53.0				
Viewing Area	72.0 x 40.0				
Dot Size	0.48 x 0.48				
Dot Pitch	0.52 x 0.52	mm			
Mounting Hole	88.0 x 65.0				
Character Size	N/a				

FEATURES

- Type: Graphic
- Display format: 128 x 64 dots
- Built-in controller: Samsung KS 0107/KS 0108 (or equivalent)
- Duty cycle: 1/64
- + 5 V power supply
- N.V. built-in
- Compliant to RoHS directive 2002/95/EC

ABSOLUTE MAXIMUM RATINGS						
ITEM	SYMBOL	STAN				
	STINDUL	MIN.	TYP.	MAX.	UNIT	
Power Supply	V_{DD} to V_{SS}	4.75	5.0	5.25	V	
Input Voltage	VI	- 0.3	-	V_{DD}	V	

Note

• $V_{SS} = 0 V, V_{DD} = 5.0 V$

ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL	CONDITION	STA	STANDARD VALUE			
			MIN.	TYP.	MAX.	UNIT	
Input Voltage	V _{DD}	L level	0.7 V _{DD}	-	V _{DD}	V	
Input Voltage	V _{IO}	H level	0	-	0.3 V _{DD}	V	
Supply Current	I _{DD}	$V_{DD} = +5 V$	-	2.5	7.5	mA	
		- 20 °C	9.9	10.4	10.9		
Recommended LC Driving	V_{DD} to V_0	0 °C	9.7	10.2	10.7		
Voltage for Normal Temperature		25 °C	8.9	9.4	9.9	V	
Version Module		50 °C	8.6	9.1	9.6		
		70 °C	8.4	8.9	9.4		
LED Forward Voltage	VF	25 °C	-	4.2	4.6	V	
LED Forward Current - Array		l⊧ 25 °C		330	660		
LED Forward Current - Edge	IF	23 0	-	120	240	mA	
EL Power Supply Current	I _{EL}	V _{EL} = 110 V _{AC} , 400 Hz	-	-	5.0	mA	

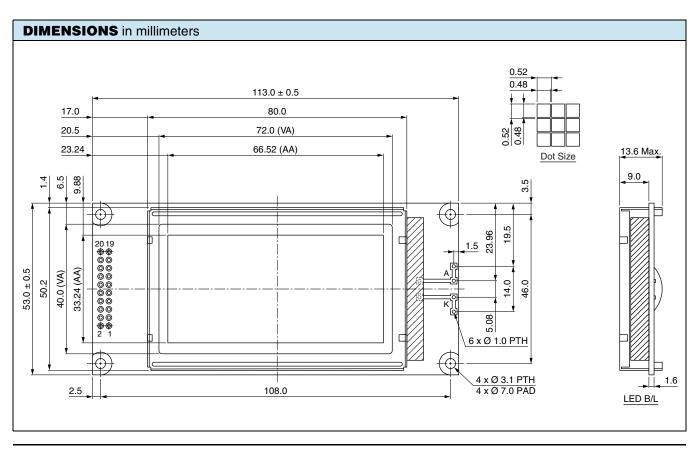
OPTIONS									
PROCESS COLOR					BACKLIGHT				
TN	STN Gray	STN Yellow	STN Blue	FSTN B&W	STN Color	None	LED	EL	CCFL
	x	х	х	x		х	x	x	

For detailed information, please see the "Product Numbering System" document.

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INTERFACE PIN FUNCTION						
PIN NO.	SYMBOL	FUNCTION				
1	V _{SS}	Ground				
2	V _{DD}	Power supply for logic				
3	V ₀	Operating voltage LCD driving				
4	D/I	Date/instruction				
5	R/W	H/L read/write signal				
6	E	$H \rightarrow L$ enable signal				
7	DB0	Data bus line				
8	DB1	Data bus line				
9	DB2	Data bus line				
10	DB3	Data bus line				
11	DB4	Data bus line				
12	DB5	Data bus line				
13	DB6	Data bus line				
14	DB7	Data bus line				
15	CS1	$H \rightarrow Chip 1 enable$				
16	CS2	$H \rightarrow Chip 2 enable$				
17	RES	Reset				
18	V _{OUT}	Negative voltage output				
19	A	Power supply for B/L				
20	к	Power supply for B/L				



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